

ZXMN2088DE6

20V Dual SOT23-6 N-channel enhancement mode MOSFET with low gate drive capability

Summary

V _{(BR)DSS}	$R_{DS(on)}$ (Ω)	I _D (A)
	0.200 @ V _{GS} = 4.5V	2.1
20	0.240 @ V _{GS} = 2.5V	1.9
	0.310 @ V _{GS} = 1.8V	1.7



Description

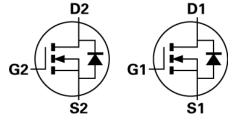
This new generation dual n-channel trench MOSFET from Zetex features low on-resistance achievable with low gate drive.

Features

- · Low on-resistance
- · Low gate drive capability
- SOT23-6 (dual) package

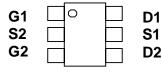
Applications

- Power Management functions
- · Disconnect switches
- · Relay driving and load switching



Ordering information

Device	Reel size	Tape width	Quantity
	(inches)	(mm)	per reel
ZXMN2088DE6TA	7	8	3,000



Pinout - top view

Device marking

2088

Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Drain-Source voltage	V _{DSS}	20	V
Gate-Source voltage	V _{GS}	± 8	V
Continuous Drain current @ V _{GS} = 4.5V; T _A =25°C (b) (d)	I _D	2.1	А
@ V _{GS} = 4.5V; T _A =70°C (b) (d)		1.7	
@ V _{GS} = 4.5V; T _A =25°C ^{(a) (d)}		1.7	
Pulsed Drain current (c)	I _{DM}	8	Α
Power dissipation at T _A =25°C ^{(a) (d)}	P _D	0.9	W
Linear derating factor		7.2	mW/°C
Power dissipation at T _A =25°C ^{(a) (e)}	P _D	1.1	W
Linear derating factor		8.8	mW/°C
Power dissipation at T _A =25°C ^{(b) (d)}	P _D	1.3	W
Linear derating factor		10.4	mW/°C
Operating and storage temperature range	T_j , T_{stg}	-55 to +150	°C

Thermal resistance

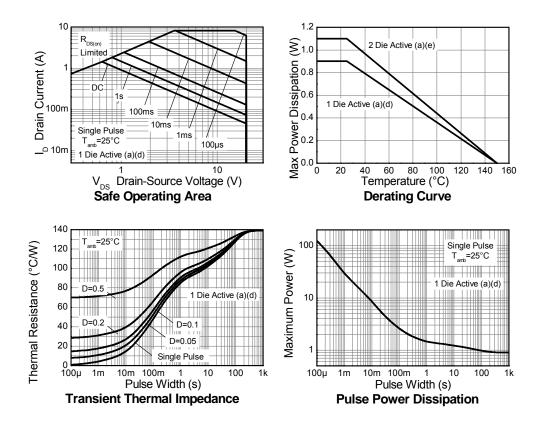
Parameter	Symbol	Value	Unit
Junction to Ambient (a) (d)	$R_{\theta JA}$	139	°C/W
Junction to Ambient (a) (e)	$R_{\theta JA}$	113	°C/W
Junction to Ambient (b) (d)	$R_{\theta JA}$	96	°C/W

NOTES:

- (a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (b) As above measured at $t \le 5$ sec.
- (c) Repetitive rating 25mm x 25mm FR4 PCB, D=0.02, pulse width 300us pulse width limited by maximum junction temperature.
- (d) For device with one active die
- (e) For device with two active die running at equal power.

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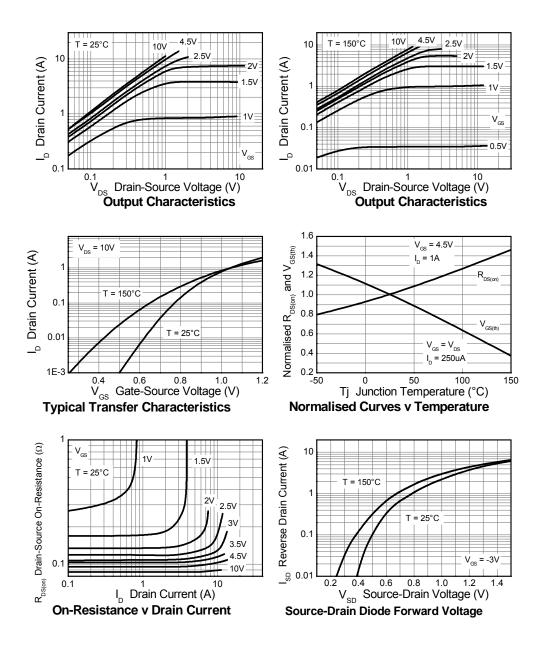
Thermal Characteristics



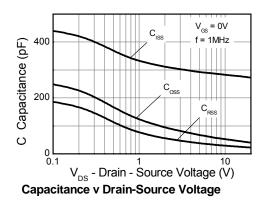
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Static						
Drain-Source breakdown voltage	V _{(BR)DSS}	20			V	I _D = 250μA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}			100	nA	V _{DS} = 3V, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}			1	μΑ	V _{DS} = 20V, V _{GS} =0V
Gate-Body leakage	I _{GSS}			100	nA	V_{GS} =±8V, V_{DS} =0V
Gate-Source threshold voltage	$V_{GS(th)}$	0.4		1.0	V	I _D = 250μA, V _{DS} =V _{GS}
Static Drain-Source on-state	R _{DS(on)}		112	0.200	Ω	V _{GS} = 4.5V, I _D = 1.0A
resistance (*)			137	0.240	Ω	V _{GS} = 2.5V, I _D = 0.6A
			165	0.310	Ω	V _{GS} = 1.8V, I _D = 0.3A
Forward transconductance (*)(‡)	g _{fs}		4.6		S	V _{DS} = 10V, I _D = 1.0A
Dynamic (‡)	•	1				
Input capacitance	C _{iss}		279		pF	V _{DS} = 10V, V _{GS} =0V
Output capacitance	C _{oss}		52		pF	f=1MHz
Reverse transfer capacitance	C _{rss}		29		pF	
Switching (†) (‡)						
Turn-on-delay time	t _{d(on)}		2		ns	V _{DD} = 10V,V _{GS} =4.5V
Rise time	t _r		3.2		ns	I _D = 1A
Turn-off delay time	$t_{d(off)}$		12.7		ns	$R_G \approx 6.0\Omega$
Fall time	t _f		6.2		ns	
Gate Charge						
Total Gate charge	Qg		3.8		nC	V _{DS} = 10V,
Gate-Source charge	Q_{gs}		0.41		nC	V _{GS} = 4.5V
Gate Drain charge	Q_{gd}		0.56		nC	I _D = 2.4A
Source-drain diode						
Diode forward voltage (‡)	V _{SD}		0.75	0.95	٧	T _j =25°C, I _S = 1.0A, V _{GS} =0V
Reverse recovery time	t _{rr}		6.6		ns	T _j = 25°C,
Reverse recovery charge	Q _{rr}		1.6		nC	I _F = 1.24A
						di/dt = 100A/µs

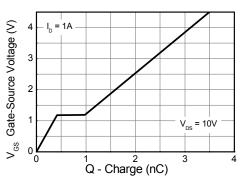
NOTES: $^{(\star)}$ Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$. $^{(\dagger)}$ Switching characteristics are independent of operating junction temperature. $^{(\ddagger)}$ For design aid only, not subject to production testing.

Typical Characteristics



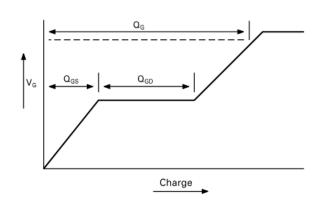
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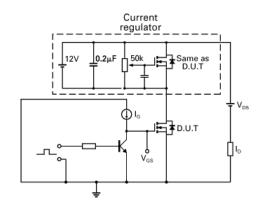




Gate-Source Voltage v Gate Charge

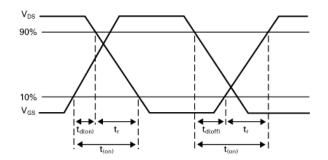
Test Circuits

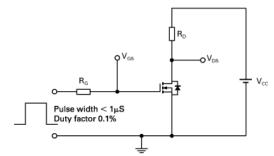




Basic gate charge waveform

Gate charge test circuit



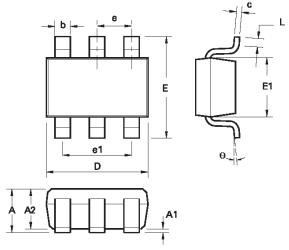


Switching time waveforms

Switching time test circuit

Packaging details - SOT236

Package outline



DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.
Α	0.90	1.45	0.354	0.0570
A1	0.00	0.15	0.00	0.0059
A2	0.90	1.30	0.0354	0.0511
b	0.35	0.50	0.0078	0.0196
С	0.09	0.26	0.0035	0.0102
D	2.70	3.10	0.1062	0.1220
E	2.20	3.20	0.0866	0.1181
E1	1.30	1.80	0.0511	0.0708
L	0.10	0.60	0.0039	0.0236
е	0.95	0.95 REF		4 REF
e1	1.90	1.90 REF		8 REF
L	0°	30°	0°	30°

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

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