

Vishay Siliconix

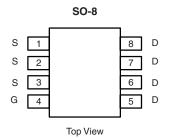
P-Channel 20-V (D-S) MOSFET

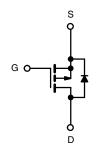
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
- 20	0.040 at V _{GS} = - 4.5 V	- 6.2			
	0.060 at V _{GS} = - 2.7 V	- 5.0			

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- Compliant to RoHS Directive 2002/95/EC







P-Channel MOSFET

Ordering Information: Si9433BDY-T1-E3 (Lead (Pb)-free) Si9433BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 \ ^{\circ}C$, unles	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 12			
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	– I _D	- 6.2	- 4.5		
	T _A = 70 °C		- 5.0	- 3.5		
Pulsed Drain Current		I _{DM}	- 20		А	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 2.3	- 1.2		
	T _A = 25 °C	– P _D	2.5	1.3	w	
Maximum Power Dissipation ^a	T _A = 70 °C		1.6	0.8		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum handling to Angleingti	t ≤ 10 s	- R _{thJA} R _{thJF}	45	50	
Maximum Junction-to-Ambient ^a	Steady State		80	95	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		20	24	

Notes:

a. Surface Mounted on FR4 board, t \leq 10 s.

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SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted									
Parameter	Symbol	Test Conditions Mi		Typ. ^a	Max.	Unit			
Static									
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.6		- 1.5	V			
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA			
Zero Gate Voltage Drain Current		$V_{DS} = -20 V, V_{GS} = 0 V$			- 1	μΑ			
	IDSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 70 °C			- 10				
On-State Drain Current ^b	1	$V_{DS} \le$ - 5 V, V_{GS} = - 4.5 V	- 20			A			
	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 2.7 V	- 5						
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 6.2 A		0.030	0.040	Ω			
		V_{GS} = - 2.7 V, I _D = - 5.0 A		0.050	0.060				
Forward Transconductance ^b	9 _{fs}	$V_{DS} = -9 V, I_{D} = -6.2 A$		15		S			
Diode Forward Voltage ^b	V _{SD}	I _S = - 2.6 A, V _{GS} = 0 V		- 0.76	- 1.1	V			
Dynamic ^a									
Total Gate Charge	Qg			8.8	14				
Gate-Source Charge	Q _{gs}	V_{DS} = - 6 V, V_{GS} = - 4.5 V, I_D = - 6.2 A		1.8		nC			
Gate-Drain Charge	Q _{gd}			2.4					
Gate Resistance	R _g			8.5		Ω			
Turn-On Delay Time	t _{d(on)}			40	60				
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		55	85				
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		65	100	ns			
Fall Time	t _f			30	45	1			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.3 A, dl/dt = 100 A/μs		35	55				

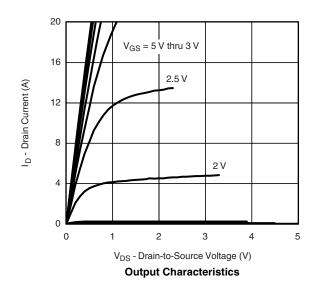
Notes:

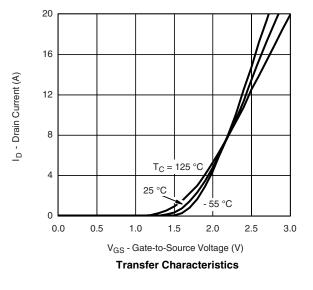
a. For design aid only; not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

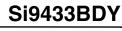
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





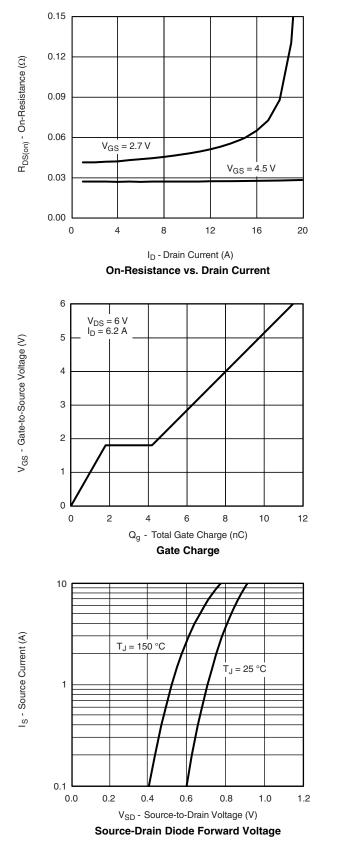
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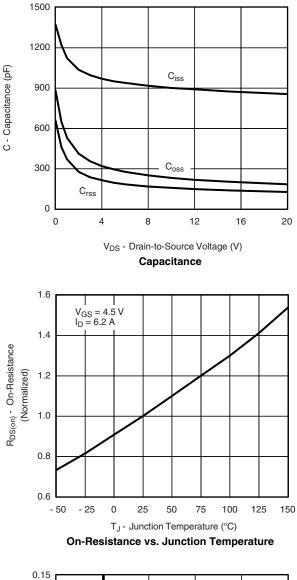


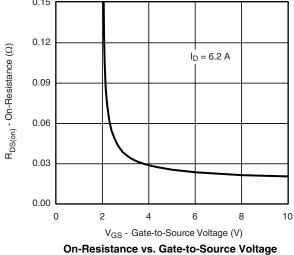
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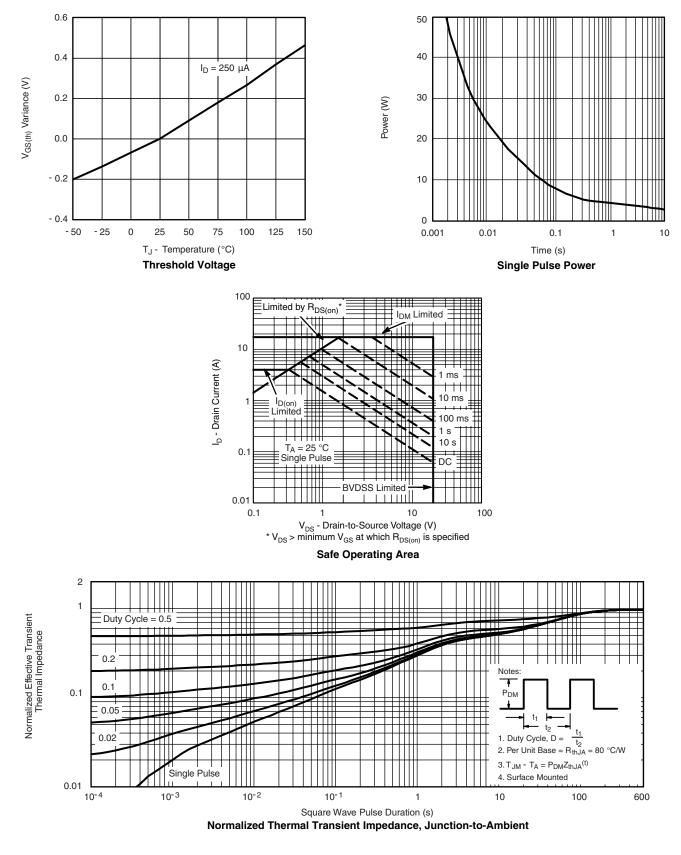


Document Number: 72755 S09-0870-Rev. B, 18-May-09

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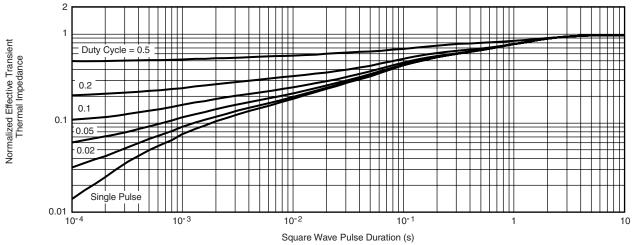
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg272755.



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