BAT54-V, BAT 54A-V, BAT 54C-V, BAT 54S-V



**Vishay Semiconductors** 

BAT54A-V

# Small Signal Schottky Diodes, Single and Dual

RoHS

COMPLIANT

### Features

- These diodes feature very low turn-on voltage and fast switching
- These devices are protected by a PN junction guard ring against excessive voltage, such as electrostatic discharges
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

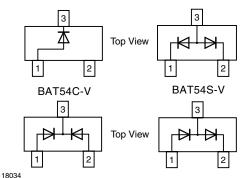
#### **Mechanical Data**

Case: SOT23 plastic case Weight: approx. 8.8 mg Packaging codes/options:

GS18/10k per 13" reel (8 mm tape), 10k/box GS08/3k per 7" reel (8 mm tape), 15k/box



BAT54-V



#### **Parts Table**

Part	Ordering code	Type marking	Remarks	
BAT54-V	BAT54-V-GS18 or BAT54-V-GS08	L4	Tape and reel	
BAT54A-V	BAT54A-V-GS18 or BAT54A-V-GS08	L42	Tape and reel	
BAT54C-V	BAT54C-V-GS18 or BAT54C-V-GS08	L43	Tape and reel	
BAT54S-V	BAT54S-V-GS18 or BAT54S-V-GS08	L44	Tape and reel	

### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Repetitive peak reverse voltage		V <sub>RRM</sub>	30	V	
Forward continuous current		۱ <sub>F</sub>	200 <sup>1)</sup>	mA	
Repetitive peak forward current		I <sub>FRM</sub>	300 <sup>1)</sup>	mA	
Surge forward current current	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	600 <sup>1)</sup>	mA	
Power dissipation		P <sub>tot</sub>	230	mW	

Note

<sup>1)</sup> Device on fiberglass substrate, see layout on next page.



## **Vishay Semiconductors**

### **Thermal Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Thermal resistance junction to ambiant air		R <sub>thJA</sub>	430 <sup>1)</sup>	K/W	
Junction temperature		Tj	125	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C	

Note

<sup>1)</sup> Device on fiberglass substrate, see layout on next page.

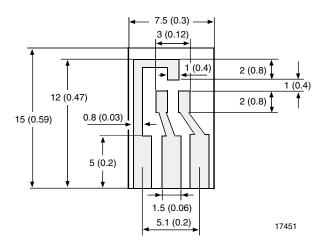
## **Electrical Characteristics**

 $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified

Parameter	Test condition	Symbol	Min.	Тур.	Max.	Unit
Reverse Breakdown voltage	$I_R = 100 \ \mu A \ (pulsed)$	V <sub>(BR)</sub>	30			V
Leakage current	Pulse test t <sub>p</sub> < 300 $\mu$ s, $\delta$ < 2 % at V <sub>R</sub> = 25 V	I <sub>R</sub>			2	μΑ
	$I_{F}$ = 0.1 mA, $t_{p}$ < 300 $\mu s,$ $\delta$ < 2 %	V <sub>F</sub>			240	mV
	$I_{F}$ = 1 mA, $t_{p}$ < 300 $\mu s,$ $\delta$ < 2 %	V <sub>F</sub>			320	mV
Forward voltage	$I_F$ = 10 mA, $t_p$ < 300 $\mu s,  \delta$ < 2 %	V <sub>F</sub>			400	mV
	$I_{\text{F}}$ = 30 mA, $t_{\text{p}}$ < 300 $\mu\text{s},\delta$ < 2 %	V <sub>F</sub>			500	mV
	$I_{F}$ = 100 mA, $t_{p}$ < 300 $\mu s,$ $\delta$ < 2 %	V <sub>F</sub>			800	mV
Diode capacitance	V <sub>R</sub> = 1 V, f = 1 MHz	CD			10	pF
Reverse recovery time	$I_F = 10 \text{ mA to } I_R = 10 \text{ mA},$ $i_R = 1 \text{ mA}, R_L = 100 \Omega$	t <sub>rr</sub>			5	ns

# Layout for R<sub>thJA</sub> test

Thickness: Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)



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# **Vishay Semiconductors**

### **Typical Characteristics**

 $T_{amb} = 25 \text{ °C}$ , unless otherwise specified

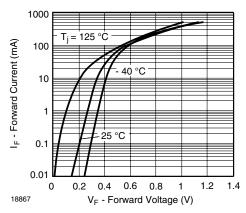


Figure 1. Typical Forward Voltage Forward Current vs. Various Temperatures

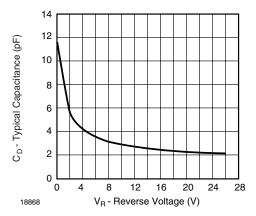
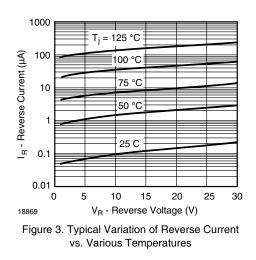


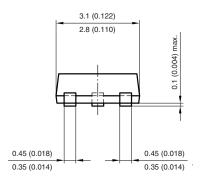
Figure 2. Diode Capacitance vs. Reverse Voltage  $\mathrm{V}_\mathrm{R}$ 

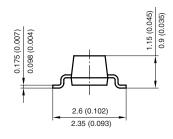




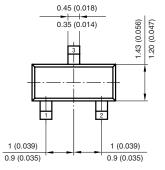
# Vishay Semiconductors

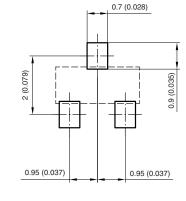
### Package Dimensions in millimeters (inches): SOT23





Foot print recommendation:





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4

BAT54-V, BAT 54A-V, BAT 54C-V, BAT 54S-V



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### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



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