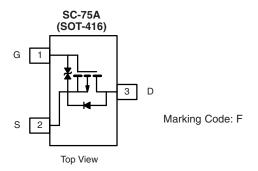




P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY							
V _{DS(min.)} (V)	$R_{DS(on)}\left(\Omega\right)$	V _{GS(th)} (V)	I _D (mA)				
- 60	4.0 at V _{GS} = - 10 V	- 1 to 3.0	- 190				



Ordering Information: Si1021R-T1-E3 (Lead (Pb)-free) Si1021R-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFETs
- High-Side Switching
- Low On-Resistance: 4 Ω
- Low Threshold: 2 V (typ.)
- Fast Switching Speed: 20 ns (typ.)
- Low Input Capacitance: 20 pF (typ.)
- Miniature Package
- ESD Protected: 2000 V

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Power Supply Converter Circuits
- Solid-State Relays

BENEFITS

- · Ease in Driving Switches
- Low Offset Voltage
- Low-Voltage Operation
- · High-Speed Circuits
- · Easily Driven without Buffer
- Small Board Area

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted								
Parameter		Symbol	Limit	Unit				
Drain-Source Voltage		V_{DS}	- 60	V				
Gate-Source Voltage		V_{GS}	± 20					
Continuous Drain Current /T 150 °C\a	T _A = 25 °C	ı	- 190					
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C	- I _D	- 135	mA				
Pulsed Drain Current ^b		I _{DM}	- 650	1				
Dawar Biasia atian?	T _A = 25 °C	P _D	250	mW				
Power Dissipation ^a	T _A = 85 °C	' Б	130					
Maximum Junction-to-Ambient ^a		R _{thJA}	500	°C/W				
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C				

Notes:

- a. Surface mounted on FR4 board.
- b. Pulse width limited by maximum junction temperature.

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SPECIFICATIONS $T_J = 25 ^{\circ}C$, unless otherwise noted								
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit		
Static		<u> </u>		•				
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	- 60			V		
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -0.25 \text{ mA}$	- 1		- 3.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 10	μΑ		
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			± 200			
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			± 500			
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current		V _{DS} = - 50 V, V _{GS} = 0 V			- 25			
	I _{DSS}	V _{DS} = - 50 V, V _{GS} = 0 V, T _J = 85 °C			- 250			
0	I _{D(on)}	V _{DS} = -10 V, V _{GS} = - 4.5 V	- 50			m 1		
On-State Drain Current ^a		V _{DS} = -10 V, V _{GS} = - 10 V	- 600			mA		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -25 \text{ mA}$			8	Ω		
		V _{GS} = - 10 V, I _D = - 500 mA			4			
		V _{GS} = - 10 V, I _D = - 500 mA, T _J = 125 °C			6			
Forward Transconductance	9 _{fs}	V _{DS} = - 10 V, I _D = - 100 mA	80			mS		
Diode Forward Voltage ^a	V _{SD}	V _{DS} = - 200 mA, V _{GS} = 0 V	80			V		
Dynamic								
Total Gate Charge	Qg			1.7		nC		
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -15 \text{ V}, I_{D} \cong -500 \text{ mA}$		0.26				
Gate-Drain Charge	Q _{gd}			0.46				
Input Capacitance	C _{iss}			23		pF		
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		10				
Reverse Transfer Capacitance	C _{rss}	1		5				
Switching ^b	-							
Turn-On Time	t _{ON}	$V_{DD} = -25 \text{ V}, R_L = 150 \Omega,$		20		ns		
Turn-Off Time	t _{OFF}	$I_D \cong$ - 200 mA, V_{GEN} = - 10 V, R_G = 10 Ω		35				

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

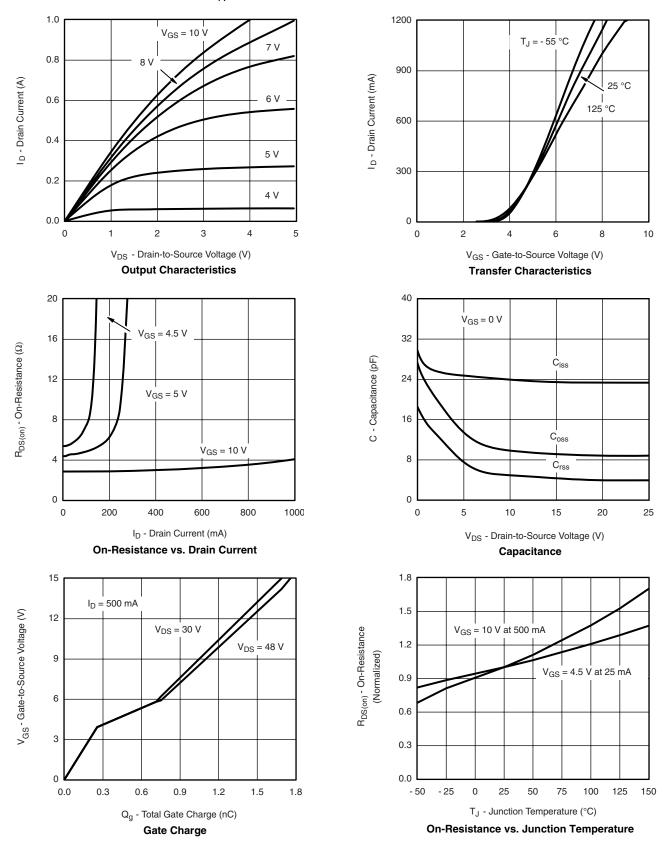
b. Switching time is essentially independent of operating temperature.







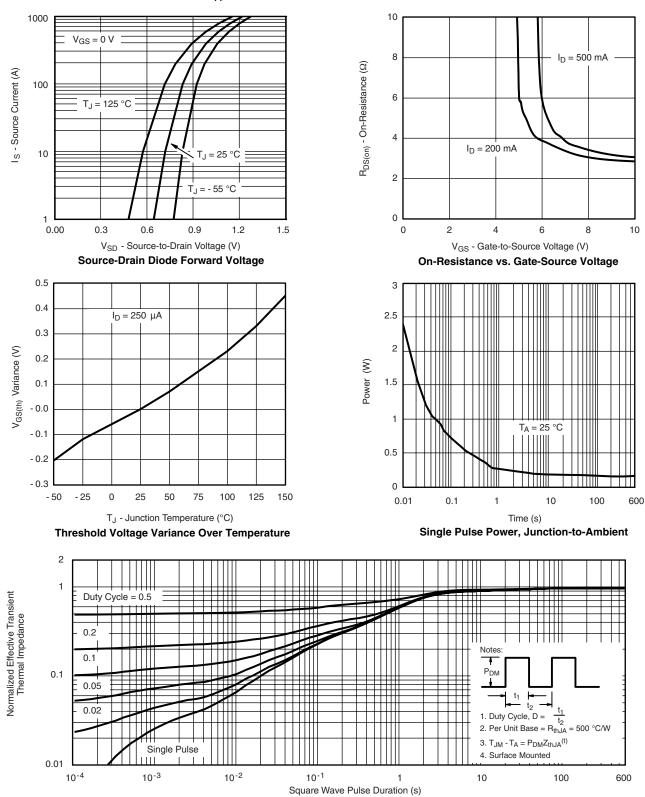
TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



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TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71410.



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