Programmable Logic Controllers



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For more information on this product family, visit our website.

Additional resources include:

- New and updated product information
- Downloadable software demos & upgrades
- Part configuration tool & cross reference
- Online stock check & ordering
- IDEC field sales & distributor search
- Online literature request

- Downloadable manuals & CAD drawings
- Manufacturer's suggested retail price list
- Product training schedule & locations
- Advertising & trade show schedules
- Press releases & FAQs



Selection Guide

Programmable Logic Controllers

		MicroSm	nart Family	0 N (0 (H (0N0)	0 (0.1
		MicroSmart Pentra	MicroSmart	OpenNet Controller (ONC)	SmartRelay
Page		3	8	45	56
Appearance		NEW	The state of the s		Does Server Children Tarrect Value Constitution To a book to the c
Rated Voltage)	24V DC, 100-240V AC	24V DC, 100-240V AC	24V DC	12-24V DC, 24V AC/DC, 100-240V AC/DC
Max. Digital I	/0	512	264	480	50
Max. Analog	I/O	56	56	42	10
Program Capa		62.4K bytes	31.2K bytes	32K bytes	2K bytes
Max. Communication Ports		7	2	3	1
Networking	Modbus RTU/ASCII	Yes	-	-	-
	Modbus TCP	Yes			
	AS-Interface	Yes	Yes	-	Yes
	LonWorks	-	-	-	Yes
32-bit Data		Yes	-	Yes	•
Floating Point		Yes	-	-	-
High-Speed I/O Freq. Approvals		100KHz	c Upus C E Lloyd's Register	c UL us C E	CUL US C E Lloyd's Register APPROVED

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Micro-controllers play an increasingly central role in today's industrial applications. You have many controllers to choose from, but the one you turn to most often is the one that fits best, physically and practically. You'll find IDEC PLCs in various applications from water treatment plants to HVAC to printing press operations and more. They're always dependable, easy to program and almost as smart as you are.

IDEC brought some of the first micro-PLCs to the market, and has been meeting your changing control automation needs for decades. Now with the MicroSmart Pentra, you get the fastest and most full featured programmable logic controller there is.









International Approvals

All MicroSmart controllers have regulatory agency certifications for the worldwide market including being cULus Listed for Class1 Division 2 Hazardous Locations, TUV approved, CE, and certified for marine use by ABS and Lloyd's Registry.

Rugged, Compact, Modular Design

Every CPU module comes equipped with embedded I/O points, and you can conveniently add snap-on expansion modules for up to 512 I/Os based on your system requirements. All MicroSmart controllers are DIN-rail or panel mountable.

Write & Run Your Programs Now

Relax. Programming the MicroSmart is fast and straightforward. Use IDEC's WindLDR software to configure, modify and monitor your MicroSmart programs with ease. This powerful and intuitive software makes it simple to get your system up and running.

Upgrade Without Downtime

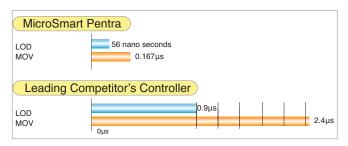
For added convenience, the same expansion I/O modules and accessories can be used on both the MicroSmart and MicroSmart Pentra controllers. In fact, both controllers share the same architecture, instruction set and programming software. The use of a single software platform for all IDEC PLCs means you won't have to reprogram or learn a new system to move from one to another.





The Fastest Micro Controller in its Class!

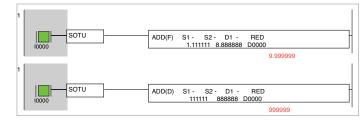
MicroSmart Pentra is the fastest micro controller available in its class. The overall processing speed of the new Logic Engine CPU is 16 times faster than our competitor's average controller.



Supports 32-bit data and floating point math

MicroSmart Pentra Performance

MicroSmart Pentra supports double-word, floating point math operations, capturing and storing large values, and returning computed results accurate to seven decimal places.



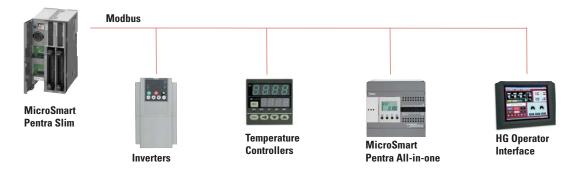
Field Upgradeable Firmware

Extend the life of your PLC! Upgrade your firmware on-site as new functions and versions become available.



Built-in Modbus RTU/ASCII master & slave, and Modbus TCP (1:1) de-facto protocol

Modbus messaging protocol is a de-facto protocol in industrial networking. Communication with other devices on a Modbus network can be easily achieved with built-in Macros instructions.



MicroSmart Pentra Performance

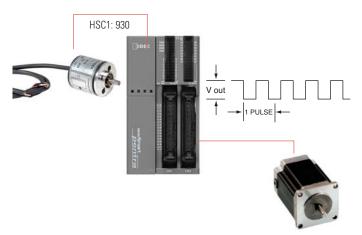
Maximum 7 Communication Ports

MicroSmart Pentra models can accomodate up to a total of seven communication ports. Now you can connect your HMI, PC, barcode reader, RFID equipment, printer and more.



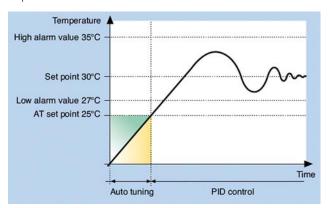
Integrated 100KHz Fast Inputs and Outputs

Configure up to four high-speed inputs from high-speed output devices such as rotary encoders or proximity switches at a maximum frequency of 100KHz, independent of the scan time. Up to three high-speed outputs can be used for simple positioning controls for stepper or servo motors.



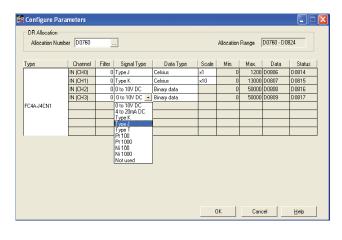
56 PID Loops

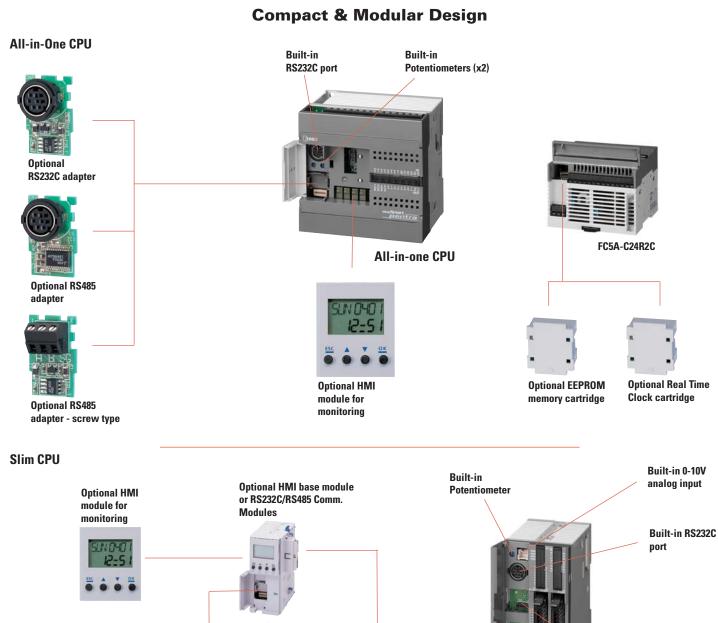
PID is the standard solution to many industrial process controls because of its accuracy and stability. With up to 56 PID loops and advanced auto-tuning features, your systems can be tuned to optimum values for the desired control response.

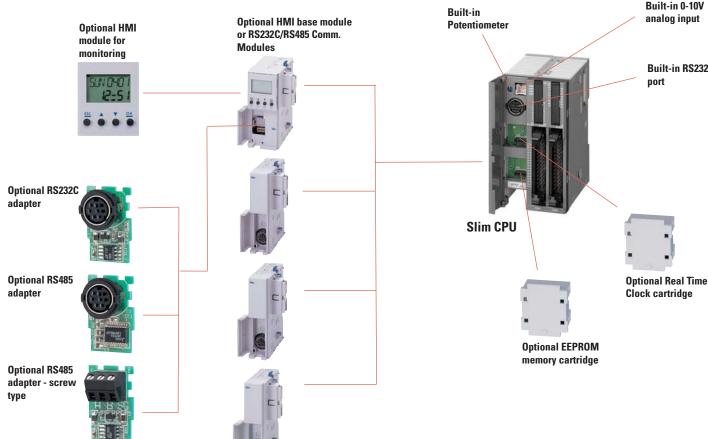


Maximum 56 Analog I/O

Your options include 0-10V, 4-20mA, RTD, thermocouple, thermistor inputs and +/-10V output. With built-in Macro instructions, configuring analog parameters is just a step away.







MicroSmart Pentra CPU Part Numbers

All-in-One

Appearance	Part Number	Power	I/O Points	Input	Output	Expandability
13332	FC5A-C10R2C	24V DC	- 10 (6 in/4 out)			
	FC5A-C10R2	100-240V AC	10 (0 111/4 001)			N/A
	FC5A-C16R2C	24V DC				N/A
	FC5A-C16R2	100-240V AC	16 (9 in/7 out)	24V DC (Sink/Source)	Relay	
	FC5A-C24R2C	24V DC	- 24/14 :- /10 - · · · · ·			88 Maximum I/O (up to
	FC5A-C24R2	100-240V AC	24 (14 in/10 out)			4 expansion modules)

Slim

Appearance	Part Number	Power	I/O Points	Input	Output	Expandability	
	FC5A-D16RK1	24V DC	16 (8 in/8 out)	24V DC (Sink/Source)	6 Relays, 2 Transistor Sink	496 Maximum I/O (up to 15 expansion modules)	
	FC5A-D16RS1		16 (6 III/8 OUL)		6 Relays, 2 Transistor Source		
I station	FC5A-D32K3*		32 (16 in/16 out)	24V DC (SIIIK/SOUICE)	Transistor Sink	512 Maximum I/O (up to 15 expansion	
	FC5A-D32S3*		32 (10 m) 10 out)		Transistor Source	modules)	



*See page 20 for MIL Connector Cables and Breakout Modules.

Automation Software



IDEC

- Available in 10, 16, 20, 24, and 40 I/O CPUs.
- PID Controls
- -Program up to 14 PID loops
- High Speed I/O
 - -Built-in 4 high speed inputs
 - -Single or Dual Phase
 - -Max. 20KHz frequency
- Built-in 2 High speed outputs (Slim model only)
- Configure up to 264 I/O Points
- Data link up to 32 MicroSmart and Pentra CPUs
- Using RS485 communication module/port, you can create a network of up to 32 CPUs.
- Worldwide Approvals
 - -cULus listed, CE marked
 - -Class 1 Div. 2 for hazardous locations
 - -Lloyds Registered and ABS approved for shipping industry











MicroSmart CPU Part Numbers

MicroSmart Performance

All-in-One

Appearance	Part Number	Power	I/O Points	Input	Output	Expandability
	FC4A-C10R2C	24V DC	- 10 (6 in/ 4 out)			
illing in the second of the se	FC4A-C10R2	100-240V AC	10 (0 11) 4 001)	24V DC (Sink/Source)	Relay	N/A
The state of the s	FC4A-C16R2C	24V DC	- 16 (9 in/ 7 out)			1971
	FC4A-C16R2	100-240V AC	10 (8 III) 7 Out)			
	FC4A-C24R2C	24V DC	- 24 (14 in/ 10 out)			88 Maximum I/O (up to 4 expansion
illigrammy	FC4A-C24R2	100-240V AC	21,, 10 out)			modules)

MicroSmart CPU Part Numbers

Slim

Appearance	Part Number	Power	I/O Points	Input	Output	Expandability	
	FC4A-D20RK1			24V DC (Sink/Source)	6 Relays, 2 Transistor Sink	244 Maximum I/0 (up to 7	
TANASCHA (M. 1974)	FC4A-D20RS1		20 (12 in/8 out)		6 Relays, 2 Transistor Source	expansion modules)	
	FC4A-D20K3	24V DC			Transistor Sink	148 Maximum I/O	
The state of the s	FC4A-D20S3				Transistor Source	(up to 7 expansion modules)	
	FC4A-D40K3				Transistor Sink	264 Maximum I/O (up to 7 expansion modules)	
	FC4A-D40S3		40 (24 in/16 out)		Transistor Source		

...

MicroSmart Starter Kits

		Part Numbers	Controller	Power Supply	Software (Prog. Cables Included)
		MM-SMART-10	10 I/O All-in-One CPU	-	WindLDR
nart		MM-SMART-16	16 I/O All-in-One CPU	-	WindLDR
MicroSmart		MM-SMART-20	20 I/O Slim CPU	15W	WindLDR
Μic	Mic	MM-SMART-24	24 I/O AII-in-One CPU	-	WindLDR
		MM-SMART-40	40 I/O Slim CPU	15W	WindLDR
MicroSmart Pentra	- Inches	MM-PENTRA-16	16 I/Os Slim CPU	30W	WindLDR
Micro		MM-PENTRA-24	24 I/Os All-in-One CPU	-	WindLDR

Starter Kits and Solution Packages

MicroSmart Solution Packages

	_	Part Numbers	Operator Interface*	Controller	Power Supply
		MM-SMART-16-HG2F-M	HG2F 5.7" Mono STN	16 I/O AII-in-One CPU	15W
	TANK MONTON	MM-SMART-20-HG2F-M	HG2F 5.7" Mono STN	20 I/O Slim CPU	60W
		MM-SMART-24-HG2F-M	HG2F 5.7" Mono STN	24 I/O AII-in-One CPU	15W
		MM-SMART-40-HG2F-M	HG2F 5.7" Mono STN	40 I/O Slim CPU	60W
		MM-SMART-16-HG2F-C	HG2F 5.7" Color STN	16 I/O AII-in-One CPU	15W
E	TANK MICROTON TOWNS OF THE STATE OF THE STA	MM-SMART-20-HG2F-C	HG2F 5.7" Color STN	20 I/O Slim CPU	60W
MicroSmart	TOTAL STREET STREET	MM-SMART-24-HG2F-C	HG2F 5.7" Color STN	24 I/O AII-in-One CPU	15W
Aicro		MM-SMART-40-HG2F-C	HG2F 5.7" Color STN	40 I/O Slim CPU	60W
~		MM-SMART-20-HG3F	HG3F 10.4" Color TFT	20 I/O Slim CPU	60W
	□-□-□- [C != =	MM-SMART-24-HG3F	HG3F 10.4" Color TFT	24 I/O All-in-One CPU	60W
	0000	MM-SMART-20-HG4F	HG4F 12.1" Color TFT	20 I/O Slim CPU	60W
		MM-SMART-24-HG4F	HG4F 12.1" Color TFT	24 I/O All-in-One CPU	60W
	PROCESS AND ASSESSED.	MM-PENTRA-16-HG1F	HG1F 4.6" Mono STN	16 I/O Slim CPU	30W
		MM-PENTRA-24-HG1F	HG1F 4.6" Mono STN	24 I/O All-in-One CPU	30W
	TATION NAMED TO STATE OF THE PARTY OF THE PA	MM-PENTRA-16-HG2F-C	HG2F 5.7" Color STN	16 I/O Slim CPU	30W
entra		MM-PENTRA-24-HG2F-C	HG2F 5.7" Color STN	24 I/O All-in-One CPU	30W
MicroSmart Pentra		MM-PENTRA-16-HG3F	HG3F 10.4" Color TFT	16 I/O Slim CPU	30W
Micı		MM-PENTRA-24-HG3F	HG3F 10.4" Color TFT	24 I/O AII-in-One CPU	30W
		MM-PENTRA-16-HG4F	HG4F 12.1" Color TFT	16 I/O Slim CPU	30W
	008000	MM-PENTRA-24-HG4F	HG4F 12.1" Color TFT	24 I/O All-in-One CPU	30W
1.	*HG1F: Light Gray Bezel, RS232 Comm., HG2F,	/3F/4F: Light Gray Bezel.			



^{1. *}HG1F: Light Gray Bezel, RS232 Comm., HG2F/3F/4F: Light Gray Bezel

 $^{2. \}quad \text{All packages come with WindLDR \& WindO/I-NV2 software, programming and interface cables}.$

Solution Packages

Whether you want to give IDEC products a try or already use them but want a complete automation system that is quick and easy to put together, you'll find a great deal in our solution packages. Each package includes an IDEC operator interface:

- HG1F 4.6" STN monochrome LCD touchscreen
- HG2G 5.7" STN 256 color or monochrome LCD touchscreen
- HG3F 10.4" TFT 256 color LCD touchscreen
- HG4F 12.1" TFT 256 color LCD touchscreen

They also include a MicroSmart or MicroSmart Pentra PLC (Slim or All-in-One design), a slim power supply, cables and software. Buy one package and you're ready to go.



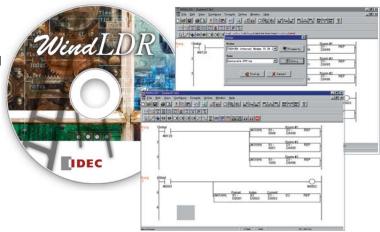
Control at your fingertips

		Part Numbers		Operator Interface**	Controller Type	Power Supply
art		MM-SMART-20-HG3F		HG3F 10.4" Color TFT	20 I/O Slim CPU	60W
MicroSmart		MM-SMART-24-HG3F		HG3F 10.4" Color TFT	24 I/O All-in-One CPU	60W
/licr						
		MM-SMART-20-HG4F		HG4F 12.1" Color TFT	20 I/O Slim CPU	60W
		MM-SMART-24-HG4F		HG4F 12.1" Color TFT	24 I/O All-in-One CPU	60W
		MM-PENTRA-16-HG1F		HG1F 4.6" Mono STN	16 I/O Slim CPU	30W
	10 may (10 m)	MM-PENTRA-24-HG1F		HG1F 4.6" Mono STN	24 I/O All-in-One CPU	30W
		MM-PENTRA-16-HG2G-EM		HG2G 5.7" Mono STN, Ethernet	16 I/O Slim CPU	30W
		MM-PENTRA-24-HG2G-M		HG2G 5.7" Mono STN	24 I/O All-in-One CPU	30W
		MM-PENTRA-24-HG2G-EM		HG2G 5.7" Mono STN, Ethernet	24 I/O All-in-One CPU	30W
ıtra		MM-PENTRA-16-HG2G-EC		HG2G 5.7" Color STN, Ethernet	16 I/O Slim CPU	30W
r Pe	0 T E 27	MM-PENTRA-24-HG2G-C		HG2G 5.7" Color STN	24 I/O All-in-One CPU	30W
MicroSmart Pentra		MM-PENTRA-24-HG2G-EC		HG2G 5.7" Color STN, Ethernet	24 I/O Slim CPU	30W
cro						
Σ		MM-PENTRA-16-HG3F		HG3F 10.4" Color TFT	16 I/O Slim CPU	30W
	discontinue di	MM-PENTRA-24-HG3F		HG3F 10.4" Color TFT	24 I/O All-in-One CPU	30W
	EEEE ::	MM-PENTRA-16-HG4F		HG4F 12.1" Color TFT	16 I/O Slim CPU	30W
		MM-PENTRA-24-HG4F		HG4F 12.1" Color TFT	24 I/O All-in-One CPU	30W

WindLDR

Programming Software

Unique ladder logic programming tool designed to program all IDEC PLCs



Part Number

Part Number	Description
FC9Y-LP2CDW	WindLDR PLC programming software

Single Platform for all IDEC PLCs

WindLDR is an excellent, long-term investment for your control solutions. It programs every IDEC PLC including the OpenNet Controller, MicroSmart and the fastest micro-controller on the market, MicroSmart Pentra. It's adaptable to whatever hardware you need today and down the road.

Simple-to-use Editors

Use the tag editor to access and edit coil data. Edit comments and rung comments. Simulation mode tests your program in WindLDR to guarantee that it works the way you expected, before downloading it to your PLC.

User-friendly Interfaces

Icon-based toolbars and drag-and-drop functionality make basic ladder programming accessible to anyone. But WindLDR also shows you how to display parameters and settings and how to input your parameters, and the built-in shortcuts and tutorials will keep you on the right track.

Free Lifetime Upgrade

Not only is WindLDR the easiest and most convenient ladder programming software on the market, it also comes with a very special price with no strings attached. Our software comes with a free-lifetime upgrade. That means that you no longer need to spend thousands of dollars for software that has to be renewed every year costing you additional money. Save yourself money by using an IDEC PLC and WindLDR programming software.



Specifications

All-in-One

Part Number	AC Power	FC5A-C10R2	FC5A-C16R2	FC5A-C24R2	FC4A-C10R2	FC4A-C16R2	FC4A-C24R2				
rait ivuilibei	DC Power	FC5A-C10R2C	FC5A-C16R2C	FC5A-C24R2C	FC4A-C10R2C	FC4A-C16R2C	FC4A-C24R2C				
Rated Voltage		AC power model: 100 to 240V AC, DC power model: 24V DC									
Allowable Voltage Ran	ge	AC power model: 85 to 264V AC, DC power model: 20.4 to 28.8V DC (including ripple)									
Rated Power Frequency	/		AC power model: 50/60 Hz (47 to 63 Hz)								
Maximum Input Curren	t	250mA (85V AC) 160mA (24V DC)	300mA (85V AC) 190mA (24V DC)	450mA (85V AC) ¹ 360mA (24V DC) ²	250mA (85V AC) 160mA (24V DC)	300mA (85V AC) 190mA (24V DC)	450mA (85V AC) ² 360mA (24V DC) ³				
Maximum Power	AC Power		FC5A-C1	10R2/FC4A-C10R2: 30V/ 6R2/FC4A-C16R2: 31V/ 24R2/FC4A-C24R2: 40V/	(264 V AC) / 22VA (100	OV AC) ³					
Consumption	DC Power		FC5A-C10R2C/FC4A-C10R2C: 3.9W (24V DC) ⁴ FC5A-C16R2C/FC4A-C16R2C: 4.6W (24V DC) ⁴ FC5A-C24R2C/FC4A-C24R2C: 8.7W (24V DC) ²								
Allowable Momentary Power Interruption				10ms (rated p	ower voltage)						
Dielectric Strength				en power and ⊕ or 📤 t een I/O and ⊕ or 套 te							
Insulation Resistance				er and 🕒 or 📤 terminal and 🕀 or 📤 terminals:							
Noise Resistance			1/(AC power terminals: DC power terminals: D terminals (coupling cla	1.0 kV, 50 ns to 1µs	JS					
Inrush Current		3!	5A	40A	3	5A 40A					
Power Supply Wire		UL1015 AWG22, UL1007 AWG18									
Operating Temperature	1	0 to 55°C									
Storage Temperature		−25 to +70°C (no freezing)									
Relative Humidity			Leve	I RH1 (IEC61131-2), 1 to	95% RH (no condensa	tion)					
Altitude				Operation: 0 to 2,000m,	Transport: 0 to 3,000m						
Pollution Degree				2 (IEC606	64-1)						
Corrosion Immunity				Free from cor	rosive gases						
Degree of Protection				IP20 (IEC	C60529)						
Grounding Wire				UL1007, A	WG16						
Vibration Resistance		When mounted on a DIN rail or panel surface: 5 to 9 Hz amplitude 3.5 mm, 9 to 150 Hz acceleration 9.8 m/s 2 (1G), 2 hours per axis on each of three mutually perpendicular axes (IEC61131-2)									
Shock Resistance		147	7 m/s ² (15G), 11ms dura	ation, 3 shocks per axis,	on three mutually perp	endicular axes (IEC611	131)				
Weight		AC: 230g DC: 240g	AC: 250g DC: 260g	AC: 305g DC: 310g	AC: 230g DC: 240g	AC: 250g DC: 260g	AC: 305g DC: 310g				



- CPU module (including 250mA sensor power) + 4 I/O modules
 CPU module + 4 I/O modules
 CPU module (including 250mA sensor power)
 CPU module (24V DC)

Programmable Logic Controllers MicroSmart Series

Slim

Part Number	er				A-D16RK1 A-D16RS1		A-D32K3 A-D32S3		IA-D20K3 IA-D20S3		A-D20RK1 A-D20RS1		A-D40K3 A-D40S3
Control Syste	em							Stored	d program system				
Instruction V	۸/ا -								35 basic				
instruction v	voras			88 a	dvanced	92 a	dvanced	55 a	advanced	72 a	dvanced		
Program Cap	oacity 1			62.4	KB (10,400 steps)			27 H	KB (4,500 steps)	31.2	KB (5,200 steps) 2		
User Progran	n Stor	age			EEPROM (10,000 times rewritable)								
Processing			Basic Instruction	83µs	83µs (1,000 steps) 1.65ms (1,000 steps)								
Time			END Processing ³	0.35	0.35ms								
Expandable I	I/0 Mc	odules			dules + additional g the expansion po			7 m	odules				
I/O Points		nput Dutput		8	Expansion: 224 Additional: 256	16 16	Expansion: 224 Additional: 256	12 8	Expansion: 128	12 8	Expansion: 224	24 16	Expansion: 22
Internal Rela	ау			2,04	B points			1,02	24 points				
Shift Registe	er			256	points			128	points				
Data Registe	er			42,0	00 points ⁴			1,30	00 points				
Expansion Da	ata Re	gister		6,00	O points			_		6,00	0 points		
Counter				256	points			100	points				
Timer (1-sec,	100-ms	s, 10-ms, 1-r	ms)	256	points			100	points				
	Backı	up Data				Intern	al relay, shift registe	er, cou	ınter, data register,	expan	sion data register		
	Backı	up Duration				Α	oprox. 30 days (typic	al) at	25°C after backup I	battery	fully charged		
RAM	Batte	ry			Lithium secondary battery								
Backup	Charg	ging Time					Approx. 15 hours for	or cha	rging from 0% to 9	0% of	full charge		
	Battery Life								5 years				
	Repla	ceability							N/A				
Self-diagnos	stic Fur	nction			Power failure, watchdog timer, data link connection, user program EPPROM sum check, timer/counter preset value sum check, user program RAM sum check, keep data, user program syntax, user program writing, CPU module, clock IC, I/O bus initialize, user program execution								
Input Filter					Without filter or 3 to 15ms filter (selectable in increments of 1ms)								
Catch Input/I	'Interru	ıpt Input		Mini	Four inputs (I2 through I5) Minimum turn on pulse width: 5µs minimum Minimum turn off pulse width: 5µs minimum				Four inputs (I2 through I5) Minimum turn on pulse width: 40µs minimum Minimum turn off pulse width: 150µs minimum				
High-			ing ligh-speed Counter	Total Sing	Total 4 points Single/two-phase selectable: 100 KHz (2 points) Single-phase:100 KHz (2 points)			Total 4 points Single/two-phase selectable: 20 KHz (2 points) Single-phase: 5 KHz (2 points)					
speed Counter		ting Range			4294967295 (32 bi	0)	0 to 65535 (16 bits)						
		ation Mode		5 15	1 . 137 203 (02 b)	-,	Rotary enco		ode and adding cou	ınter m	node		
Analog			Number						1 point				
Potentiomete	er		Data Range						0 to 255				
	1	Number							1 point				
Analog	- 1	nput Voltag	je Range						0 to 10V DC				
Voltage Inpu	ıt j	nput Imped	ance					А	pprox. 100kΩ				
	[Data Range						0 -	to 255 (8 bits)				
Pulse	1	Number		2 poi	ints	3 ро	ints	2 pc	oints				
Output	ľ	Maximum F	requency	100KHz 20KHz									
Sensor Powe Supply	er	·	age Current		_								
D . 4		solation					D00000 /						
Port 1				11	-	RS232C (maintenar					_	311	
Port 2 Comm			er (option) ³	Poss		Poss			sible	Poss		Poss	
Clock Cartrid				Poss		Poss			sible	Poss		Poss	
Memory Cart				Poss		Poss			sible	Poss		Poss	
HMI Module	(uptio	111)		Poss	INIG	Poss	inie	ros	sible	Poss	inie	Poss	inie



- 1. 1 step equals 6 bytes.
- Expandable up to 64 KB when a memory cartridge is used.
- Not including expansion I/O service time, clock function processing time, data link processing time, and interrupt processing time.
- 4. Extra data registers D10000 through D49999 are enabled using WindLDR
- Function Area Settings, then run-time program download cannot be used.
- Maintenance communication, user communication, Modem communication, data link, Modbus master/slave communication (FC5A only).

Note: The maximum number of relay outputs that can be turned on simulatneously is 54 including those on the CPU module.

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Part Number			FC5A-C10R2 FC5A-C10R2C	FC5A-C16R2 FC5A-C16R2C	FC5A-C24R2 FC5A-C24R2C	FC4A-C10R2 FC4A-C10R2C	FC4A-C16R2 FC4A-C16R2C	FC4A-C24R2 FC4A-C24R2C					
Control Sy	stem				Stored prog	ram system							
etruction	Morde				35 b	asic							
Instruction Words			76 advanced	76 advanced	81 advanced	38 advanced	40 advanced	46 advanced					
Program Capacity ¹			13.8 KB (2,300 steps)	27 KB (4,500 steps)	54 KB (9,000 steps)	4.8 KB (800 steps)	15 KB (2,500 steps)	27 KB (4,500 step					
User Program Storage				EEPROM (10,000 times rewritable)									
Processing Basic Instruction				1.16ms (1,000 steps)		1.65ms (1,000 steps)							
Time END Processing ²		0.64ms		1	0.64ms								
Expandable I/O Module		dule	_		4 modules	_		4 modules					
O Points		nput	6	9	14 Expansion:	6	9	14 Ex					
		Output	4	7	10 64	4	7	10 sic					
iternal Re				2,048 points		256 points	1,024 points						
hift Regis	ster			128 points	64 points	128 points							
ata Regis	ster			2,000 points	400 points	1,300 points							
ktra Data	Register			_	_								
ounter				256 points	32 points	100 points							
Timer (1-sec, 100-ms, 10-ms, 1-ms)				256 points		32 points	100	O points					
	Backup (Internal relay, shift register, counter, data register										
	Backup (Duration	Approx. 30 days (typical) at 25°C after backup battery fully charged										
Д	Battery		Lithium secondary battery										
acku	Charging Time		Approx. 15 hours for charging from 0% to 90% of full charge										
RAM Backup	Battery Life		5 years										
AA.	Replacea	ability	N/A										
Self-diagnostic Function			Power failure, watchdog timer, data link connection, user program EPPROM sum check, timer/counter preset value sum check, user program RAM sum check, keep data, user program syntax, user program writing, CPU module, clock IC, I/O bus initialize, user program execution										
elf-diagn	iostic Fund	ction											
ŭ		ction		data, user program syn		ing, CPU module, cloc	k IC, I/O bus initialize, u						
nput Filter				o data, user program syn Without fil	tax, user program writ ter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse	ing, CPU module, cloc selectable in increment 2 through I5) width: 40µs minimum	k IC, I/O bus initialize, ι nts of 1ms)						
nput Filter	r ut/Interrup			o data, user program syn Without fil	tax, user program writ ter or 3 to 15ms filter (Four inputs (ing, CPU module, cloc selectable in increment 2 through I5) width: 40µs minimum	k IC, I/O bus initialize, ι nts of 1ms)						
nput Filter atch Inpu	r ut/Interrup Maximu Frequence	ot Input Im Counting Icy and High-speed	RAM sum check, keep	o data, user program syn Without fil N Total 4 points phase selectable: 50KHz	tax, user program writ ter or 3 to 15ms filter (Four inputs (I Minimum turn on pulse Minimum turn off pulse z (1 point)	ing, CPU module, cloc selectable in increme 2 through I5) a width: 40µs minimum width: 150µs minimum Single/tv	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2	user program exect					
atch Inpu	r ut/Interrup Maximu Frequent Counter	ot Input Im Counting Icy and High-speed Points	RAM sum check, keep	o data, user program syn Without fil N Total 4 points	tax, user program writ ter or 3 to 15ms filter (Four inputs (I Minimum turn on pulse Minimum turn off pulse z (1 point) s)	ing, CPU module, cloc selectable in increme 2 through I5) e width: 40µs minimum width: 150µs minimum Single/t	k IC, I/O bus initialize, unts of 1ms) Total 4 points	user program exect					
atch Inpu	r ut/Interrup Maximul Frequent Counter Counting	ot Input Im Counting Icy and High-speed Points g Range	RAM sum check, keep	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writ ter or 3 to 15ms filter (Four inputs (I Minimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits)	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exect					
High-speed Counter Counter	r ut/Interrup Maximu Frequent Counter	ot Input om Counting ocy and High-speed Points g Range on Mode	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writ ter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits)	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
pput Filter atch Input atch Input atch Input atch Input atch Input atch Input	Maximul Frequenc Counter Counting Operatio	ot Input Im Counting Icy and High-speed Points Ig Range In Mode Number	RAM sum check, keep	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter module	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exect					
nput Filter Conuter Connection Co	Maximul Frequenc Counter Counting Operatio	ot Input Im Counting Icy and High-speed Points g Range Ich Mode Number Data Range	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writ ter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter module	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
put Filter atch Inpu Conuter nalog	Maximul Frequenc Counter Counting Operatio	ot Input Im Counting Icy and High-speed Points g Range on Mode Number Data Range	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter module	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
put Filter atch Inpu atch Inpu Conuter nalog otentiom	Maximul Frequent Counter Counting Operatio	ot Input Im Counting Icy and High-speed Points Ig Range In Mode Number Data Range Number Duty Range	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter module	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
atch Input atch Input atch Input analog otentiom	Maximul Frequent Counting Operation eter	ot Input Im Counting Icy and High-speed Points Ig Range In Mode Number Data Range Number Input Voltage Range Input Impedance	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter module	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
atch Input atch Input paeds-Night control co	Maximul Frequent Counter Counting Operation eter Note The Details of the Details	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number Data Range Number Input Voltage Range Input Impedance Data Range	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter module	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
pput Filter atch Input peads-upit nalog otentiom nalog oltage Input	Maximul Frequence Counting Operation eter Note: The content of the counting operation eter of the counting operation eter operation of the counting operation eter operation of the counting operation	ot Input Im Counting Icy and High-speed Points Ig Range In Mode Number Data Range Number Input Voltage Range Input Impedance	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) nd adding counter mod	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
padds-uput Filter atch Input padds-uput padd	Maximul Frequence Counter Counting Operation eter I II	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number In Data Range Number Input Voltage Range Input Impedance Input Impe	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter mod 1 point 255	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
put Filter atch Inpu paeds-dilH nalog otentiom nalog oltage Input ulse utput	Maximul Frequence Counter Counting Operation eter put III III III III III III III III III I	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number In Data Range Input Voltage Range Input Impedance Impedance Input Impedance I	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I) Winimum turn on pulse Ainimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an 2 points 0 to	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter mod 1 point 255	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
put Filter padds-u6iH	Maximul Frequence Counter Counting Operation eter put III III III III III III III III III I	ot Input Im Counting Icy and High-speed Points g Range In Mode Number Data Range Number Input Voltage Range Input Impedance Data Range Number Max. Frequency Output Voltage Current Overload Detection	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points Phase selectable: 50KHz gle-phase: 5KHz (3 points	tax, user program writter or 3 to 15ms filter (Four inputs (I) Minimum turn on pulse Minimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an 2 points 0 to	ing, CPU module, cloc (selectable in increment 2 through I5) 2 width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter mod 1 point 255 0 -15%), 250mA	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
put Filter peads-light padds-light peads-light peads-l	Maximul Frequence Counter Counting Operation eter put III III III III III III III III III I	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number In Data Range Input Voltage Range Input Impedance Impedance Input Impedance I	RAM sum check, keep Single/two- Sing	o data, user program syn Without fil N Total 4 points phase selectable: 50KHz gle-phase: 5KHz (3 points Ri	tax, user program writter or 3 to 15ms filter (Four inputs (I) Winimum turn on pulse Alinimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an 2 points 0 to	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to S (16 bits) and adding counter mod 1 point 255 0 -15%), 250mA	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
put Filter atch Inpu paeds-light nalog otentiom nalog oltage Input ulse utput ensor Pov upply AC Power	Maximul Frequent Counter Counting Operation eter put In	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number Input Voltage Range Input Impedance I	Single/two-Sing	o data, user program syn Without fil N Total 4 points phase selectable: 50KHz gle-phase: 5KHz (3 points Ri	tax, user program writter or 3 to 15ms filter (Four inputs (I) Winimum turn on pulse (I) Minimum turn off pulse (I) I point) I to 6553	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to S (16 bits) and adding counter mod 1 point 255 0 -15%), 250mA	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	user program exec OKHz (1 point) points)					
pands-liberated Input Filterate Input Filterate Input Filterate Input In	Maximul Frequence Counting Operation eter put In	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number Input Voltage Range Input Voltage Range Input Impedance Input Impedance Input Voltage Input Impedance Input Voltage Input Impedance Imput Impedance Imput Impedance Input Impedance Imput	Single/two-	o data, user program syn Without fil N Total 4 points phase selectable: 50KHz gle-phase: 5KHz (3 points Ri	tax, user program writter or 3 to 15ms filter (Four inputs (I) Winimum turn on pulse Alinimum turn off pulse z (1 point) s) 0 to 6553 otary encoder mode an 2 points 0 to	ing, CPU module, cloc (selectable in increment 2 through I5) e width: 40µs minimum width: 150µs minimum Single/to S (16 bits) and adding counter mod 1 point 255 0 -15%), 250mA	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 p	OKHz (1 point) coints) 2 points					
nput Filter Catch Input Padds-ubiH Analog Cotention Analog Cotention Cotage Input	Maximul Frequent Counter Counting Operation eter put In	ot Input Im Counting Icy and High-speed Points Ig Range In Mode In Number Input Voltage Range Input Voltage Range Input Impedance Input Impedance Input Voltage Imput Vol	Single/two-Sing	o data, user program syn Without fil N Total 4 points phase selectable: 50KHz gle-phase: 5KHz (3 points Ri RS232C Possible	tax, user program writter or 3 to 15ms filter (Four inputs (I Minimum turn on pulse (I 1) point) s) O to 6553 otary encoder mode at 2 points O to 24V DC (+10% to N, Isolated from th (maintenance commul Possible	ing, CPU module, cloc (selectable in increment 2 through I5) 2 width: 40µs minimum width: 150µs minimum Single/to 5 (16 bits) and adding counter mod 1 point 255 0 -15%), 250mA //A e internal circuit nication, user commun	k IC, I/O bus initialize, unts of 1ms) Total 4 points wo-phase selectable: 2 Single-phase: 5KHz (3 phase) de	OKHz (1 point) points) 2 points					



- 2. Not including expansion I/O service time, clock function processing time, data link processing time, and interrupt processing time.
 - 3. Maintenance communication, user communication, Modem communication, datalink, Modbus master/slave communication (FC5A only).

Note: The maximum number of relay outputs that can be turned on simultaneously is 33 including those on the CPU module.

Communication Port (RS232C Port 1)

Model	Slim CPU	All-in-One CPU			
Standards	EIA RS232C				
Maximum Baud Rate	FC5A: 57,600 bps (maintenance communication) FC4A: 19,200 bps (maintenance communication)				
Maintenance Communication	Pos	sible			
User Communication	Pos	sible			
Modem Communication	N/A				
Data Link	N	I/A			
Cable	Special cable (FC2A-KC4C, FC2A-KP1C, FC4A-KC1C, FC4A-KC2C)				
Isolation between Internal Circuit and Communication Port	Not is	solated			

Input Specifications

Part Number		-	FC5A-D16RK1 FC5A-D16RS1	-	FC5A-D32K3 FC5A-D32S3	-	FC5A-C10R2 FC5A-C10R2C	FC5A-C16R2 FC5A-C16R2C	FC5A-C24R2 FC5A-C24R2C	
rart Numbei	•	FC4A-D20K3 FC4A-D20S3	-	FC4A-D20RK1 FC4A-D20RS1	-	FC4A-D40K3 FC4A-D40S3	FC4A-C10R2 FC4A-C10R2C	FC4A-C16R2 FC4A-C16R2C	FC4A-C24R2 FC4A-C24R2C	
Input Points		12 (12/1 common)	8 (8/1 common)	12 (12/1 common)	16 (8/1 common)	24 (12/1 common)	6 (6/1 common)	9 (9/1 common)	14 (14/1 common)	
Input Voltage		24V DC sink/source input signal								
Input Voltage Range					20.4 to 28.8V DC					
Input Current		FC5A I0, I1, I3, I4, I6, I7: 4.5mA/point (24V DC) I2, I5, I10 to I17: 7mA/point (24V DC) FC4A I0, I1, I6, I7: 5mA/point (24V DC) I2 to I5, I10 to I27: 7mA/point (24V DC)					FC5A IO and I1: 6.4mA/point I2 to I7, I10 to I15: 7mA/point (24V DC) FC4A IO and I1: 11mA I2 to I7, I10 to I15: 7mA/point (24V DC)			
Input Impedance		FC5A					$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			
Turn ON Time		FC5A I0, I1, I3, I4, I6, I7: 5µs + filter value					$ \begin{array}{lll} FC5A \ IO \ and \ I1: & 2\mu s + filter \ value \\ I2 \ to \ I7: & 35\mu s + filter \ value \\ I6, \ I7, \ I10 \ to \ I15: & 40\mu s + filter \ value \\ FC4A \ IO \ and \ I1: & 35\mu s + filter \ value \\ I2 \ to \ I5: & 35\mu s + filter \ value \\ I6, \ I7, \ I10 \ to \ I15: & 40\mu s + filter \ value \\ \end{array} $			
Turn OFF Time		FC5A I0, I1, I3, I4, I6, I7: 5µs + filter value I2 and I5: 150µs + filter value I10 to I17: 150µs + filter value FC4A I0, I1, I6, I7: 45µs + filter value I2 to I5: 150µs + filter value I10 to I27: 150µs + filter value					FC5A I0 and I1: I2 to I7: I6, I7, I10 FC4A I0 and I1: I2 to I5: I6, I7, I10	150µs - to 115: 150µs - 45µs + 150µs -	filter value + filter value + filter value filter value + filter value + filter value	
Connector	On Mother Board	FL26A2MA (Oki Electric Cable)	MC1.5/18-G-3.81I (Phoenix Contact)	ЗК	FL26A2MA (Oki Electric Cal	ble)	_			
	Insertion Durability		1	00 times minimum			_			
Isolation		Between input terminals: Photocoupler isolated Internal circuit: Not isolated								
Input		Type 1 (IEC61131-2)								
External Load for I/O Interconnection		Not needed								
Single Determination Method		Static								
Effect of Improper Input Connection		Both sinking and sourcing input signals can be connected. If any input exceeding the rated value is applied, permanent damage may be caused.								
Cable Length		3 m in compliance with electromagnetic immunity								

FC5A-D16RS1

FC4A-D20RS1

FC5A-C16R2

FC5A-C16R2C

FC4A-C16R2

FC4A-J4CN1,

FC4A-J8AT1,

FC4A-AS62M,

FC4A-N08A11,

70.0

70.0

FC4A-T08K1,

FC4A-C16R2C

70.0

FC4A-M08BR1,

FC4A-L03AP1,

FC4A-K1A1.

FC4A-J8C1,

FC4A-K2C1,

14.6

90.0

IDEC

FC5A-D16RK1.

FC4A-D20RK1,

47.5

FC5A-C10R2,

FC4A-C10R2,

FC4A-C10R2C,

FC4A-N08B1,

FC4A-R081.

FC4A-T08S1,

FC4A-L03A1,

FC4A-J2A1,

3.8 | 23.5

3.8 | 23.5

90.0

FC4A-N16B1, FC4A-R161

90.0

14.6

80.0

FC5A-C10R2C,

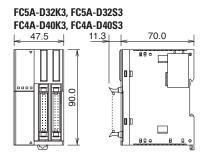


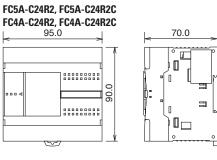


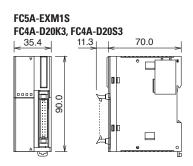




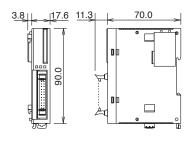
Dimensions (mm)

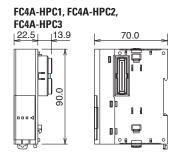


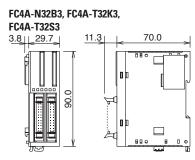


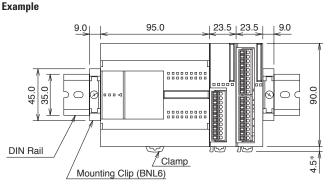


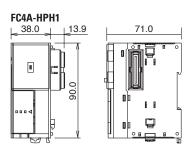








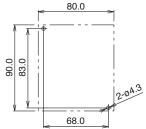




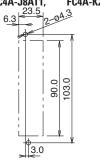
The figure illustrates a system setup consisting of the all-in-one 24-I/O CPU module, an 8-point relay output module, and a 16-point DC input module mounted on a 35mm-wide-DIN rail using BNL6 mounting clips.

Mounting Hole Layout (mm)



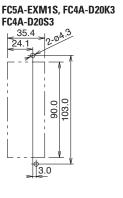


FC4A-N08A11, FC4A-R081 FC4A-R161, FC4A-T08K1 FC4A-T08S1, FC4A-M08BR1 FC4A-L03A1, FC4A-L03AP1 FC4A-J2A1, FC4A-K1A1 FC4A-J4CN1, FC4A-T8C1 FC4A-J8AT1, FC4A-K2C1

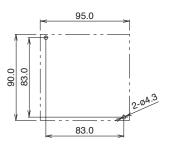


FC5A-EXM2 FC4A-M24BR2 6.3 39.1 2.04.3 0.00 0.00

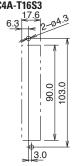
3.0



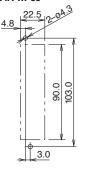
FC5A-C24R2, FC4A-C24R2C FC4A-C24R2, FC4A-C24R2C



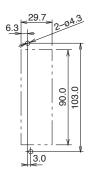
FC5A-EXM1M FC4A-N16B3, FC4A-T16K3, FC4A-T16S3



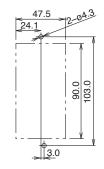
FC4A-HPC1 FC4A-HPC2 FC4A-HPC3



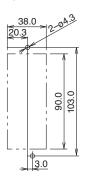
FC4A-N32B3, FC4A-T32K3, FC4A-T32S3



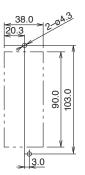
FC5A-D16RK1 FC5A-D16RS1 FC5A-D32K3 FC5A-D32S3 FC4A-D20RK1 FC4A-D20RS1 FC4A-D40K3 FC4A-D40S3



FC4A-HPH1

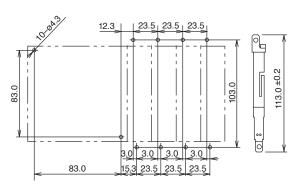


FC4A-HPH1



Examples

Mounting hole layout for FC5A-C24R2 or FC4A-C24R2 and four 23.5mm-wide I/O modules



Mounting hole layout from left, FC4A-HPH1, FC4A-D20K3, FC4A-N16B3, FC4A-N32B3, and FC4A-M24BR2 modules

