

SEMITRANS<sup>®</sup> 3

## **IGBT Modules**

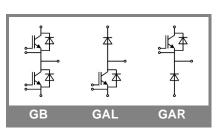
SKM 200GB123D SKM 200GAL123D SKM 200GAR123D

#### Features

- MOS input (voltage controlled)
- N channel, homogeneous Si
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to 6 x I<sub>cnom</sub>
- Latch-up free
- Fast & soft inverse CAL diodes
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (13 mm) and creepage distances (20 mm)

### **Typical Applications**

- AC inverter drives
- UPS



Absolu	te Maximum Ratings	$T_c = 2$	25 °C, unless otherwise	specified	
Symbol	I Conditions		Values Ur		
IGBT					
V <sub>CES</sub>	T <sub>j</sub> = 25 °C T <sub>i</sub> = 150 °C		1200	V	
I <sub>C</sub>	T <sub>j</sub> = 150 °C	T <sub>case</sub> = 25 °C	200	Α	
		T <sub>case</sub> = 85 °C	180	А	
I <sub>CRM</sub>	I <sub>CRM</sub> =2xI <sub>Cnom</sub>		300	А	
V <sub>GES</sub>			± 20	V	
t <sub>psc</sub>	$V_{CC}$ = 600 V; $V_{GE} \le 20$ V; VCES < 1200 V	T <sub>j</sub> = 125 °C	10	μs	
Inverse	Diode			•	
I <sub>F</sub>	T <sub>j</sub> = 150 °C	T <sub>case</sub> = 25 °C	200	А	
		T <sub>case</sub> = 80 °C	130	А	
I <sub>FRM</sub>	I <sub>FRM</sub> =2xI <sub>Fnom</sub>		300	А	
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms; sin.	T <sub>j</sub> = 150 °C	1440	А	
Freewh	eeling Diode			•	
I <sub>F</sub>	T <sub>j</sub> = 150 °C	T <sub>case</sub> = 25 °C	260	А	
		T <sub>case</sub> = 80 °C	180	А	
I <sub>FRM</sub>	I <sub>FRM</sub> =2xI <sub>Fnom</sub>		400	А	
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms; sin.	T <sub>j</sub> = 150 °C	1800	А	
Module	)				
I <sub>t(RMS)</sub>			500	А	
T <sub>vj</sub>			- 40 + 150 (125)	°C	
T <sub>stg</sub>			- 40+ 125	°C	
V <sub>isol</sub>	AC, 1 min.		2500	V	

Characteristics T			= 25 °C, unless otherwise specified			
Symbol	Conditions		min.	typ.	max.	Units
IGBT						
V <sub>GE(th)</sub>	$V_{GE} = V_{CE}, I_C = 6 \text{ mA}$		4,5	5,5	6,5	V
I <sub>CES</sub>	$V_{GE}$ = 0 V, $V_{CE}$ = $V_{CES}$	T <sub>j</sub> = 25 °C		0,1	0,3	mA
V <sub>CE0</sub>		T <sub>j</sub> = 25 °C		1,4	1,6	V
		T <sub>j</sub> = 125 °C		1,6	1,8	V
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25°C		7,33	9,33	mΩ
		T <sub>j</sub> = 125°C		10	12,66	mΩ
V <sub>CE(sat)</sub>	I <sub>Cnom</sub> = 150 A, V <sub>GE</sub> = 15 V	T <sub>j</sub> = °C <sub>chiplev.</sub>		2,5	3	V
C <sub>ies</sub>				10	13	nF
C <sub>oes</sub>	V <sub>CE</sub> = 25, V <sub>GE</sub> = 0 V	f = 1 MHz		1,5	2	nF
C <sub>res</sub>				0,8	1,2	nF
Q <sub>G</sub>	V <sub>GE</sub> = -8V - +20V			1500		nC
R <sub>Gint</sub>	T <sub>j</sub> = °C			2,5		Ω
t <sub>d(on)</sub>				220	400	ns
t,	R <sub>Gon</sub> = 5,6 Ω	V <sub>CC</sub> = 600V		100	200	ns
É <sub>on</sub>		I <sub>C</sub> = 150A		24		mJ
<sup>t</sup> d(off)	R <sub>Goff</sub> = 5,6 Ω	T <sub>j</sub> = 125 °C		600	800	ns
t <sub>f</sub>		V <sub>GE</sub> = -15V		70	100	ns
E <sub>off</sub>				17		mJ
R <sub>th(j-c)</sub>	per IGBT				0,09	K/W



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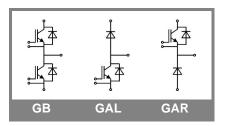
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Characte		I		4		111
-	Conditions		min.	typ.	max.	Units
Inverse D		T - 05 %O		0	0.5	
$v_F = v_{EC}$	I <sub>Fnom</sub> = 150 A; V <sub>GE</sub> = 0 V			2	2,5	V
.,		$T_j = 125 \ ^\circ C_{chiplev.}$ $T_j = 25 \ ^\circ C$		1,8		V
V <sub>F0</sub>		$I_j = 25 \text{ °C}$		1,1	1,2	V
		T <sub>j</sub> = 125 °C				V
r <sub>F</sub>		T <sub>j</sub> = 25 °C		6	8,7	mΩ
		T <sub>j</sub> = 125 °C T <sub>j</sub> = 125 °C				mΩ
IRRM	I <sub>F</sub> = 150 A	T <sub>j</sub> = 125 °C		90		Α
Q <sub>rr</sub>	di/dt = 1500 A/µs			8		μC
Err	V <sub>GE</sub> = -15 V; V <sub>cc</sub> = 600V			6,6		mJ
R <sub>th(j-c)D</sub>	per diode				0,25	K/W
	eling Diode					
$V_F = V_{EC}$	I <sub>Fnom</sub> = 200 A; V <sub>GE</sub> = 0 V	T <sub>j</sub> = 25 °C <sub>chiplev.</sub>		2	2,5	V
		T <sub>j</sub> = 125 °C <sub>chiplev.</sub>		1,8		V
V <sub>F0</sub>		$T_j = 125 \ ^\circ C_{chiplev.}$ $T_j = 25 \ ^\circ C$		1,1	1,2	V
		T <sub>j</sub> = 125 °C				V
r <sub>F</sub>		T <sub>i</sub> = 25 °C		4,5	6,5	V
		T <sub>j</sub> = 125 °C				V
I <sub>RRM</sub>	I <sub>F</sub> = 200 A	T <sub>i</sub> = 125 °C		120		Α
Q <sub>rr</sub>	di/dt = 2000 A/µs	j		11		μC
E <sub>rr</sub>	$V_{GE}$ = 0 V; $V_{CC}$ = 600 V					mJ
R <sub>th(j-c)FD</sub>	per diode				0,18	K/W
Module						-
L <sub>CE</sub>				15	20	nH
R <sub>CC'+EE'</sub>	res., terminal-chip	T <sub>case</sub> = 25 °C		0,35		mΩ
00.22		T <sub>case</sub> = 125 °C		0,5		mΩ
R <sub>th(c-s)</sub>	per module				0,038	K/W
M <sub>s</sub>	to heat sink M6		3		5	Nm
M <sub>t</sub>	to terminals M6, M4		2,5		5	Nm
w					325	g

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.



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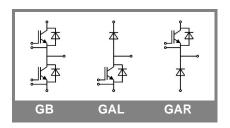
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Z <sub>th</sub>			
	Conditions	Values	Units
Z Ri th(j-c)l			
R <sub>i</sub>	i = 1	59	mk/W
R <sub>i</sub>	i = 2	23	mk/W
R <sub>i</sub>	i = 3	6,8	mk/W
R <sub>i</sub>	i = 4	1,2	mk/W
tau	i = 1	0,03	s
tau <sub>i</sub>	i = 2	0,0087	S
tau <sub>i</sub>	i = 3	0,002	s
tau <sub>i</sub>	i = 4	0,0002	s
Z R <sub>i</sub> th(j-c)D			
R <sub>i</sub>	i = 1	170	mk/W
R <sub>i</sub>	i = 2	66	mk/W
R <sub>i</sub>	i = 3	12	mk/W
R <sub>i</sub>	i = 4	2	mk/W
tau	i = 1	0,0348	S
tau	i = 2	0,0072	S
tau <sub>i</sub>	i = 3	0,077	S
tau <sub>i</sub>	i = 4	0,0002	s

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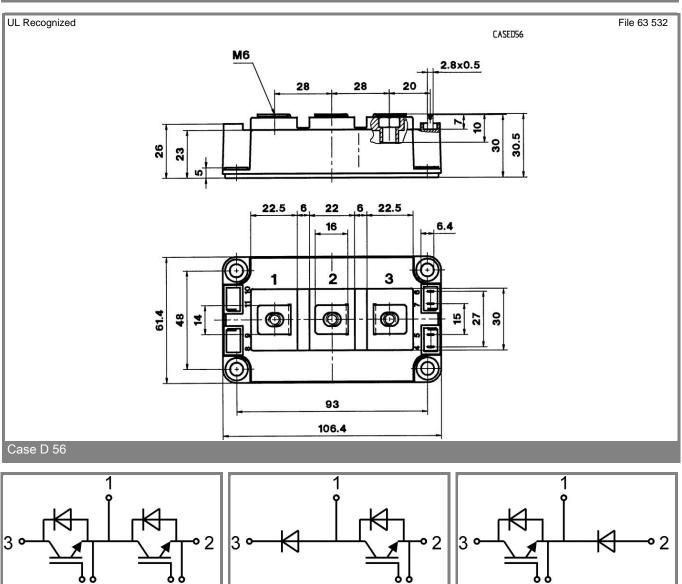
Case D 56

6 7

GB

Case D 57

(56)



6 7

GAL

45

Case D 58

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GAR