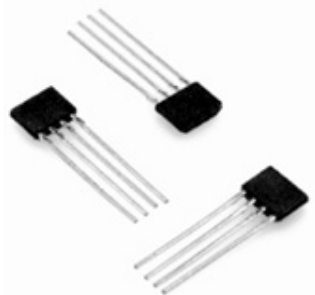




### SS421H



*Actual product appearance may vary.*

**SS421H Adjustable Hall-effect  
Underspeed Detector (Active - High);  
SIP-4 radial lead IC package**

#### Features

- Wide temperature ranges for design flexibility
- Temperature compensated for consistent operation with low-cost magnets
- Bipolar operation for easy RPM counting using ring magnets
- Built-in timing circuit greatly simplifies and reduces the cost of PC board design
- Speed trip point adjustable with external resistor and capacitor

#### Potential Applications

- Under-speed detection for fans
- Conveyors
- Motor control
- Power-up fault failure filter in motor start-up

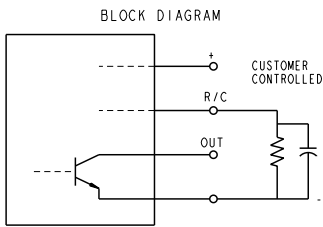
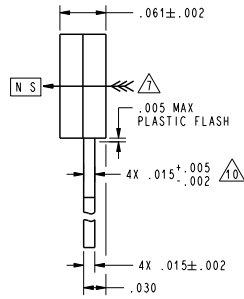
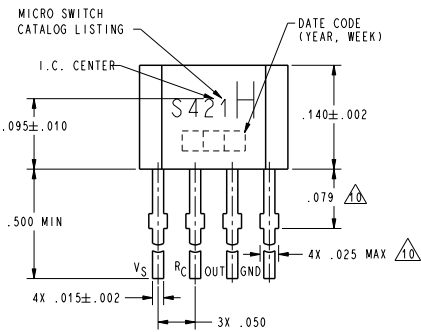
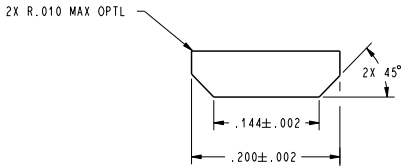
#### Description

The SS421 Series Adjustable Hall-effect Under-speed Detectors are designed to monitor fan or motor performance. This temperature-compensated, Hall-effect IC contains a timing circuit and logic, that senses magnetic input frequency. The internal circuitry contains a timer so that one or two pulses at a slower repetition rate than the set point do not produce an unwanted output.

A small amount of hysteresis has been built into the output so that operation right at the set point does not result in a chattering output. The user simply provides an external resistor and capacitor combination to select the RPM trip point that will fit the particular application.

Product Specifications	
Product Type	Hall-Effect Under-speed Detector IC
Package Style	Radial Lead IC
Supply Voltage	4.5 Vdc to 16 Vdc
Output Type	Sink
Termination Type	PC Board
Magnetic Actuation Type	Bipolar
Operating Temperature Range	-40 °C to 105 °C [-40 °F to 221 °F]
Storage Temperature	-40 °C to 125 °C [-40 °F to 257°F]
Output Voltage	0.4 Vdc max.
Switching Time Rise (10 % to 90 %)	1.5 µs max.
Switching Time Fall (90 % to 10 %)	1.5 µs max.
Output Voltage Span (min.)	0.4 Vdc
Operate Point	250 G
Release Point	-250 G
Availability	Global
Supply Current (max. @ 25 °C)	15 mA
Output Current (max.)	20.0 mA
Operate Point @ 25 °C	25.0 mT [250 G] max.
Release Point @ 25 °C	-25.0 mT [-250 G] min.
Leakage Current max.	10 µA
Differential	5.0 mT [50 G] min.
Series Name	SS421
Supply Current (typ. @ 25 °C)	15 mA

**HIGH WHEN FREQUENCY IS > SET POINT**



1. ABSOLUTE MAXIMUM RATINGS<sup>9</sup>  
(Circuit function is not guaranteed. If exceeded, permanent damage may result)

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
1.1	AMBIENT TEMPERATURE	-40	+125	°C	Storage, no power applied
1.2	AMBIENT TEMPERATURE	-40	+105	°C	Operating, power applied
1.3	SUPPLY VOLTAGE	-25	+25	VDC	-40°C TO +105°C ambient temperature
1.4	VOLTAGE AT OUTPUT	-0.5	+25	VDC	off condition of output over temperature
1.5	OPEN COLLECTOR NPN		+20	mA	Operated over temperature, Current sinking output inverted or non-inverted

2. ELECTRICAL CHARACTERISTICS  
(Over operating temperature and voltage range unless otherwise noted)  
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
2.1	SUPPLY VOLTAGE (V <sub>S</sub> )	4.5	16	VDC	
2.2	SUPPLY CURRENT (I <sub>S</sub> )	15	15	mA	Output released, V <sub>S</sub> =16V T <sub>J</sub> =+25°C
2.3	SUPPLY CURRENT (I <sub>S</sub> )	15	15	mA	Output operated, V <sub>S</sub> =16V T <sub>J</sub> =+25°C
2.4	OUTPUT VOLTAGE		0.4	VDC	Sinking 15mA, V <sub>S</sub> at
2.5	OUTPUT LEAKAGE CURRENT	10		µA	Output at 25V
2.6	RISE TIME (10% TO 90%)		1.5	µS	V <sub>S</sub> =12V, R=800ohms, C=50pf
2.7	FALL TIME (90% TO 10%)		1.5	µS	V <sub>S</sub> =12V, R=800ohms, C=50pf

3. MAGNETIC CHARACTERISTICS<sup>8</sup>  
(Over operating temperature and voltage range unless otherwise noted)  
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
3.1	OPERATE POINT		250	G	T <sub>a</sub> =+25°C (See note 3.1)
3.2	OPERATE POINT		250	G	-25°C TO +105°C (See note 3.1)
3.3	RELEASE POINT	-250		G	T <sub>a</sub> =+25°C
3.4	RELEASE POINT	-250		G	-25°C TO +105°C (See note 3.2)
3.4	DIFFERENTIAL	50		G	Operate minus release

Frequency Trip Point (FTP)(RPM)

$$FTP = \frac{60}{RC \log \frac{Vr_1 - IcB}{Vr_2 - IcB}}$$

Delay Time (DST) (Seconds)

$$DST = 7RC \log \left[ \frac{Vr_2}{Vr_1} + \frac{Vr_2}{FTP} \right]$$

WHERE,  
R, C ARE EXTERNAL COMPONENTS  
Vr1=2 VOLTS Vr2=4 VOLTS  
Ic=196 µA  
LOG IS NATURAL LOG

**NOTES**

1. THE MAGNETIC FLUX USED TO OPERATE THE SWITCH MUST BE IN THE DIRECTION AND LOCATION SHOWN (THIS ASSUMES THE CONVENTION THAT THE DIRECTION OF THE EXTERNAL FLUX OF A MAGNET IS FROM THE NORTH TO THE SOUTH POLE OF THE MAGNET)
2. THE MAGNETIC FIELD STRENGTH (GAUSS) REQUIRED TO CAUSE THE SWITCH TO CHANGE STATE (OPERATE AND RELEASE) WILL BE AS SPECIFIED IN THE MAGNETIC CHARACTERISTICS. TO TEST THE SWITCH AGAINST THE SPECIFIED MAGNETIC CHARACTERISTICS THE SWITCH MUST BE PLACED IN A UNIFORM MAGNETIC FIELD
3. ABSOLUTE MAXIMUM RATINGS ARE THE EXTREME LIMITS THAT THE DEVICE WILL WITHSTAND WITHOUT DAMAGE TO THE DEVICE. HOWEVER, THE ELECTRICAL AND MAGNETIC CHARACTERISTICS ARE NOT GUARANTEED AS THE MAXIMUM LIMITS (ABOVE RECOMMENDED OPERATING CONDITIONS) ARE APPROACHED NOR WILL THE DEVICE NECESSARILY OPERATE AT ABSOLUTE MAXIMUM RATING
4. DIMENSIONS NOTED ARE DUE TO TIE BAR REMOVAL AND ARE VALID ONLY IN .079 DIMENSION. BURRS EXTEND BACK FROM FRONT SURFACE ONLY

THIRD ANGLE PROJECTION		
SCALE	10:1	
DO NOT SCALE PRINT		
UNLESS OTHERWISE SPECIFIED TOLERANCES ARE		
ONE PLACE	(.01)	±.030
TWO PLACES	(.001)	±.015
THREE PLACES	(.0001)	±.005
ANGLES		± 2°
WEIGHT		

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**SOLID STATE SWITCH**

CATALOG LISTING  
**SS421H**

ANSI Y14.5M-1982 APPLIES

FED. MFG. CODE 9129

WEIGHT

**4. TIMING CHARACTERISTICS**  
(over operating temperature and voltage range unless otherwise noted. Does not include R & C shifts over temperature)  
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
4.1	TRIP POINT RANGE	1800	5200	PPM	Ta = 25°C (See note 4.1)
4.2	TRIP POINT TOLERANCE	-16.0	16.0	%	Variation from calculated value (See note 4.2)
4.3	TRIP POINT DELAY & POWER UP DELAY				
	DELATED VERSION	5.0	16.0	SEC	Ta = 25°C, 1800 TO 5200 PPM
	NON-DELATED VERSION		1.2	SEC	Ta = 25°C, 1800 TO 5200 PPM (See note 4.3)
4.4	DELAY SET TIME TOLERANCE THE SMALLER OF 4.4.1 & 4.4.2				
4.4.1		-40	+40	%	% of delay set time
4.4.2		-4.0	4.0	SEC	
4.5	PASSING DELAY FAIL TO PASS CONDITION		20	%	% of delay set time (See note 4.5)
4.6	EXTERNAL CAPACITOR LEAKAGE COEFFICIENT		0.05		(See note 4.6)

**5. LATCH CHARACTERISTICS**  
(over operating temperature and voltage range unless otherwise noted. External components must be connected between the output and the program/oscillator pin)  
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS#
5.1	LATCH VOLTAGE				(See note 5.1)
5.1.1		3.5	4.5	V	
5.1.2		2.0	4.5	V	at VS = 5.0
5.2	LATCH CURRENT				NOT APPLICABLE ON THIS NON-INVERTED DEVICE

**6. EXTERNAL R&C CHARACTERISTICS**  
(over operating temperature and voltage range unless otherwise noted)  
REQUIRED -25°C TO +105°C AMBIENT

ITEM	PARAMETER	MIN	MAX	UNIT	CONDITIONS
6.1	EXTERNAL RESISTOR		±5	%	Directly related to 4.2 Trip Accuracy
6.2	EXTERNAL CAPACITOR		±5	%	Directly related to 4.2 Trip Accuracy
6.3	EXTERNAL CAPACITOR LEAKAGE COEFFICIENT		0.05		(See note 6.3)

- 3.1 Operate point is defined as the gauss level above which the internal circuitry will always be indicating the presence of a south pole at the IC surface.
- 3.2 Release point is defined as the gauss level below which the internal circuitry will always be indicating the presence of a north pole at the IC surface.
- 4.1 Trip point is the frequency in PPM (pulses per minute) that causes the output to change state. A non-inverted output is high when speed is greater than the trip point. For example, a 2 pole magnet translates to 1 pulse/revolution while the 4 pole magnet translates into 2 pulses/revolution. The trip point is externally adjustable by varying the value of R and C. Maximum required value of the external capacitor is 3.3 uF.
- 4.2 This is the accuracy required from unit to unit and includes R and C each varying ±5%, over speed, supply voltage, and temperature.
- 4.3 Trip point delay is the delay in output response to an input frequency below the trip point. The time delay is determined by the value of the external resistor and capacitor. Delayed version powers up in the passing condition.
- 4.5 Passing delay tolerance is based on delay set time, its tolerance is proportional to delay set time tolerance.
- 4.6 Capacitor leakage coefficient is used to calculate leakage current in the following formula:  
Leakage Current = Leakage Coefficient x Capacitance x Capacitor Voltage  
where capacitance is value of the external capacitor.  
Capacitor Voltage is the actual variable voltage across the capacitor in operation.
- 5.1 The open collector output can be used to drive external circuitry that applies a latching voltage to the program/oscillator pin to latch the IC output in the fail state. This latch mode is to be used only on products with delay set times greater than 5 seconds and with inverted output version of the SS42IH. Va = externally applied voltage on the RC pin. (Not applicable on this non-inverted device)
- 5.2 Latching current is the sourcing current required of the applied external voltage to maintain the latch. (Not applicable on this non-inverted device)
- 6.3 Leakage coefficient is the external capacitor leakage current coefficient such that:  
Leakage Current = Leakage Coefficient x Capacitance x Capacitor Voltage.  
For example: Leakage Coefficient = 0.05; Capacitance = 3.3 uF; Capacitor Voltage of 3V; DC leakage current 0.49 uA.

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**SOLID STATE SWITCH**

CATALOG LISTING  
**SS42IH**

FED. MFG. CODE 91929

THIRD ANGLE PROJECTION

SCALE NONE

DO NOT SCALE PRINT

UNLESS OTHERWISE SPECIFIED TOLERANCES ARE

ONE PLACE	(.01)	±.030
TWO PLACES	(.001)	±.015
THREE PLACES	(.0001)	±.005
ANGLES		± 2°

WEIGHT

ANSI Y14.5M-1982 APPLIES

**TIMING TABLE**

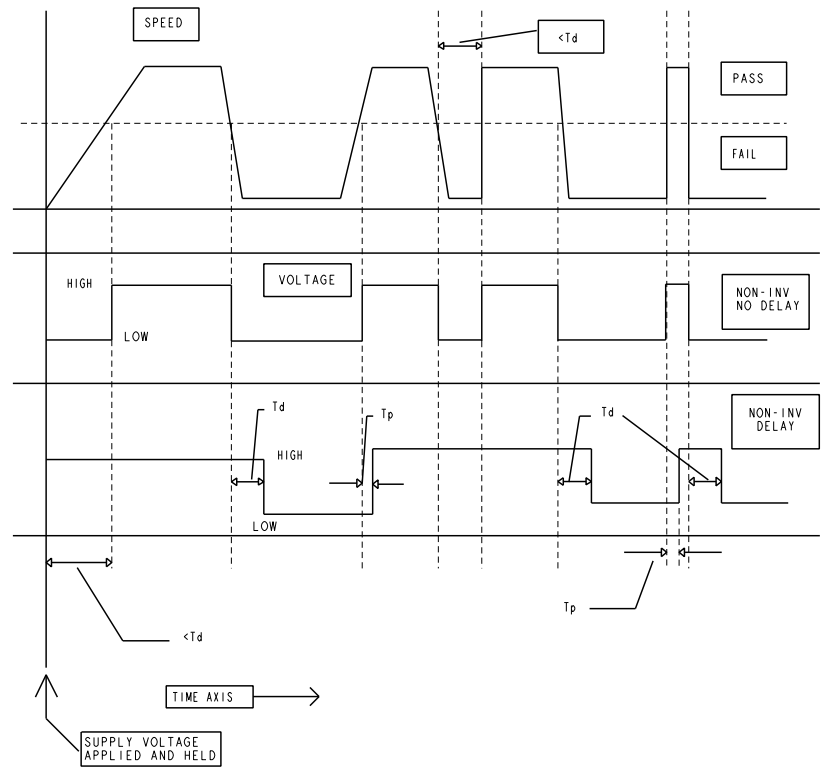
FREQUENCY TRIP POINT FTP =  $60 / RC \ln((Vr1 - IcR) / (Vr2 - IcR))$   
 INERTIAL DELAY SET TIME DST =  $7RC \ln(Vr2 / Vr1) + N * 60 / FTP$   
 In is the natural log

WHERE NOMINAL VALUE OF:

- Vr1 = 2V
- Vr2 = 2Vr1 = 4V
- Ic = 196uA
- N = 12
- R, C = exterior components

External components: R (Kohm) and C (uF)  
 Target Timings: Frequency Trip Point (PPM)/Inertial Delay Set Time(s)#

R/C	1	1.2	1.5	1.8	2.2	2.7	3.3
39	3516/0.39	2930/0.47	2344/0.59	1954/0.71	1598/0.87		
43	3744/0.40	3120/0.48	2496/0.60	2080/0.72	1702/0.88		
47	3837/0.41	3276/0.49	2620/0.62	2184/0.74	1817/0.90		
51	4087/0.42	3405/0.51	2725/0.64	2271/0.76	1898/0.93		
56	4250/0.44	3542/0.53	2834/0.66	2361/0.78	1932/0.97		
62	4417/0.46	3676/0.56	2940/0.70	2450/0.84			
68	4542/0.49	3785/0.59	3028/0.73	2525/0.88			
75	4683/0.52	3880/0.62	3120/0.76	2594/0.93			
82	4773/0.55	3978/0.66	3182/0.82	2652/0.99			
91	4884/0.59	4070/0.71	3256/0.88				
100	4970/0.63	4145/0.76	3316/0.94				
110	5057/0.68	4214/0.81					1681/4.75
120	5126/0.72	4272/0.87					1691/5.23
130	5184/0.77	4320/0.92					2077/4.67
150	5270/0.86						2085/5.06
160	5315/0.91						2100/5.98
180							2117/7.02
200							2118/7.88
220							2124/8.46
240							2131/11.08
260							2141/12.28
280							2142/14.75
300							2147/16.37
330							2152/17.31
360							2152/17.31
390							2152/17.31
430							2152/17.31
470							2152/17.31
510							2152/17.31
560							2152/17.31
620							2152/17.31
680							2152/17.31
750							2152/17.31
820							2152/17.31
910							2152/17.31
1000							2152/17.31
1100							2152/17.31
1200							2152/17.31
1300							2152/17.31
1500							2152/17.31
1600							2152/17.31
1800							2152/17.31
2000							2152/17.31
2200							2152/17.31
2400							2152/17.31



$T_d$  is the time delay after the pulses/min. go below the trip point.  $T_d$  is set by the external RC.  
 $T_p$  is the time delay after the pulses/min. go above the trip point.  $T_p$  is less than 20% of  $T_d$ .

THIRD ANGLE PROJECTION
SCALE NONE
DO NOT SCALE PRINT
UNLESS OTHERWISE SPECIFIED TOLERANCES ARE
ONE PLACE (.01) ±.030
TWO PLACES (.001) ±.015
THREE PLACES (.0001) ±.005
ANGLES ±.2°
WEIGHT

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CATALOG LISTING  
**SS42IH**

FED. MFG. CODE 91229

ANSI Y14.5M-1982 APPLIES

P.T.C./CAD 70  
 C.S.C. 14 MAY 03  
 S.W. 14 MAY 03  
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 SS42IH  
 REVISIONS  
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 1989-1985-SS  
 1989-1985-SS