

FUJI POWER MOSFET Super FAP-G Series

N-CHANNEL SILICON POWER MOSFET

Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- Avalanche-proof

Applications

- Switching regulators
- UPS (Uninterruptible Power Supply)
- DC-DC converters

Maximum ratings and characteristic Absolute maximum ratings

(Tc=25°C unless otherwise specified)

Item	Symbol	Ratings	Unit
Drain-source voltage	V _{DS}	500	V
	V _{DSX} *5	500	V
Continuous drain current	I _D	±25	A
Pulsed drain current	I _{D(puls)}	±100	A
Gate-source voltage	V _{GS}	±30	V
Repetitive or non-repetitive	I _{AR} *2	25	A
Maximum Avalanche Energy	E _{AS} *1	336.5	mJ
Maximum Drain-Source dV/dt	dV _{DS} /dt *4	20	kV/μs
Peak Diode Recovery dV/dt	dV/dt *3	5	kV/μs
Max. power dissipation	P _D	T _a =25°C	2.50
		T _c =25°C	335
Operating and storage temperature range	T _{ch}	+150	°C
	T _{stg}	-55 to +150	°C

*1 L=987μH, V_{CC}=50V, See to Avalanche Energy Graph *2 T_{ch}≤150°C

*3 I_F≤-I_D, -di/dt=50A/μs, V_{CC}≤BV_{DSS}, T_{ch}≤150°C *4 V_{DS}≤500V *5 V_{GS}=-30V

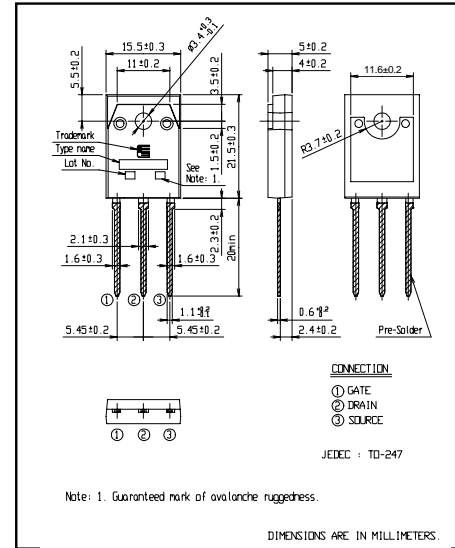
Electrical characteristics (Tc =25°C unless otherwise specified)

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	V _{(BR)DSS}	I _D =250μA V _{GS} =0V	500			V
Gate threshold voltage	V _{GS(th)}	I _D =250μA V _{DS} =V _{GS}	3.0		5.0	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =500V V _{GS} =0V	T _{ch} =25°C		25	μA
			T _{ch} =125°C		250	
Gate-source leakage current	I _{GSS}	V _{GS} =±30V V _{DS} =0V		10	100	nA
Drain-source on-state resistance	R _{DS(on)}	I _D =10.5A V _{GS} =10V		0.20	0.26	Ω
Forward transconductance	g _{fs}	I _D =10.5A V _{DS} =25V	11	22		S
Input capacitance	C _{iss}	V _{DS} =25V		2280	3420	pF
Output capacitance	C _{oss}	V _{GS} =0V		320	480	
Reverse transfer capacitance	C _{rss}	f=1MHz		16	24	
Turn-on time t _{on}	td(on)	V _{CC} =300V I _D =10.5A		27	41	ns
	t _r	V _{GS} =10V		37	56	
Turn-off time t _{off}	td(off)	R _{GS} =10Ω		75	113	
	t _f			11	17	
Total Gate Charge	Q _G	V _{CC} =300V		54	81	nC
Gate-Source Charge	Q _{GS}	I _D =21A		16	24	
Gate-Drain Charge	Q _{GD}	V _{GS} =10V		20	30	
Avalanche capability	I _{AV}	L=987μH T _{ch} =25°C	25			A
Diode forward on-voltage	V _{SD}	I _F =21A V _{GS} =0V T _{ch} =25°C		0.98	1.50	V
Reverse recovery time	t _{rr}	I _F =21A V _{GS} =0V		0.7		μs
Reverse recovery charge	Q _{rr}	-di/dt=100A/μs T _{ch} =25°C		10.0		μC

Thermal characteristics

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	R _{th(ch-c)}	channel to case			0.373	°C/W
	R _{th(ch-a)}	channel to ambient			50.0	°C/W

Outline Drawings [mm]



Equivalent circuit schematic

