

PKJ 4000 PI series DC/DC converters, Input 36-75 V, Output 30A/150W	EN/LZT 146 309 R1B February 2006 © Ericsson Power Modules AB
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**Key Features**

- Industry standard Half-brick  
57.9 x 61.0 x 12.7 mm (2.28 x 2.40 x 0.50 in.)
- High efficiency, typ. 91.5 % at full load
- 1500 Vdc input to output isolation
- Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- More than 1.4 million hours MTBF according to TelCordia



**General Characteristics**

- Output over voltage protection
- Input under voltage protection
- Over temperature protection
- Soft start
- Output short-circuit protection
- Remote sense
- Remote control
- Output voltage adjust function
- ISO 9001/14001 certified supplier

**Safety Approvals**



**Design for Environment**



Meets requirements in high-temperature lead-free soldering processes.

**Contents**

General Information	2
Safety Specification	3
Absolute Maximum Ratings	4
Product Program	Ordering No.
1.8V/30A Electrical Specification	PKJ 4518 PIT..... 5
2.5V/20A Electrical Specification	PKJ 4519 PIT..... 8
2.5V/30A Electrical Specification	PKJ 4719 PIT..... 11
3.3V/30A Electrical Specification	PKJ 4910 PIT..... 14
5V/20A Electrical Specification	PKJ 4111 PIT..... 17
5V/30A Electrical Specification	PKJ 4111A PIT ..... 20
12V/6.25A Electrical Specification	PKJ 4713A PIT ..... 23
12V/8.3A Electrical Specification	PKJ 4113 PIT..... 26
12V/12.5A Electrical Specification	PKJ 4113A PIT ..... 29
EMC Specification	32
Operating Information	33
Thermal Consideration	35
Connections	36
Mechanical Information	37
Soldering Information	38
Delivery Package Information	38
Product Qualification Specification	39

PKJ 4000 PI series  
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EN/LZT 146 309 R1B February 2006

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## General Information

### Ordering Information

See Contents for individual product ordering numbers.

Option	Suffix	Ordering No.
Positive Remote Control Logic	P	PKJ 4518 PIPT
Lead length 3.69 mm (0.145 in)	LA	PKJ 4518 PITLA

Note: As an example a positive logic, short pin product would be PKJ 4619 PIPTLA.

### Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature ( $T_A$ ) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses two different methods, Ericsson failure rate data system DependTool and Telcordia SR332.

Predicted MTBF for the series is:

- 3 million hours according to DependTool.
- 1.4 million hours according to Telcordia SR332, issue 1, Black box technique.

The Ericsson failure rate data system is based on field tracking data. The data corresponds to actual failure rates of components used in ICT equipment in temperature controlled environments ( $T_A = -5...+65^\circ\text{C}$ ). Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

### Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead in other applications other than lead in solder, lead in high melting temperature type solder, lead in glass of electronics components, lead in electronic ceramic parts and lead as an alloying element in copper containing up to 4% lead by weight, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in the products:

- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)
- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead in solder for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunication  
(Note: the products are manufactured in lead-free soldering processes and the lead present in the solder is only located in the terminal plating finishes on some components)

### Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6 $\sigma$  (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

### Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

### Limitation of Liability

Ericsson power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

PKJ 4000 PI series  
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## Safety Specification

### General information

Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 "Safety of information technology equipment".

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

### Isolated DC/DC converters

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.

In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage ( $V_{iso}$ ) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).

Leakage current is less than 1  $\mu$ A at nominal input voltage.

### 24 V DC systems

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

### 48 and 60 V DC systems

If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

### Non-isolated DC/DC regulators

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

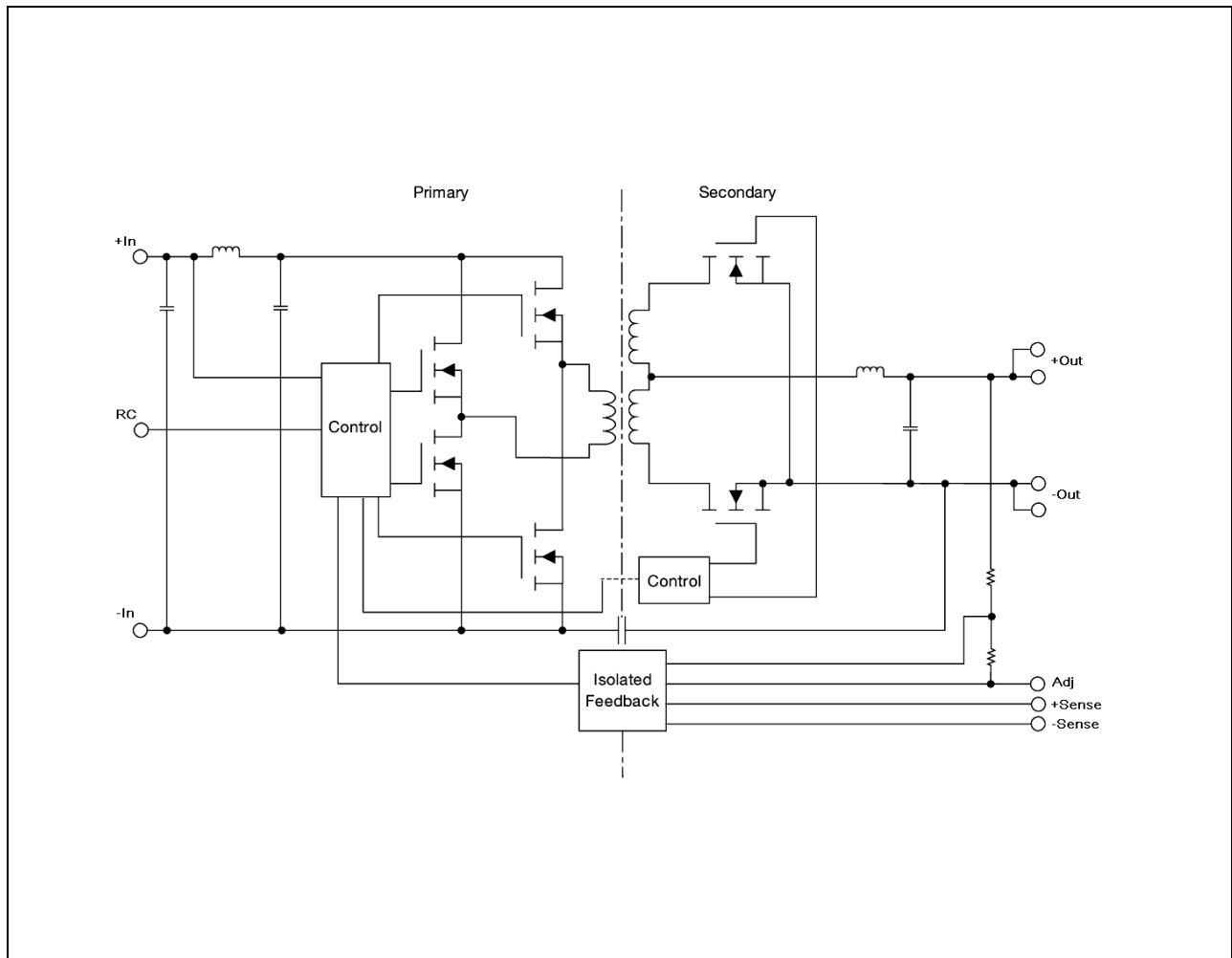
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**Absolute Maximum Ratings**

Characteristics		min	typ	max	Unit
$T_{ref}$	Operating Temperature (see Thermal Consideration section)	-40		+100	°C
$T_S$	Storage temperature	-55		+125	°C
$V_I$	Input voltage	-0.5		+80	V
$V_{iso}$	Isolation voltage (input to output test voltage)			1500	Vdc
$V_{tr}$	Input voltage transient ( $T_p$ 100 ms)			100	V
$V_{RC}$	Remote Control pin voltage (see Operating Information section)	Positive logic option		15	V
		Negative logic option	-0.5	15	V
$V_{adj}$	Adjust pin voltage (see Operating Information section)	-0.5		$2 \times V_{oi}$	V

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

**Fundamental Circuit Diagram**



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**1.8 V/30 A Electrical Specification**
**PKJ 4518 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{Ioff}$	Turn-off input voltage	30	32.5	35	V
$V_{Ion}$	Turn-on input voltage	32	34.5	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		54	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	88		%
		max $I_O$	86		
		50 % of max $I_O$ , $V_I = 48$ V	TBD		
		max $I_O$ , $V_I = 48$ V	TBD		
$P_d$	Power Dissipation	max $I_O$	8.8		W
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	1.9		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.26		W
$f_s$	Switching frequency	0-100 % of max $I_O$	150		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 30$ A	1.77	1.80	1.83	V
$V_O$	Output adjust range	See operating information	1.44		2.00	V
	Output voltage tolerance band	10-100% of max $I_O$	1.71		1.89	V
	Idling voltage	$I_O = 0$ A	1.746		1.900	V
	Line regulation	max $I_O$		2	15	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		2	15	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,	$\pm 100$		mV	
$t_{tr}$	Load transient recovery time		50		$\mu\text{s}$	
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$	TBD		ms	
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )		20	30	ms	
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$	N/A		ms	
		$I_O = 0$ A	N/A		s	
$t_{RC}$	RC start-up time	max $I_O$	20	30	ms	
	RC shutdown fall time (from RC off to 10% of $V_O$ )	max $I_O$	N/A		ms	
		$I_O = 0$ A	N/A		s	
$I_O$	Output current		0	30	A	
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	30.5	35	40	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1	36	40	A	
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$	60	120	mVp-p	
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$	2.5	2.9	V	

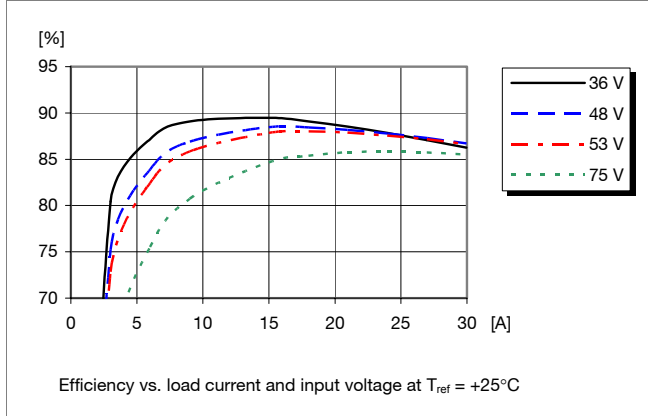
Note 1: See Operating Information section.

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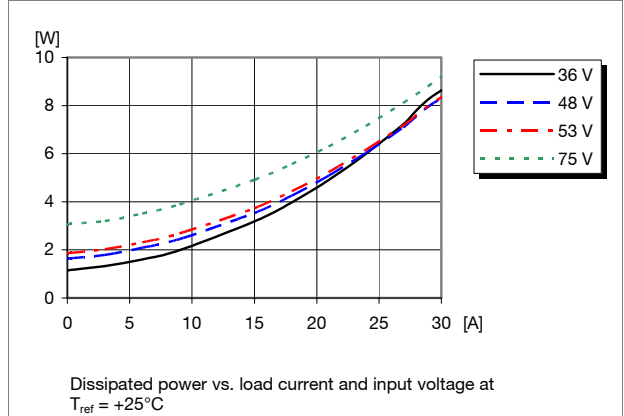
**1.8 V/30 A Typical Characteristics**

**PKJ 4518 PIT**

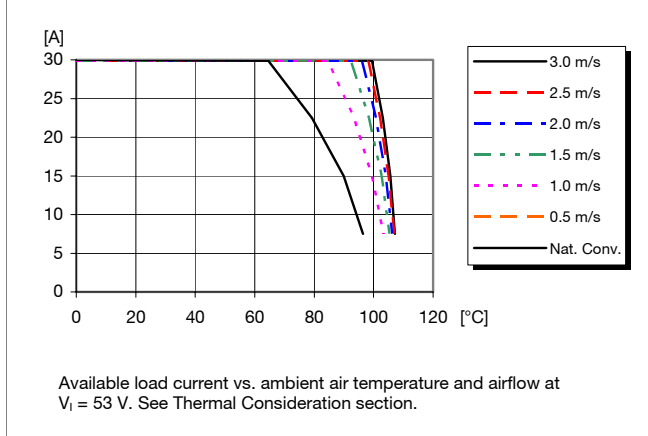
**Efficiency**



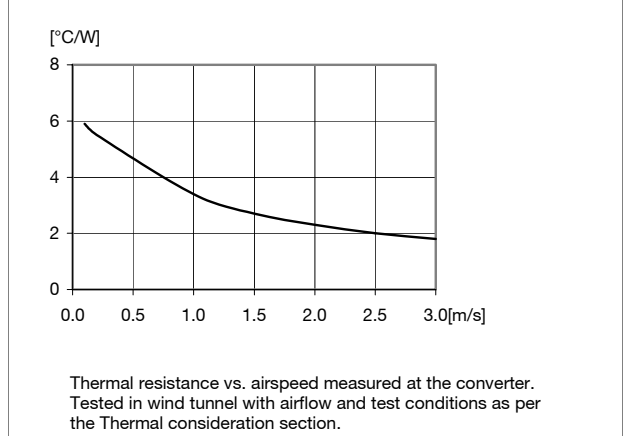
**Power Dissipation**



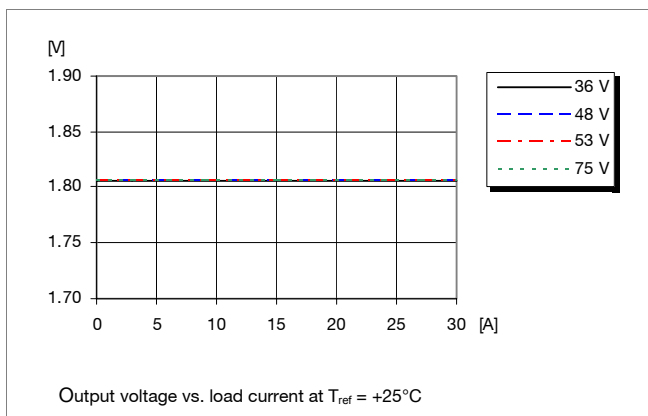
**Output Current Derating**



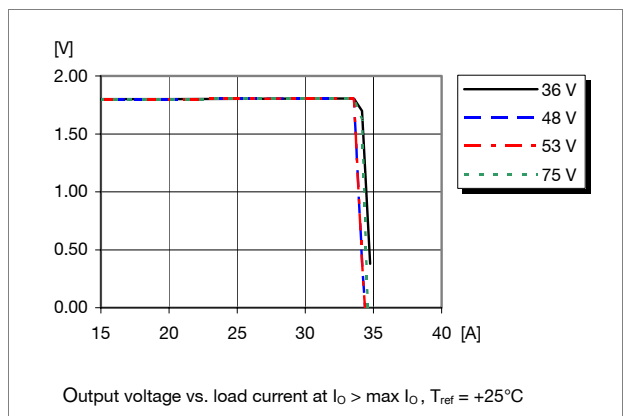
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



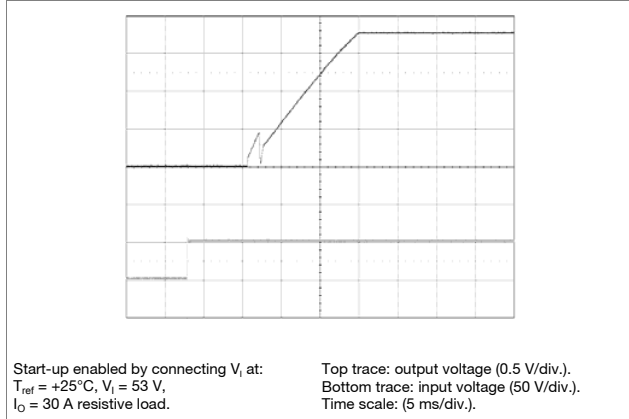
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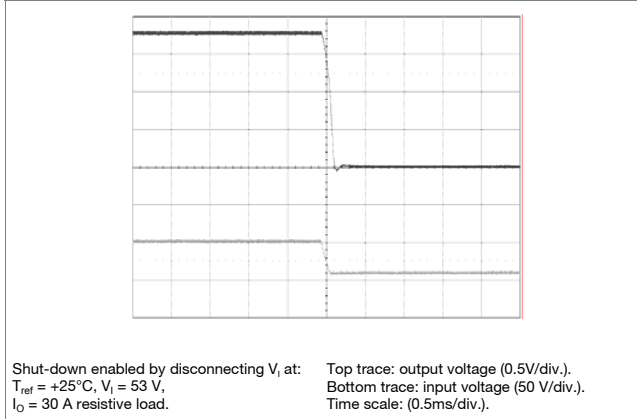
**1.8 V/30 A Typical Characteristics**

**PKJ 4518 PIT**

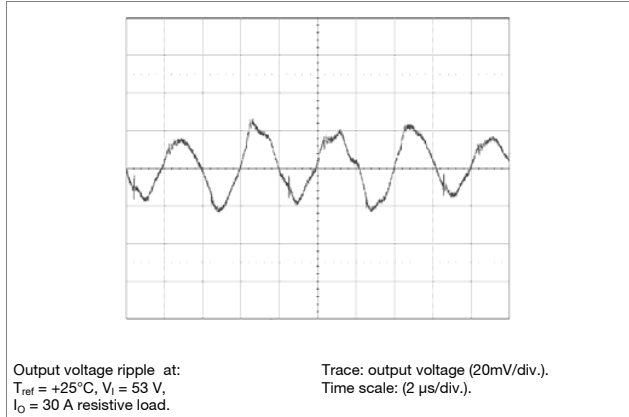
**Start-up**



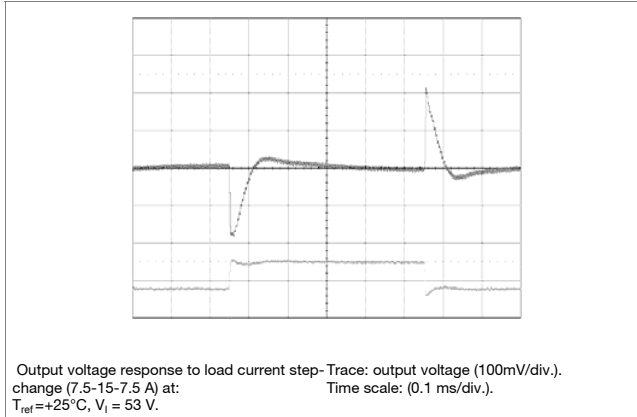
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [(V_o(100+\Delta\%)/(1.225\Delta\%)-(100+2\Delta\%)/\Delta\%)] \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 1.89\text{ Vdc}$*   
 $1.8(100+5)/(1.225 \times 5) - (100+2 \times 5)/5 = 8.8\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (100 / \Delta\% - 2) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 1.71\text{ Vdc}$*   
 $(100/5-2) = 18\text{ k}\Omega$

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**2.5 V/20 A Electrical Specification**
**PKJ 4519 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{Ioff}$	Turn-off input voltage	30	32.5	35	V
$V_{Ion}$	Turn-on input voltage	32	34.5	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		50	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	88		%
		max $I_O$	89		
		50 % of max $I_O$ , $V_I = 48$ V	88		
		max $I_O$ , $V_I = 48$ V	89		
$P_d$	Power Dissipation	max $I_O$	6.4		W
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	1.9		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.28		W
$f_s$	Switching frequency	0-100 % of max $I_O$	150		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 20$ A	2.45	2.5	2.55	V
$V_O$	Output adjust range	See operating information	2		2.75	V
	Output voltage tolerance band	10-100% of max $I_O$	2.42		2.58	V
	Idling voltage	$I_O = 0$ A	2.38		2.63	V
	Line regulation	max $I_O$		2	15	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		2	15	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 100$		mV
$t_{tr}$	Load transient recovery time			50		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$		TBD		ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )			30	55	
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		20	30	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		20	A
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	21	25	31	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		25	31	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	150	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$	3.0	3.3	3.9	V

Note 1: See Operating Information section.

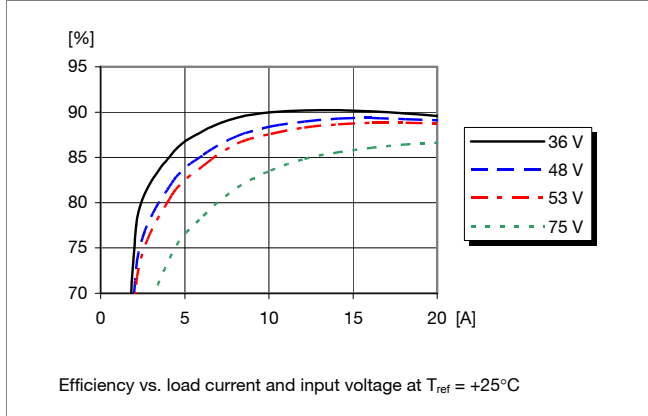


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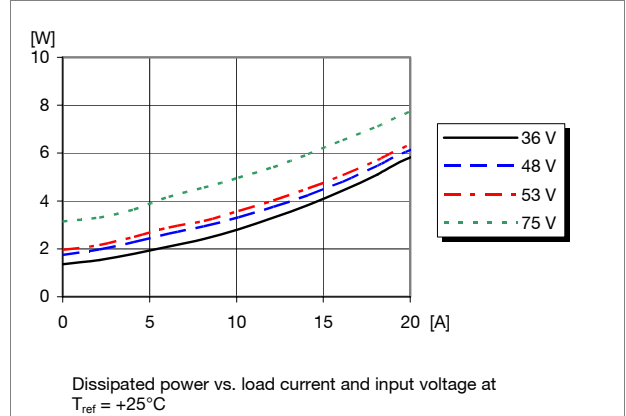
**2.5 V/20 A Typical Characteristics**

**PKJ 4519 PIT**

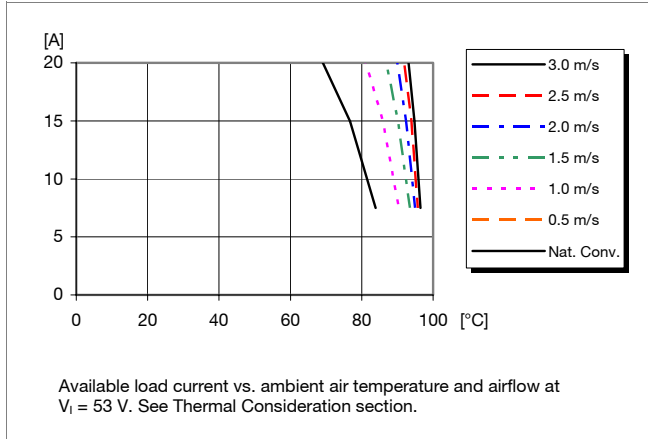
**Efficiency**



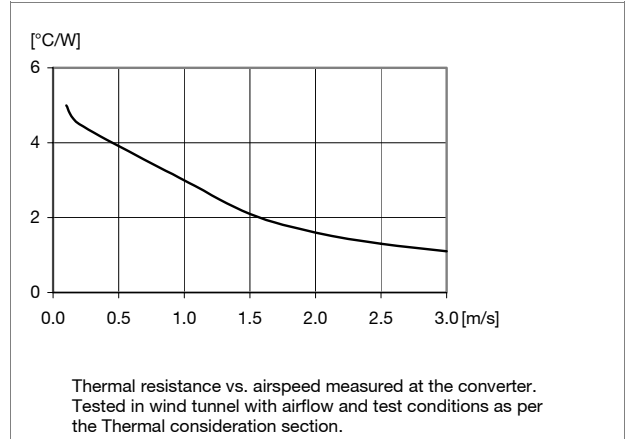
**Power Dissipation**



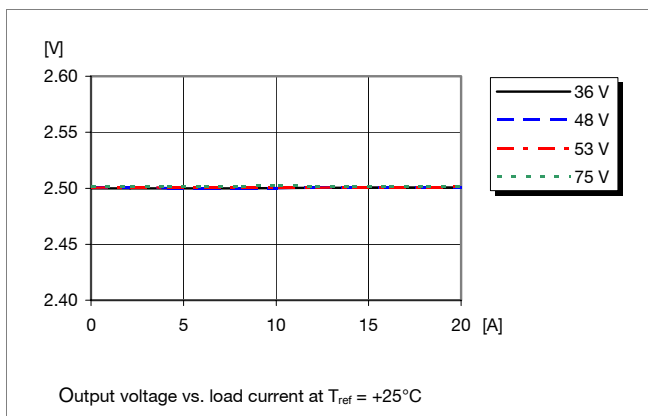
**Output Current Derating**



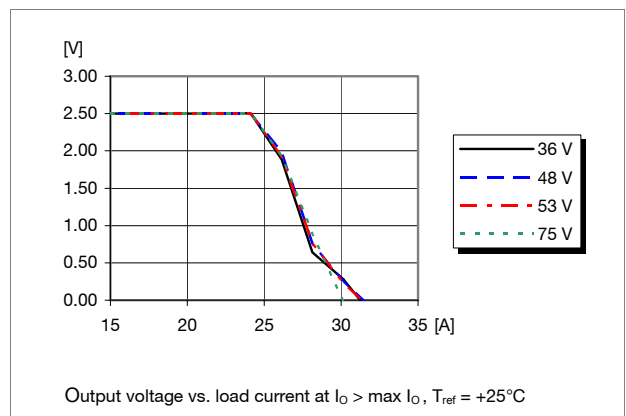
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



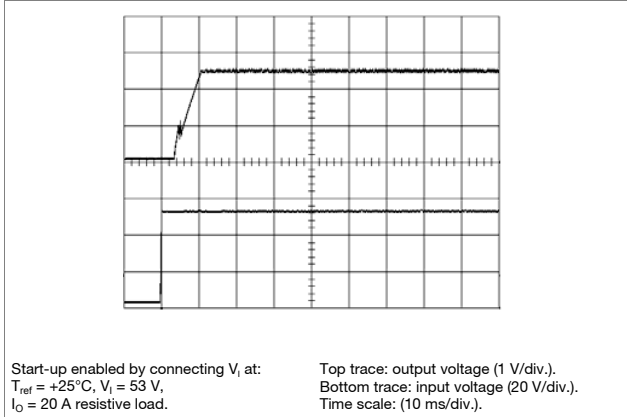
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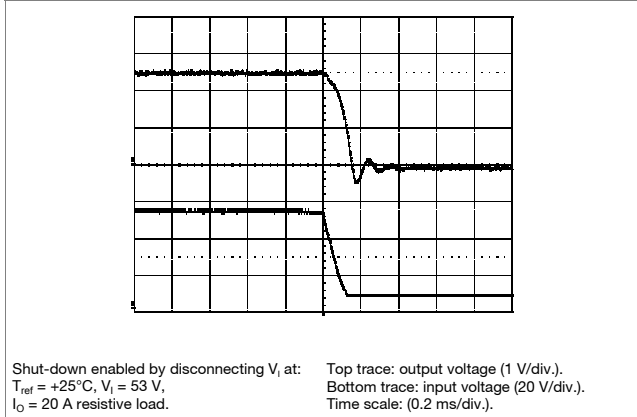
**2.5 V/20 A Typical Characteristics**

**PKJ 4519 PIT**

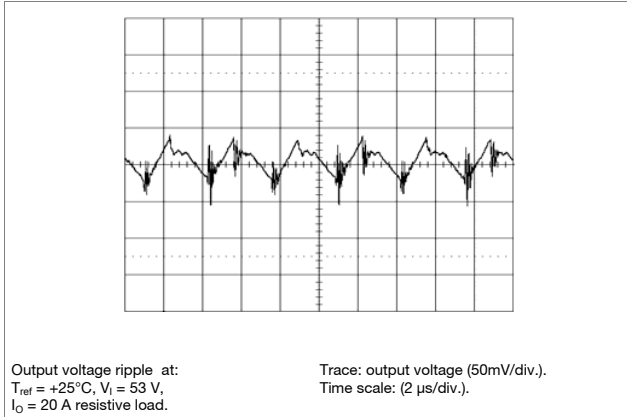
**Start-up**



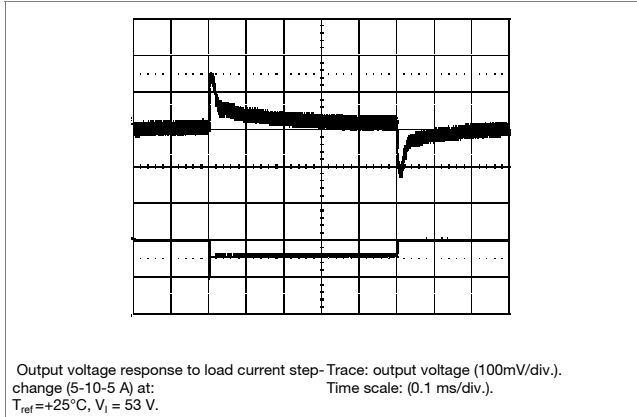
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = (9.7 \cdot V_o + 1.225) / (0.49 \cdot V_o - 1.225) \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 2.625\text{ Vdc}$*   
 $(9.7 \times 2.625 + 1.225) / (0.49 \times 2.625 - 1.225) = 435\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (21 \cdot V_o - 2.5) / (2.5 - V_o) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 2.375\text{ Vdc}$*   
 $(21 \times 2.375 - 2.5) / (2.5 - 2.375) = 379\text{ k}\Omega$

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**2.5 V/30 A Electrical Specification**
**PKJ 4719 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
 Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{Ioff}$	Turn-off input voltage	30	32.5	35	V
$V_{Ion}$	Turn-on input voltage	32	34.5	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		75	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	89		%
		max $I_O$	88		
		50 % of max $I_O$ , $V_I = 48$ V	TBD		
		max $I_O$ , $V_I = 48$ V	TBD		
$P_d$	Power Dissipation	max $I_O$	10		W
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	1.9		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.27		W
$f_s$	Switching frequency	0-100 % of max $I_O$	150		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 30$ A	2.45	2.5	2.55	V
$V_O$	Output adjust range	See operating information	2.00		2.75	V
	Output voltage tolerance band	10-100% of max $I_O$	2.42		2.58	V
	Idling voltage	$I_O = 0$ A	2.38		2.63	V
	Line regulation	max $I_O$		2	15	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		2	15	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,			$\pm 160$	mV
$t_{tr}$	Load transient recovery time				50	$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$			TBD	ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )				35	55
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$			N/A	ms
		$I_O = 0$ A			N/A	s
$t_{RC}$	RC start-up time	max $I_O$	20	30		ms
		max $I_O$			N/A	ms
		$I_O = 0$ A			N/A	s
$I_O$	Output current		0		30	A
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	31	35	41	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		35	41	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	150	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$	3.0	3.3	3.9	V

Note 1: See Operating Information section.

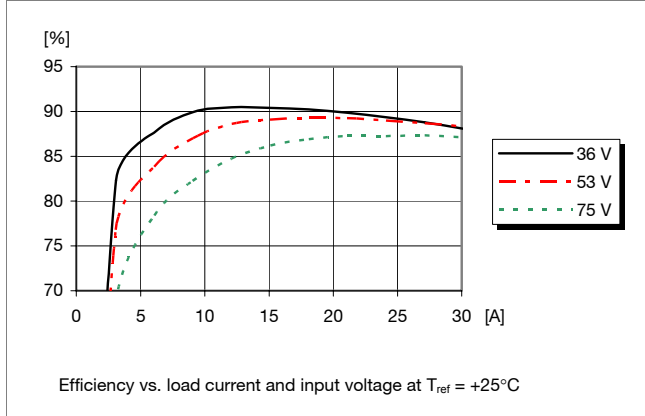
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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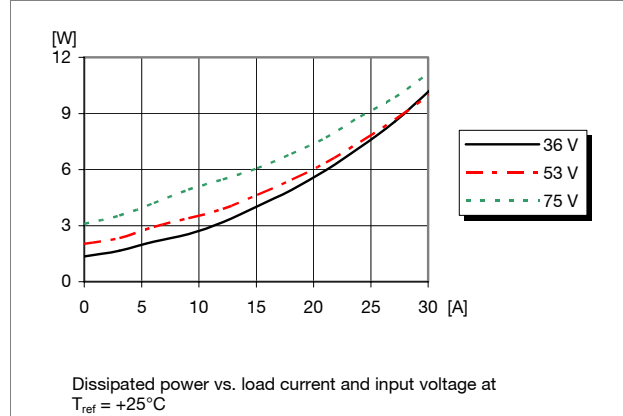
**2.5 V/30 A Typical Characteristics**

**PKJ 4719 PIT**

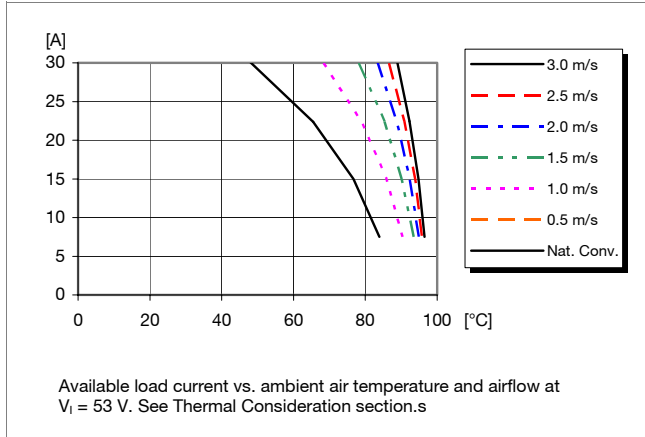
**Efficiency**



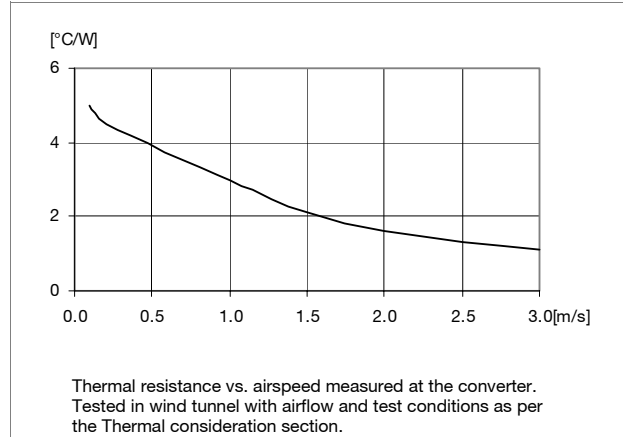
**Power Dissipation**



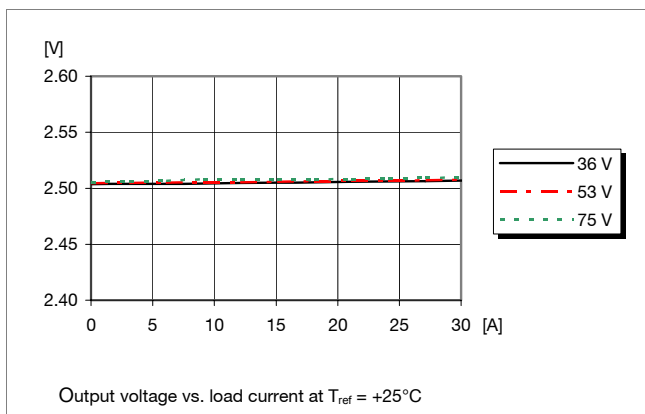
**Output Current Derating**



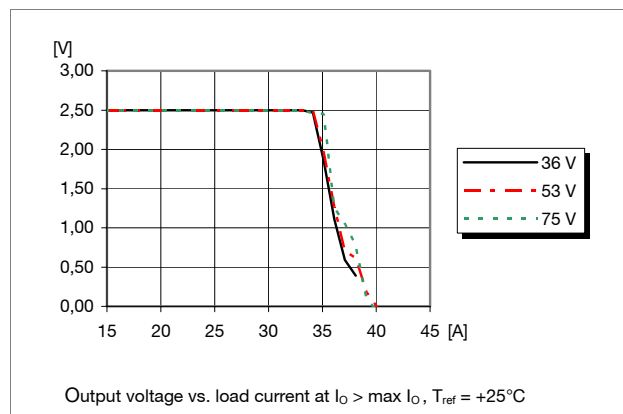
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



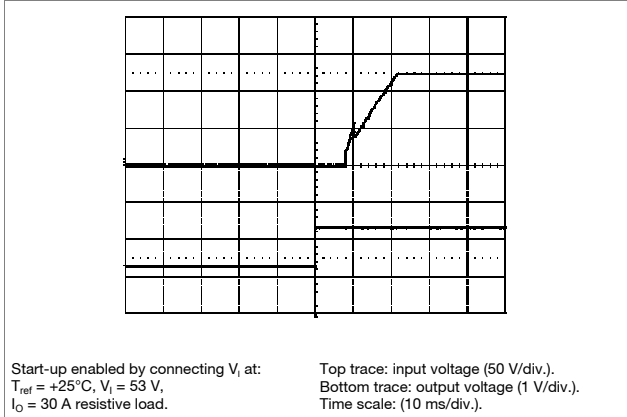
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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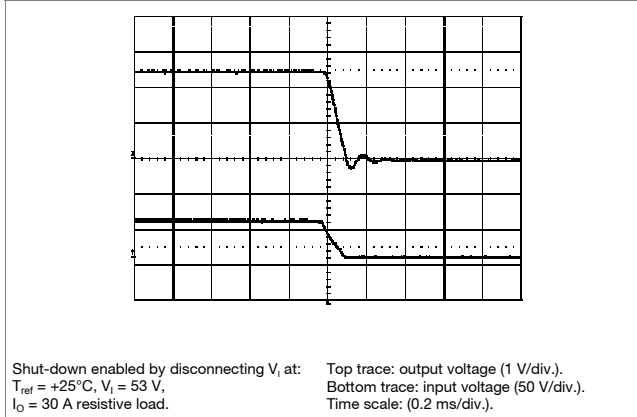
**2.5 V/30 A Typical Characteristics**

PKJ 4719 PIT

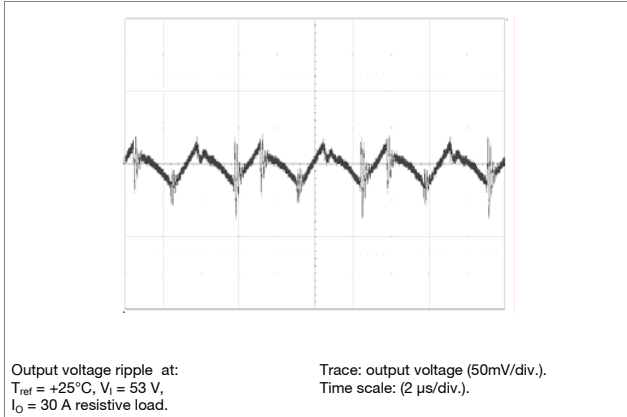
**Start-up**



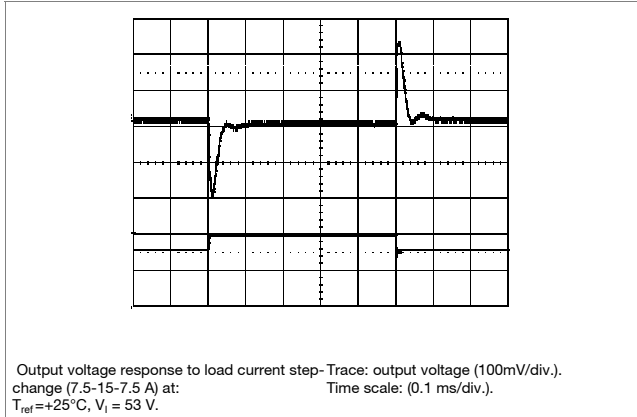
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = (9.7 \cdot V_o + 1.225) / (0.49 \cdot V_o - 1.225) \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 2.625\text{ Vdc}$*   
 $(9.7 \times 2.625 + 1.225) / (0.49 \times 2.625 - 1.225) = 435 \text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (21 \cdot V_o - 2.5) / (2.5 - V_o) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 2.375\text{ Vdc}$*   
 $(21 \times 2.625 - 2.5) / (2.5 - 2.375) = 379 \text{ k}\Omega$

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### 3.3 V/30 A Electrical Specification

**PKJ 4910 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
 Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{Ioff}$	Turn-off input voltage	30	32.5	35	V
$V_{Ion}$	Turn-on input voltage	32	34.5	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		99	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	91		%
		max $I_O$	91		
		50 % of max $I_O$ , $V_I = 48$ V	91		
		max $I_O$ , $V_I = 48$ V	91		
$P_d$	Power Dissipation	max $I_O$	10.5		W
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	2.2		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.27		W
$f_s$	Switching frequency	0-100 % of max $I_O$	150		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 30$ A	3.25	3.3	3.35	V
$V_O$	Output adjust range	See operating information	2.64		3.63	V
	Output voltage tolerance band	10-100% of max $I_O$	3.2		3.4	V
	Idling voltage	$I_O = 0$ A				V
	Line regulation	max $I_O$		1	10	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		1	10	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 180$		mV
$t_{tr}$	Load transient recovery time			50		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$		TBD		ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )			35	60	ms
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		20	30	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		30	A
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	31	35	39	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		35	41	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	150	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$	3.9	4.4	5.0	V

Note 1: See Operating Information section.

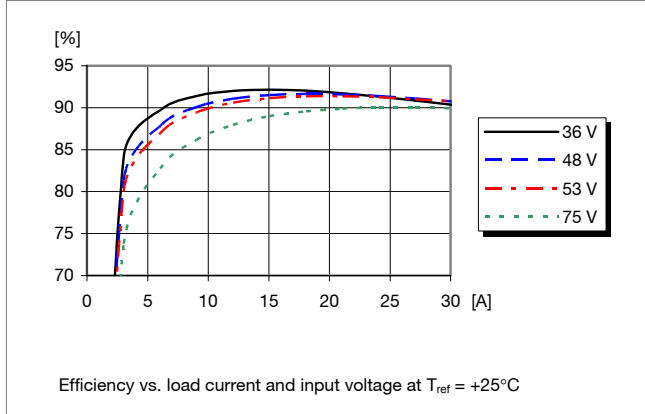
PKJ 4000 PI series  
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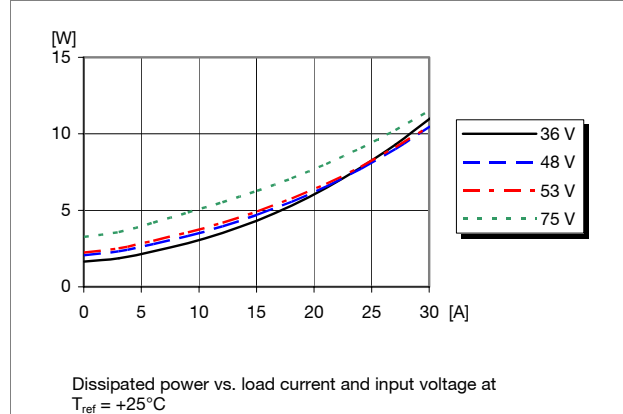
**3.3 V/30 A Typical Characteristics**

**PKJ 4910 PIT**

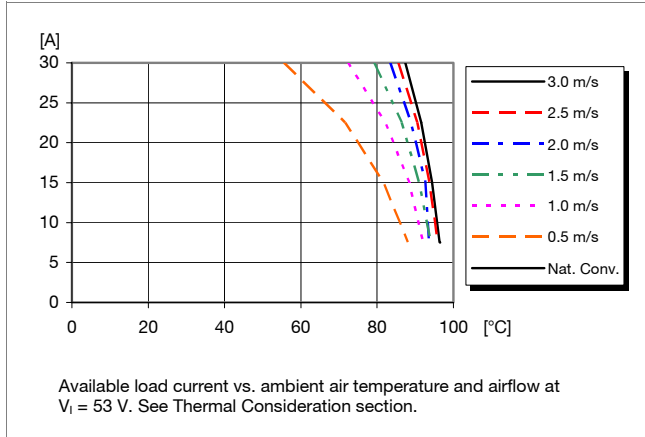
**Efficiency**



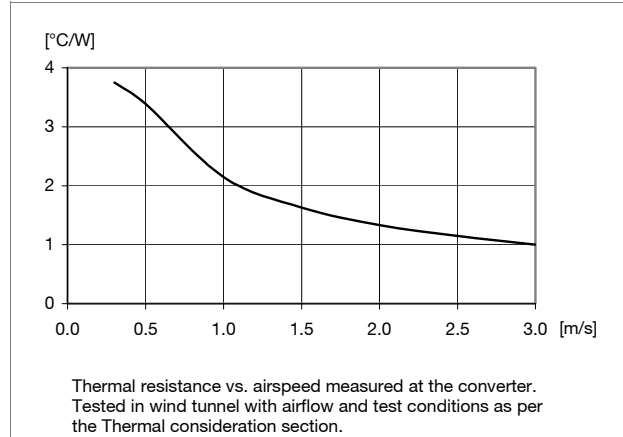
**Power Dissipation**



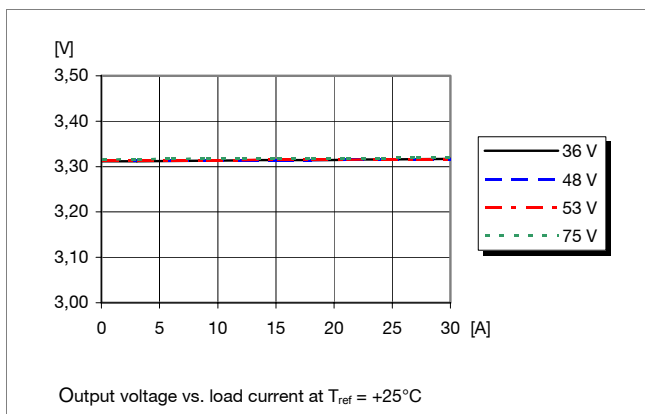
**Output Current Derating**



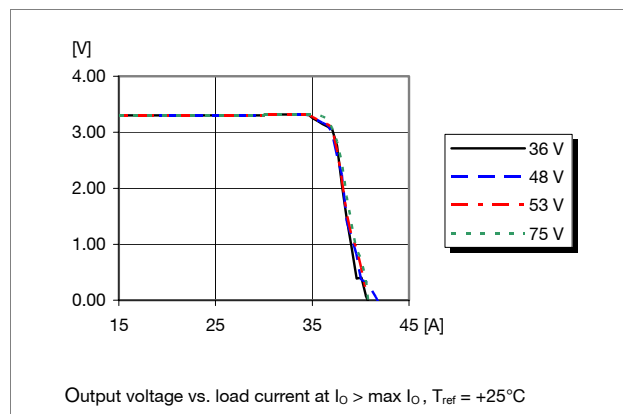
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



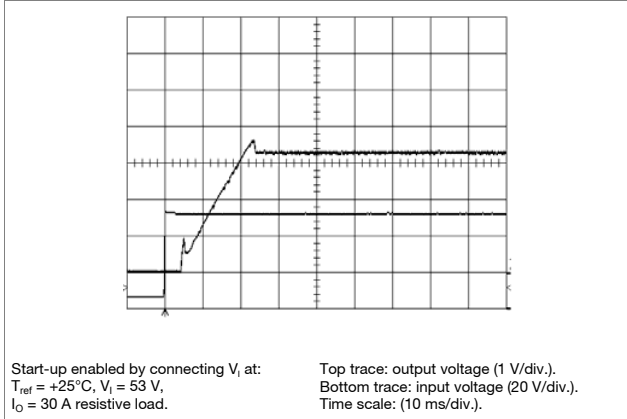
PKJ 4000 PI series  
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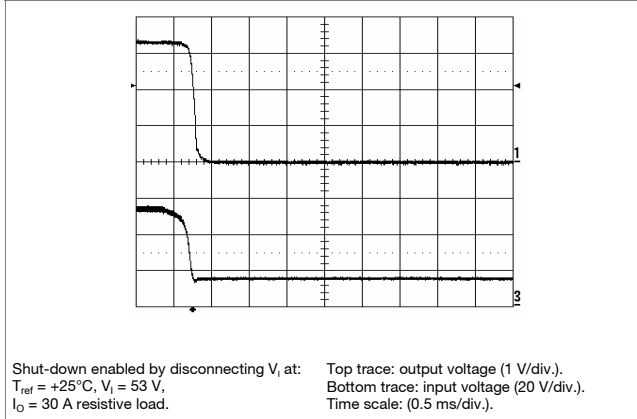
**3.3 V/30 A Typical Characteristics**

**PKJ 4910 PIT**

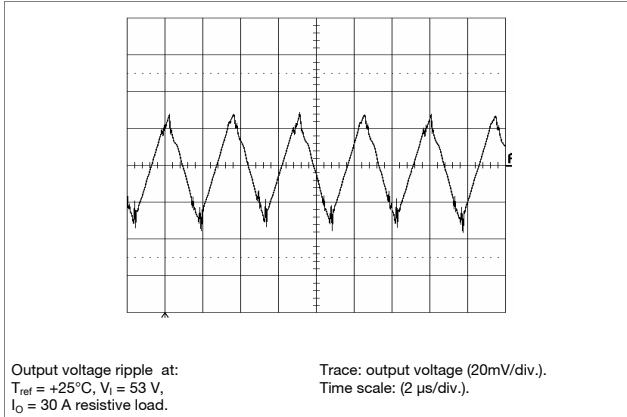
**Start-up**



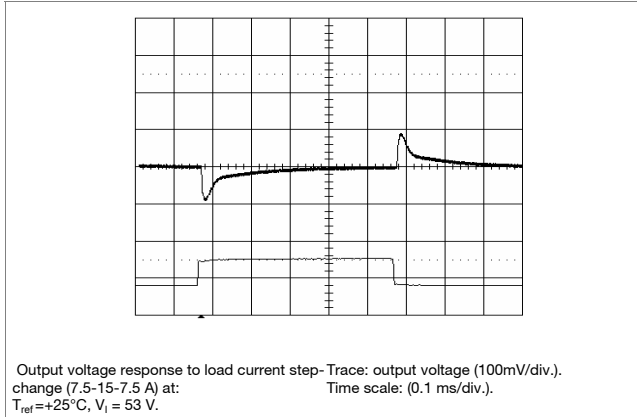
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [(V_o(100+\Delta\%)/(1.225\Delta\%)-(100+2\Delta\%)/\Delta\%)] \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 3.465\text{ Vdc}$*   
 $3.3(100+5)/(1.225 \times 5) - (100+2 \times 5)/5 = 34.6\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (100 / \Delta\% - 2) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 3.135\text{ Vdc}$*   
 $(100/5-2) = 18\text{ k}\Omega$



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**5 V/20 A Electrical Specification**
**PKJ 4111 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{Ioff}$	Turn-off input voltage	30	32.5	35	V
$V_{Ion}$	Turn-on input voltage	32	34.5	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		100	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	90		%
		max $I_O$	90		
		50 % of max $I_O$ , $V_I = 48$ V	90		
		max $I_O$ , $V_I = 48$ V	90		
$P_d$	Power Dissipation	max $I_O$	10.7		W
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	3.2		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.27		W
$f_s$	Switching frequency	0-100 % of max $I_O$	200		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 20$ A	4.9	5.0	5.1	V
$V_O$	Output adjust range	See operating information	4.0		5.5	V
	Output voltage tolerance band	10-100% of max $I_O$	4.85		5.15	V
	Idling voltage	$I_O = 0$ A				V
	Line regulation	max $I_O$		5	20	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		5	20	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 120$		mV
$t_{tr}$	Load transient recovery time			20		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$		TBD		ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )			55	90	ms
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		20	30	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		20	A
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	21	25	32	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		25	31	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	150	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$	5.8	6.1	7	V

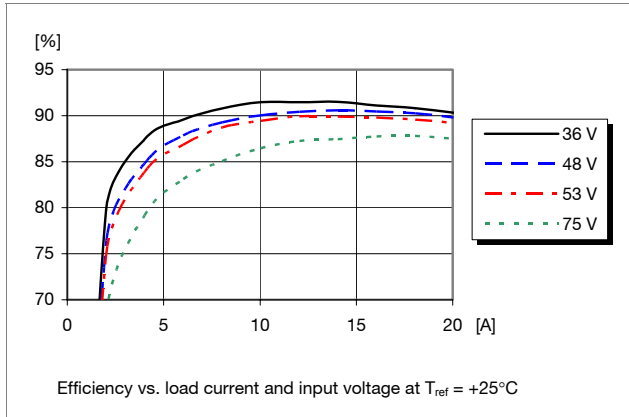
Note 1: See Operating Information section.

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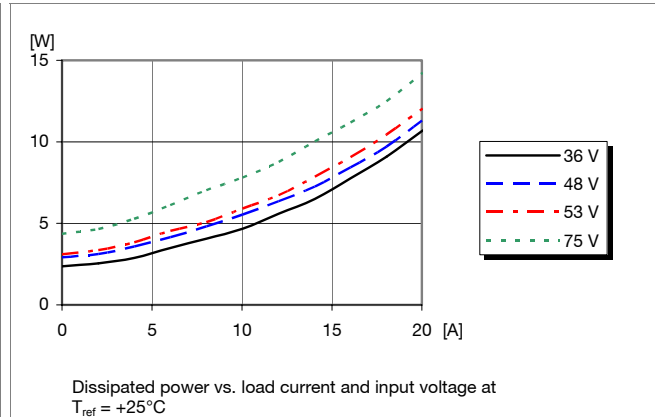
**5 V/20 A Typical Characteristics**

**PKJ 4111 PIT**

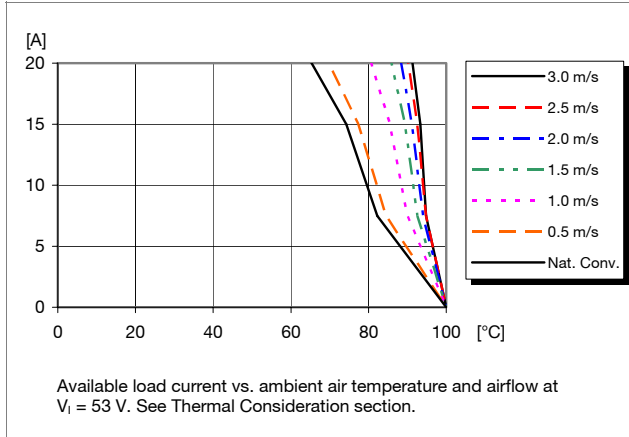
**Efficiency**



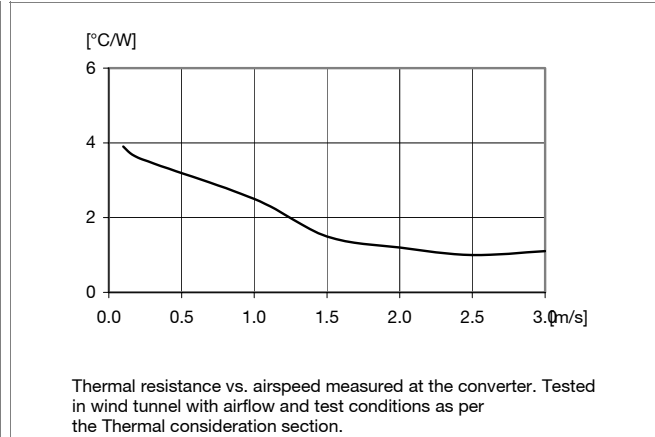
**Power Dissipation**



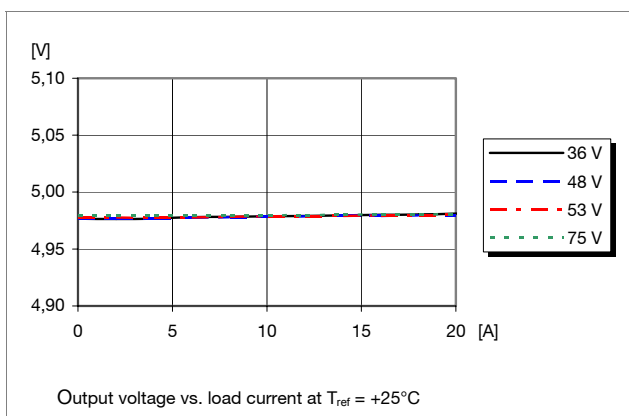
**Output Current Derating**



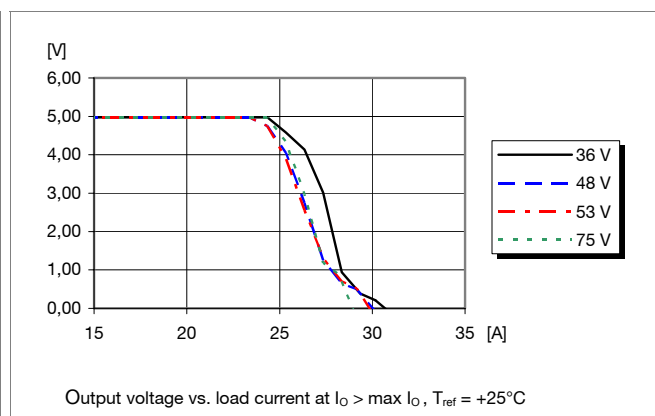
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



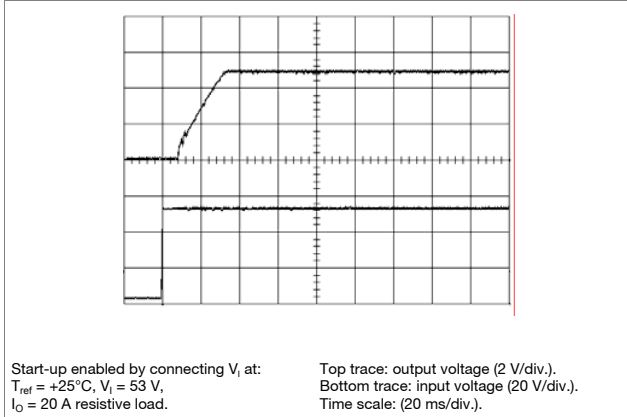
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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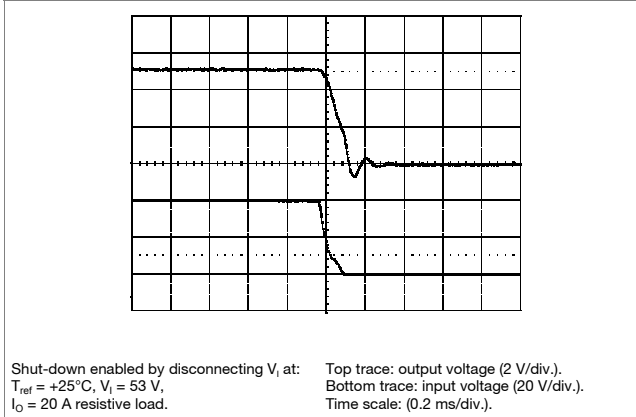
**5 V/20 A Typical Characteristics**

PKJ 4111 PIT

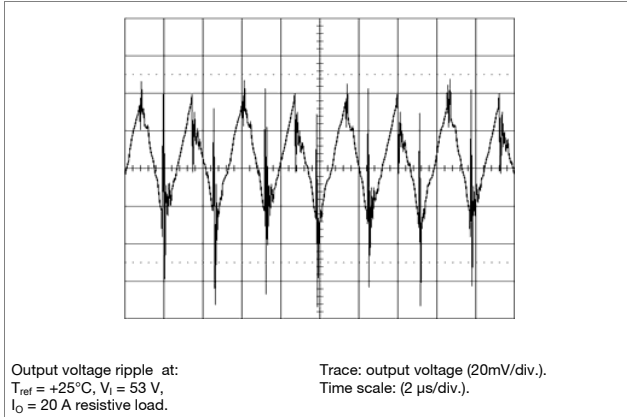
**Start-up**



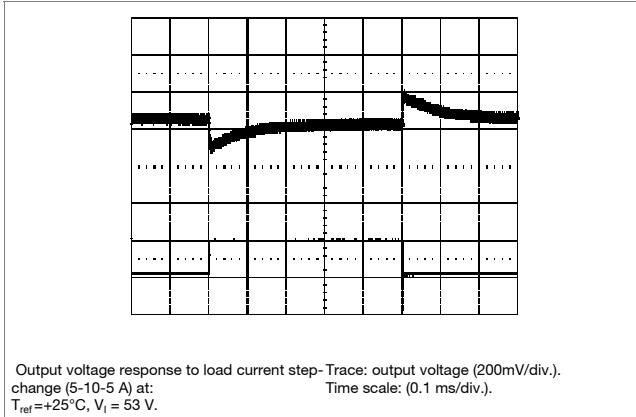
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [7.286 \cdot V_o + 1.225] / (0.2467 \cdot V_o - 1.225) \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 5.25\text{ Vdc}$*

$$[7.286 \cdot 5.25 + 1.225] / (0.2467 \cdot 5.25 - 1.225) = 563 \text{ k}\Omega$$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (11 \cdot V_o - 4.965) / (4.965 - V_o) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 4.75\text{ Vdc}$*

$$(11 \cdot 4.75 - 4.965) / (4.965 - 4.75) = 220 \text{ k}\Omega$$

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**5 V/30 A Electrical Specification**
**PKJ 4111A PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{Ioff}$	Turn-off input voltage	30	32.5	35	V
$V_{Ion}$	Turn-on input voltage	32	34.5	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		150	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	93		%
		max $I_O$	92		
		50 % of max $I_O$ , $V_I = 48$ V	93		
		max $I_O$ , $V_I = 48$ V	92		
$P_d$	Power Dissipation	max $I_O$	12.4		W
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	2.8		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.25		W
$f_s$	Switching frequency	0-100 % of max $I_O$	200		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 30$ A	4.9	5.0	5.1	V
$V_O$	Output adjust range	See operating information	4.0		5.5	V
	Output voltage tolerance band	10-100% of max $I_O$	4.85		5.15	V
	Idling voltage	$I_O = 0$ A				V
	Line regulation	max $I_O$		5	20	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		5	20	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 200$		mV
$t_{tr}$	Load transient recovery time			50		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$		TBD		ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )			55	90	ms
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		20	30	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		30	A
$I_{lim}$	Current limit threshold	$T_{ref} < \text{max } T_{ref}$	31	35	42	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		35	41	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	150	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$	5.8	6.1	7	V

Note 1: See Operating Information section.

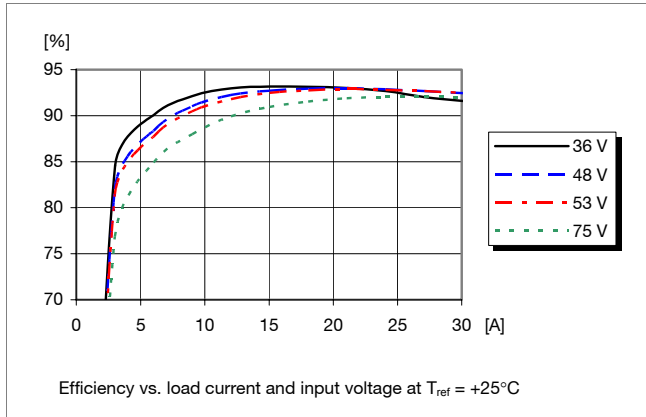
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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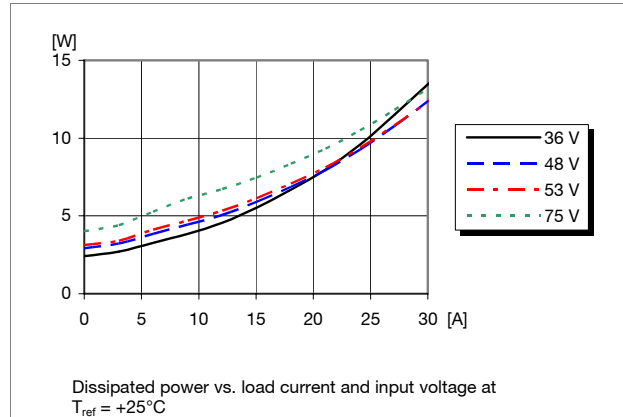
**5 V/30 A Typical Characteristics**

**PKJ 4111A PIT**

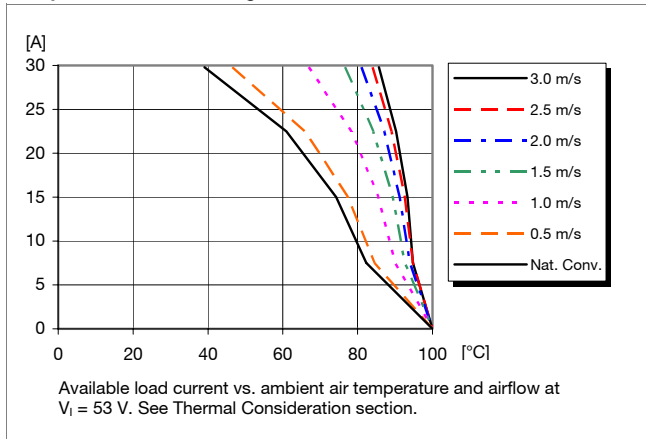
**Efficiency**



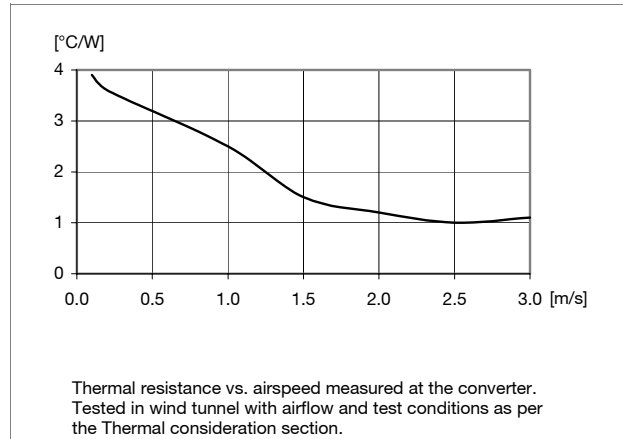
**Power Dissipation**



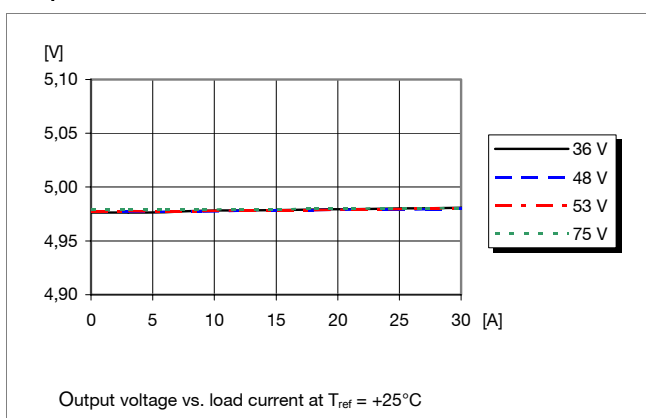
**Output Current Derating**



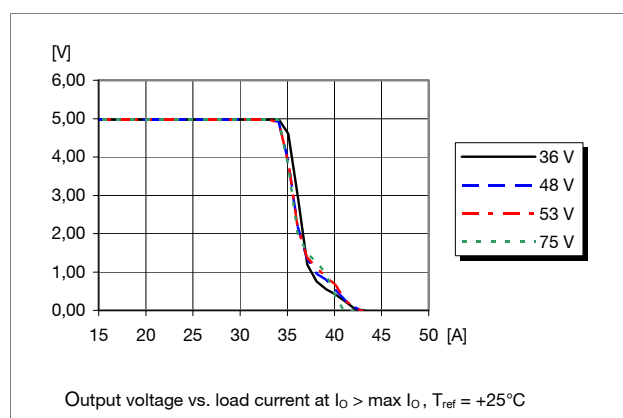
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



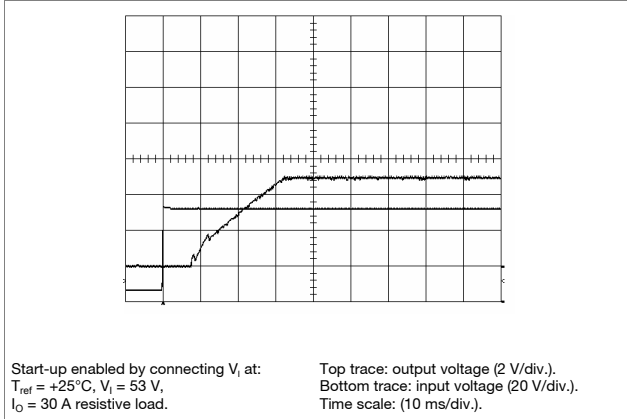
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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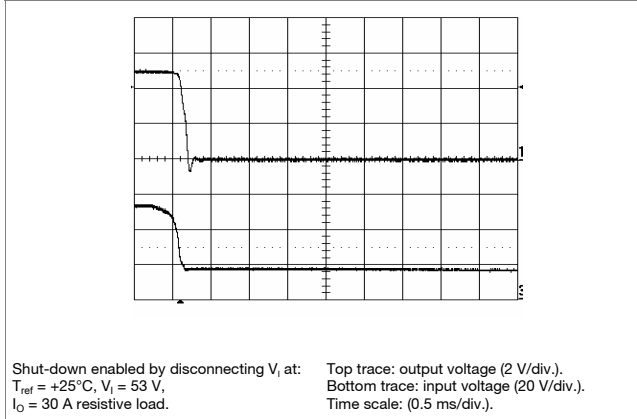
**5 V/30 A Typical Characteristics**

PKJ 4111A PIT

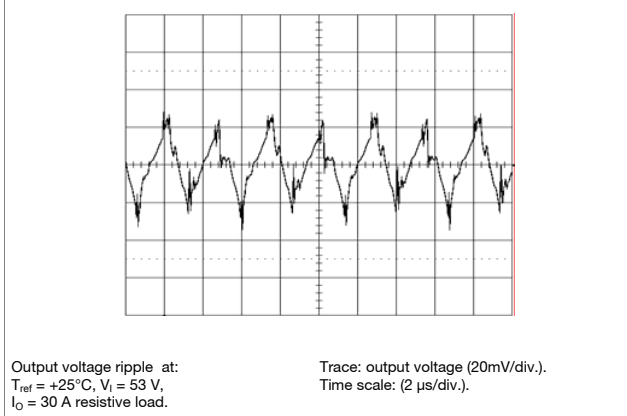
**Start-up**



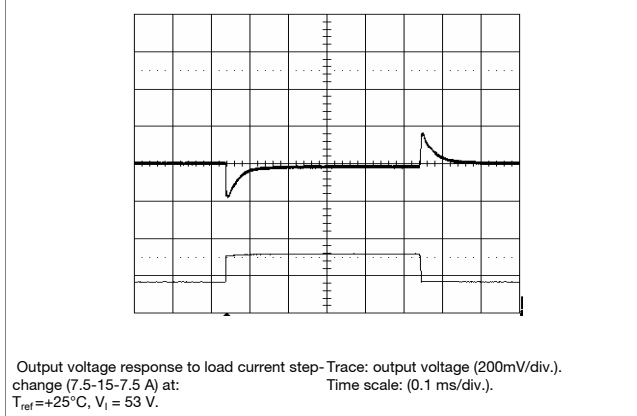
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [7.286 \cdot V_o + 1.225] / (0.2467 \cdot V_o - 1.225) \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 5.25\text{ Vdc}$*

$$[7.286 \cdot 5.25 + 1.225] / (0.2467 \cdot 5.25 - 1.225) = 563 \text{ k}\Omega$$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (11 \cdot V_o - 4.965) / (4.965 - V_o) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 4.75\text{ Vdc}$*

$$(11 \cdot 4.75 - 4.965) / (4.965 - 4.75) = 220 \text{ k}\Omega$$

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**12 V/6.25 A Electrical Specification**
**PKJ 4713 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
 Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit
$V_I$	Input voltage range	36		75	V
$V_{loff}$	Turn-off input voltage	31	33		V
$V_{lon}$	Turn-on input voltage		34	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$
$P_O$	Output power	0		75	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		53	dB
$\eta$	Efficiency	50 % of max $I_O$	91		%
		max $I_O$	93		
		50 % of max $I_O$ , $V_I = 48$ V	91		
		max $I_O$ , $V_I = 48$ V	93		
$P_d$	Power Dissipation	max $I_O$	5.6		W
$P_{li}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V	2.9		W
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)	0.05	2.5	W
$f_s$	Switching frequency	0-100 % of max $I_O$	300		kHz

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 6.25$ A	11.8	12.0	12.2	V
$V_O$	Output adjust range	See operating information	9.6		13.3	V
	Output voltage tolerance band	10-100% of max $I_O$	11.64		12.36	V
	Idling voltage	$I_O = 0$ A				V
	Line regulation	max $I_O$		3	10	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		3	10	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 100$		mV
$t_{tr}$	Load transient recovery time			150		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$		15	30	ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )			30	60	ms
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		30	60	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		6.25	A
$I_{lim}$	Current limit threshold	$T_{ref} < \text{max } T_{ref}$	7.0	8.0	9.0	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		10	12	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	100	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$		14.9	15.5	V

Note 1: See Operating Information section.

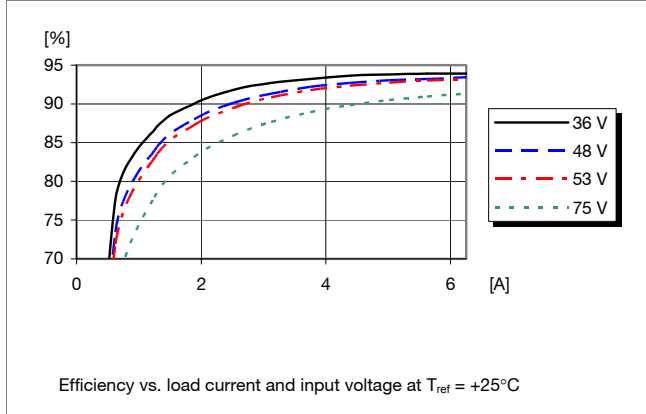
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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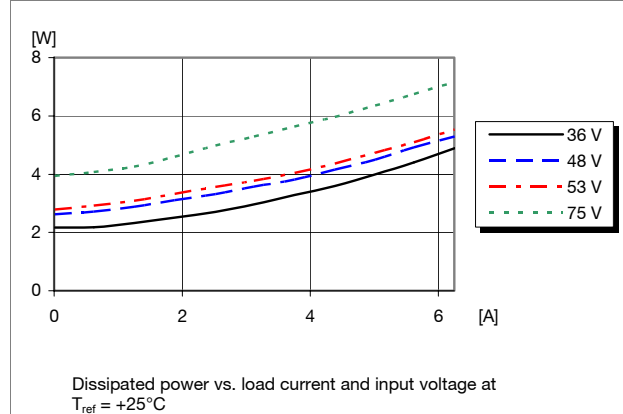
**12 V/6.25 A Typical Characteristics**

**PKJ 4713 PIT**

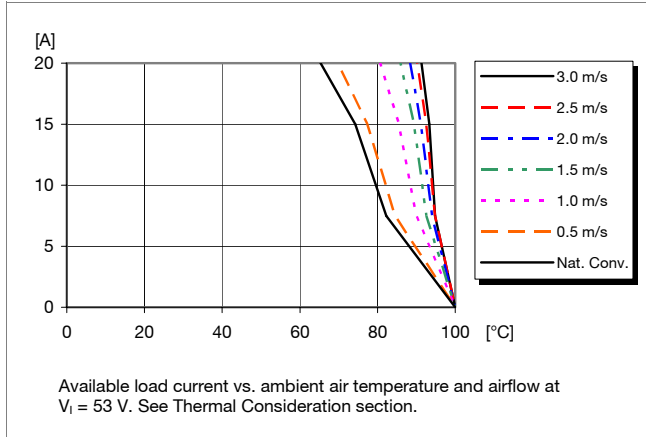
**Efficiency**



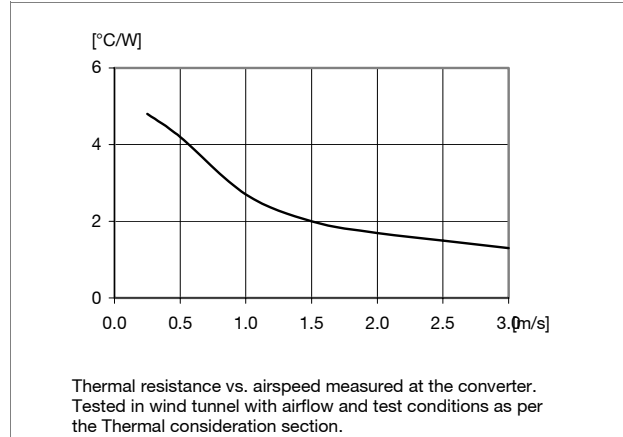
**Power Dissipation**



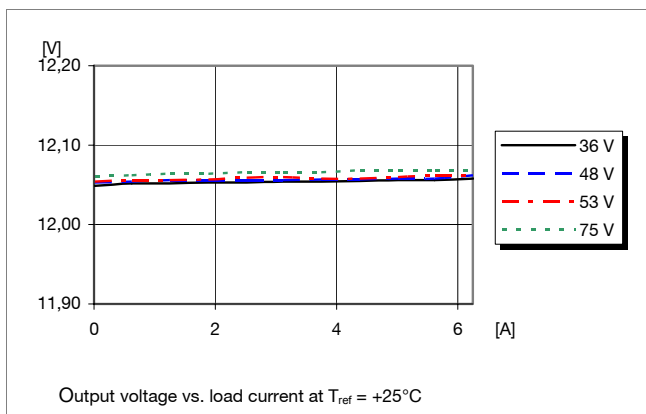
**Output Current Derating**



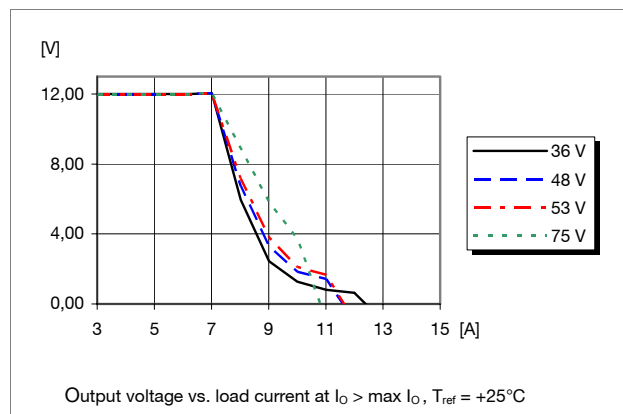
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**





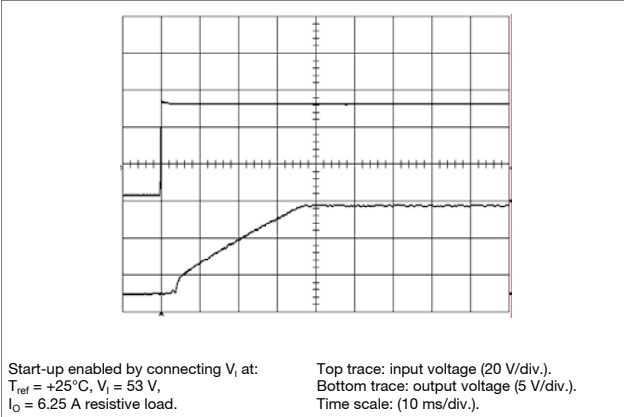
PKJ 4000 PI series  
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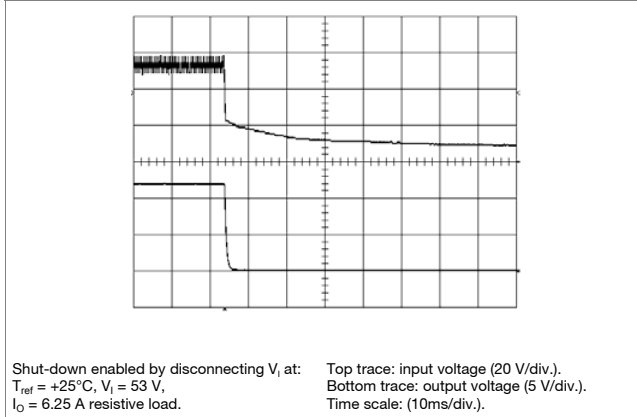
**12 V/6.25 A Typical Characteristics**

**PKJ 4713 PIT**

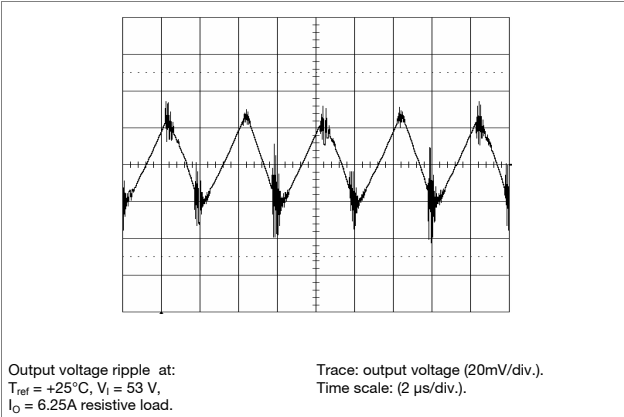
**Start-up**



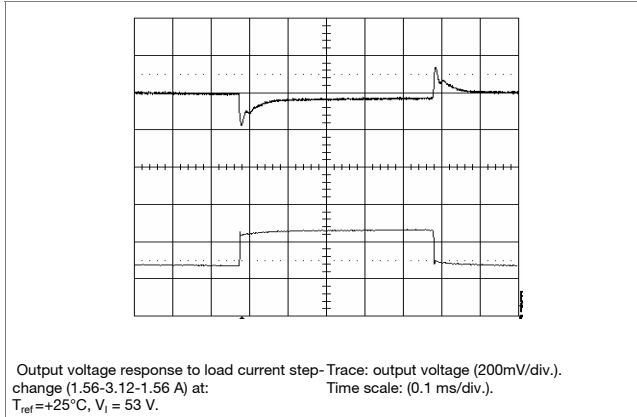
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [(V_o(100+\Delta\%)/(1.225\Delta\%)-(100+2\Delta\%)/\Delta\%)]\text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 12.6\text{ Vdc}$*   
 $12(100+5)/(1.225 \times 5) - (100+2 \times 5)/5 = 184.7\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (100 / \Delta\% - 2)\text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 11.4\text{ Vdc}$*   
 $(100/5 - 2) = 18\text{ k}\Omega$

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**12 V/8.3 A Electrical Specification**
**PKJ 4113 PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
 Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit	
$V_I$	Input voltage range	36		75	V	
$V_{Ioff}$	Turn-off input voltage	Decreasing input voltage	31	33	V	
$V_{Ion}$	Turn-on input voltage	Increasing input voltage		34	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$	
$P_O$	Output power	Output voltage initial setting	0	100	W	
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sinewave, 1 Vp-p		53	dB	
$\eta$	Efficiency	50 % of max $I_O$		92	%	
		max $I_O$		93		
		50 % of max $I_O$ , $V_I = 48$ V		92		
		max $I_O$ , $V_I = 48$ V		93		
$P_d$	Power Dissipation	max $I_O$		7.5	W	
$P_{II}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V		2.9	W	
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)		0.05	2.5	W
$f_s$	Switching frequency	0-100 % of max $I_O$		300	kHz	

$V_{O1}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 8.3$ A	11.8	12.0	12.2	V
$V_O$	Output adjust range	See operating information	9.6		13.3	V
	Output voltage tolerance band	10-100% of max $I_O$	11.64		12.36	V
	Idling voltage	$I_O = 0$ A				V
	Line regulation	max $I_O$		3	10	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		3	10	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 100$		mV
$t_{tr}$	Load transient recovery time			150		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{O1}$ )	10-100% of max $I_O$		15	30	ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{O1}$ )			30	60	ms
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		20	30	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		8.3	A
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	9.0	10.5	14	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		12	14	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{O1}$		75	100	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$		14.9	15.5	V

Note 1: See Operating Information section.

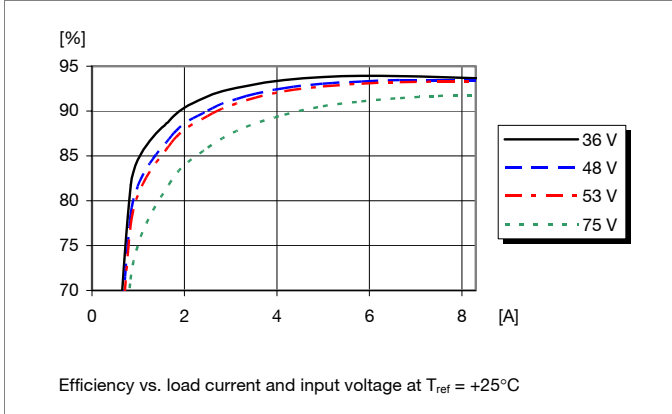
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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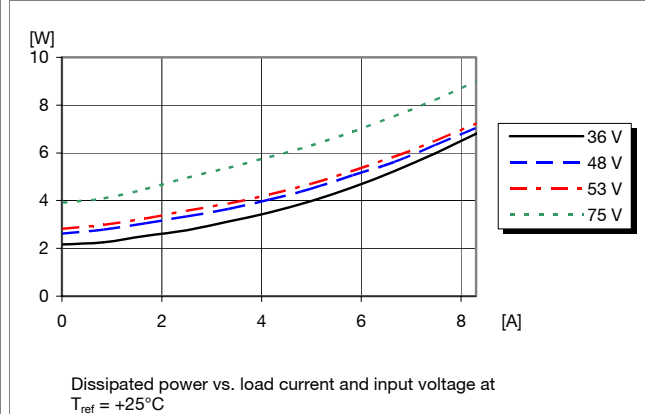
**12 V/8.3 A Typical Characteristics**

**PKJ 4113 PIT**

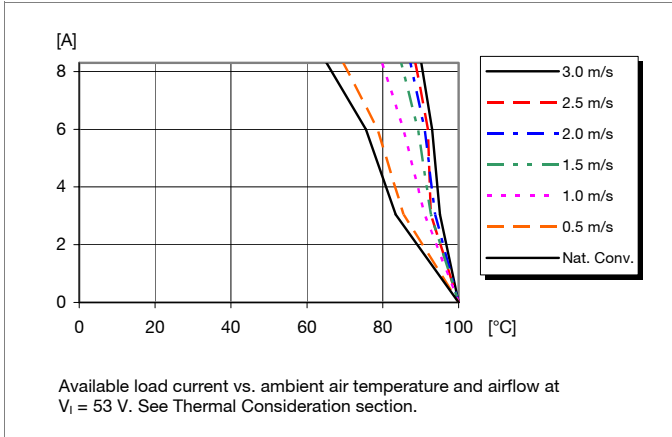
**Efficiency**



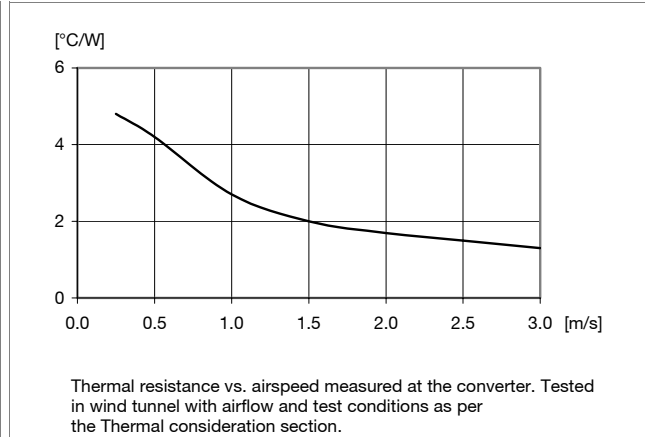
**Power Dissipation**



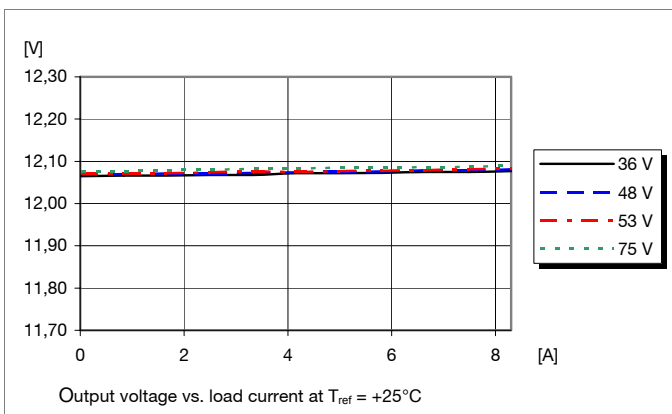
**Output Current Derating**



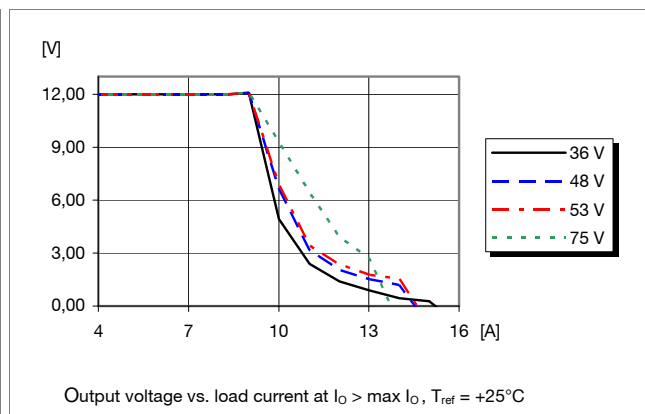
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



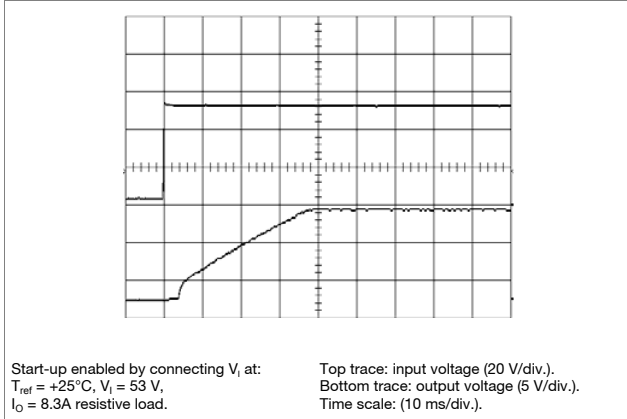
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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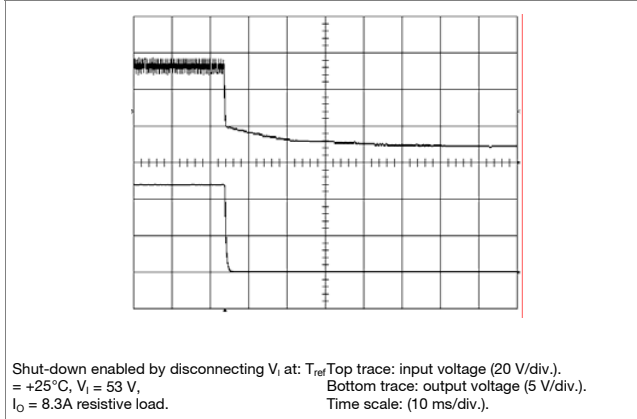
**12 V/8.3 A Typical Characteristics**

**PKJ 4113 PIT**

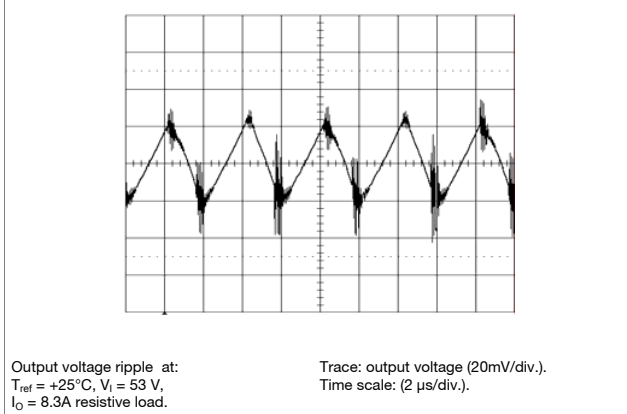
**Start-up**



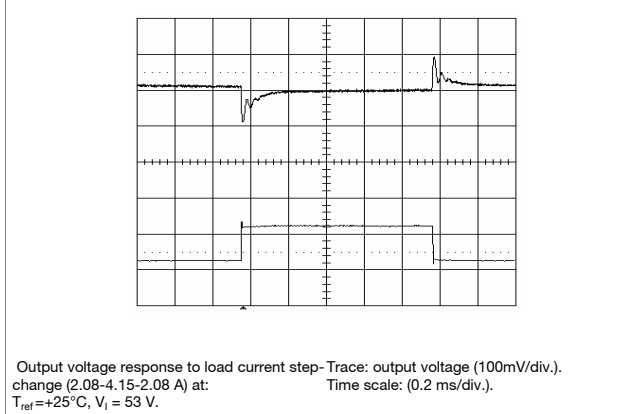
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [(V_o(100+\Delta\%)/(1.225\Delta\%)-(100+2\Delta\%)/\Delta\%)] \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 12.6\text{ Vdc}$*   
 $12(100+5)/(1.225 \times 5) - (100+2 \times 5)/5 = 184.7\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (100 / \Delta\% - 2) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 11.4\text{ Vdc}$*   
 $(100/5-2) = 18\text{ k}\Omega$

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**12 V/12.5 A Electrical Specification**
**PKJ 4113A PIT**

$T_{ref} = -40$  to  $+100^{\circ}\text{C}$ ,  $V_I = 36$  to  $75$  V, sense pins connected to output pins unless otherwise specified under Conditions.  
Typical values given at:  $T_{ref} = +25^{\circ}\text{C}$ ,  $V_I = 53$  V, max  $I_O$ , unless otherwise specified under Conditions.

Characteristics	Conditions	min	typ	max	Unit	
$V_I$	Input voltage range	36		75	V	
$V_{loff}$	Turn-off input voltage	Decreasing input voltage	31	33	V	
$V_{lon}$	Turn-on input voltage	Increasing input voltage		34	36	V
$C_I$	Internal input capacitance		2.8		$\mu\text{F}$	
$P_O$	Output power	Output voltage initial setting	0	150	W	
SVR	Supply voltage rejection (ac)	$f = 100$ Hz sinewave, 1 Vp-p		53	dB	
$\eta$	Efficiency	50 % of max $I_O$		92	%	
		max $I_O$		93		
		50 % of max $I_O$ , $V_I = 48$ V		92		
		max $I_O$ , $V_I = 48$ V		93		
$P_d$	Power Dissipation	max $I_O$		11.3	W	
$P_{li}$	Input idling power	$I_O = 0$ A, $V_I = 53$ V		2.8	W	
$P_{RC}$	Input standby power	$V_I = 53$ V (turned off with RC)		0.05	2.5	W
$f_s$	Switching frequency	0-100 % of max $I_O$		300	kHz	

$V_{Oi}$	Output voltage initial setting and accuracy	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, $I_O = 12.5$ A	11.8	12	12.2	V
$V_O$	Output adjust range	See operating information	9.6		13.3	V
	Output voltage tolerance band	10-100% of max $I_O$	11.64		12.36	V
	Idling voltage	$I_O = 0$ A				V
	Line regulation	max $I_O$		3	10	mV
	Load regulation	$V_I = 53$ V, 0-100% of max $I_O$		3	10	mV
$V_{tr}$	Load transient voltage deviation	$V_I = 53$ V, Load step 25-50-25 % of max $I_O$ , $di/dt = 1$ A/ $\mu\text{s}$ ,		$\pm 100$		mV
$t_{tr}$	Load transient recovery time			150		$\mu\text{s}$
$t_r$	Ramp-up time (from 10-90 % of $V_{Oi}$ )	10-100% of max $I_O$		15	30	ms
$t_s$	Start-up time (from $V_I$ connection to 90% of $V_{Oi}$ )			30	60	ms
$t_f$	$V_{in}$ shutdown fall time (from $V_I$ off to 10% of $V_O$ )	max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$t_{RC}$	RC start-up time	max $I_O$		20	30	ms
		max $I_O$		N/A		ms
		$I_O = 0$ A		N/A		s
$I_O$	Output current		0		12.5	A
$I_{lim}$	Current limit threshold	$T_{ref} < \max T_{ref}$	13.5	14.5	16.0	A
$I_{sc}$	Short circuit current	$T_{ref} = 25^{\circ}\text{C}$ , see Note 1		16.5	18	A
$V_{Oac}$	Output ripple & noise	See ripple & noise section, max $I_O$ , $V_{Oi}$		75	100	mVp-p
OVP	Over voltage protection	$T_{ref} = +25^{\circ}\text{C}$ , $V_I = 53$ V, 0-100% of max $I_O$		14.9	15.5	V

Note 1: See Operating Information section.

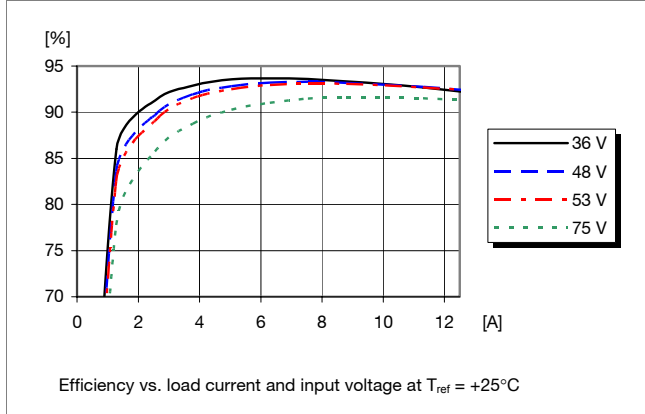
PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

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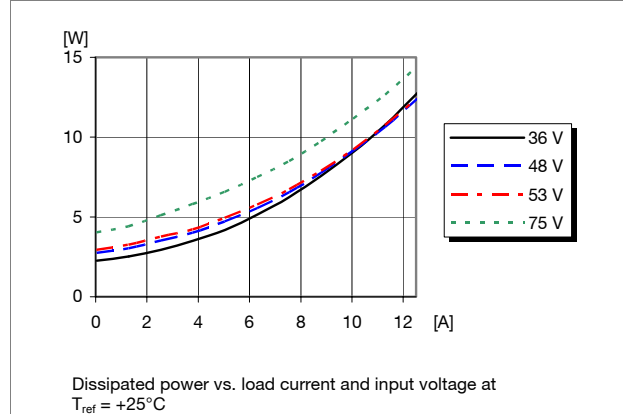
**12 V/12.5 A Typical Characteristics**

**PKJ 4113A PIT**

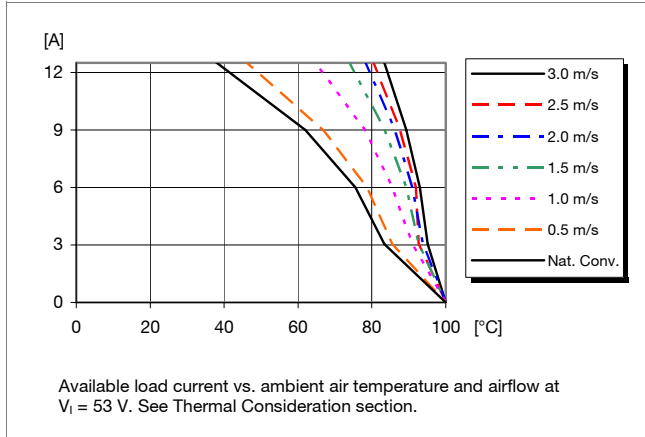
**Efficiency**



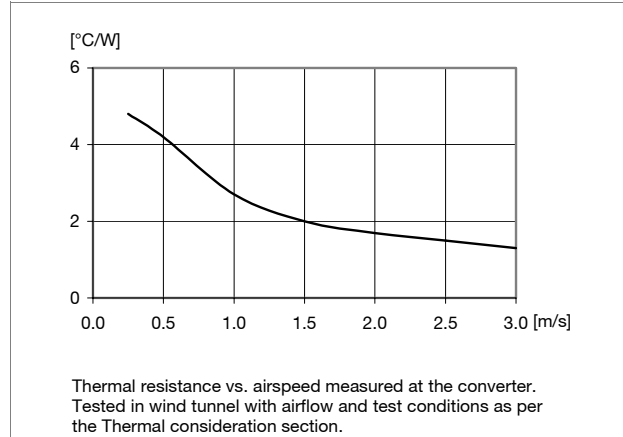
**Power Dissipation**



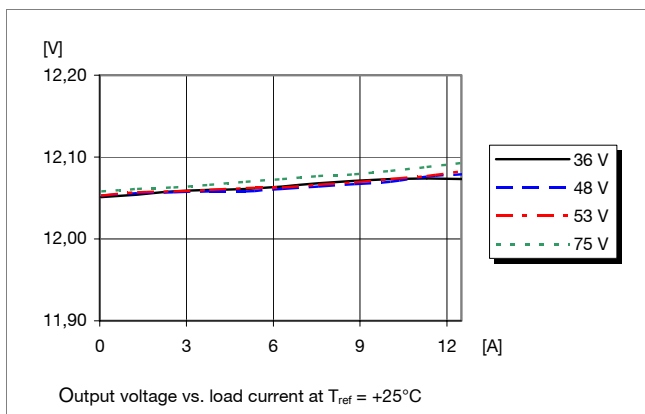
**Output Current Derating**



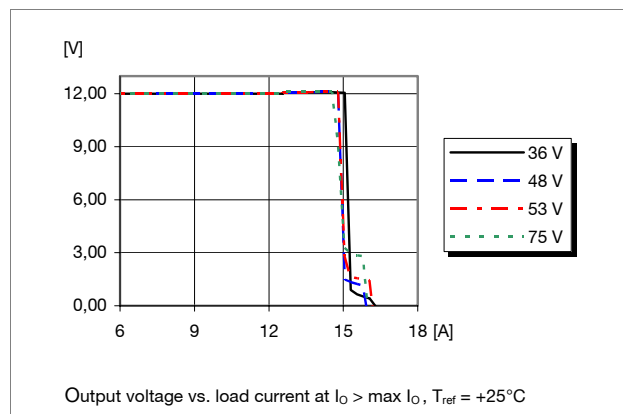
**Thermal Resistance**



**Output Characteristics**



**Current Limit Characteristics**



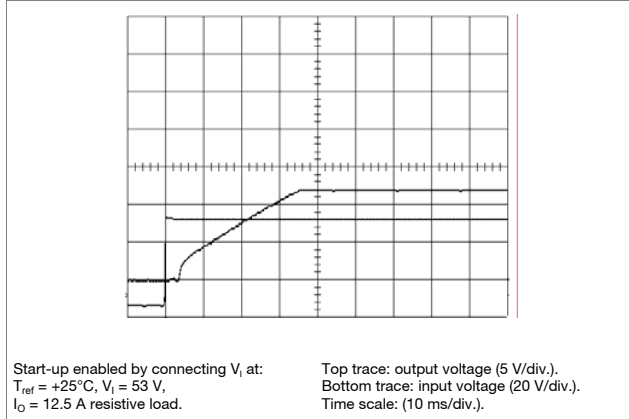
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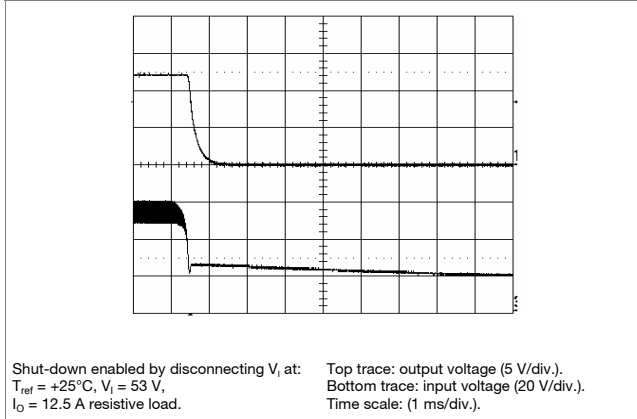
**12 V/12.5 A Typical Characteristics**

PKJ 4113A PIT

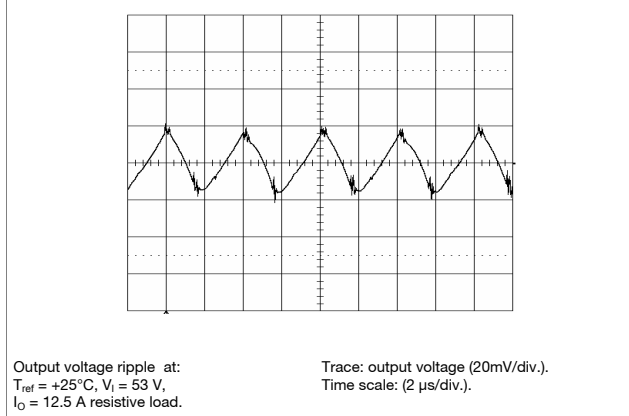
**Start-up**



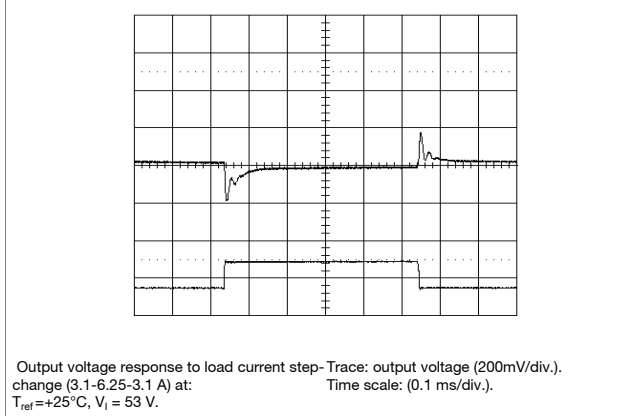
**Shut-down**



**Output Ripple & Noise**



**Output Load Transient Response**



**Output Voltage Adjust (see operating information)**

**Passive adjust**

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$$R_{adj} = [(V_o(100+\Delta\%)/(1.225\Delta\%)-(100+2\Delta\%)/\Delta\%)] \text{ k}\Omega$$

*Eg Increase 5% =>  $V_{out} = 12.6\text{ Vdc}$*   
 $12(100+5)/(1.225 \times 5) - (100+2 \times 5)/5 = 184.7\text{ k}\Omega$

Output Voltage Adjust Downwards, Decrease:

$$R_{adj} = (100 / \Delta\% - 2) \text{ k}\Omega$$

*Eg Decrease 5% =>  $V_{out} = 11.4\text{ Vdc}$*   
 $(100/5-2) = 18\text{ k}\Omega$

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DC/DC converters, Input 36-75 V, Output 30A/150W

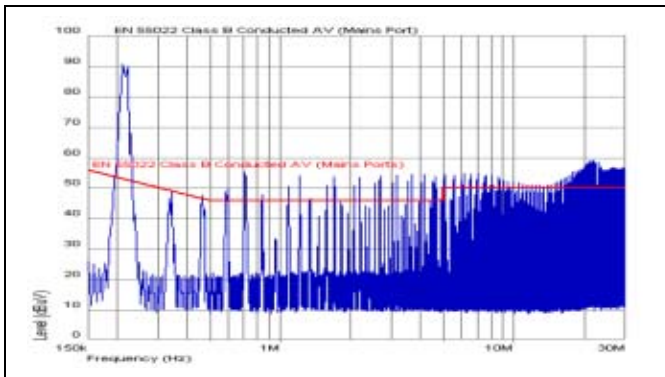
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**EMC Specification**

Conducted EMI measured according to EN55022, CISPR 22 and FCC part 15J (see test set-up).  
The fundamental switching frequency is 150 kHz for PKJ4000 PIT @  $V_1 = 53\text{ V}$ , max  $I_o$ .

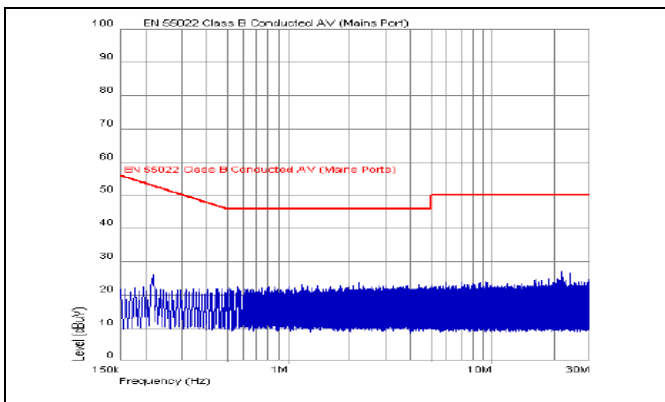
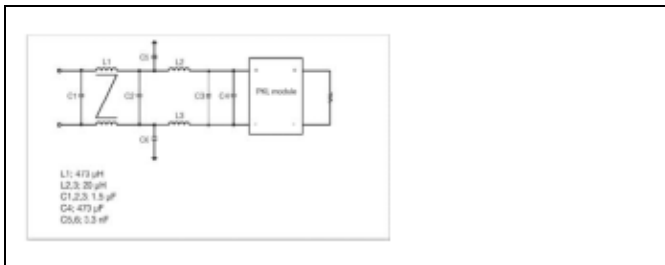
**Conducted EMI Input terminal value (typ)**



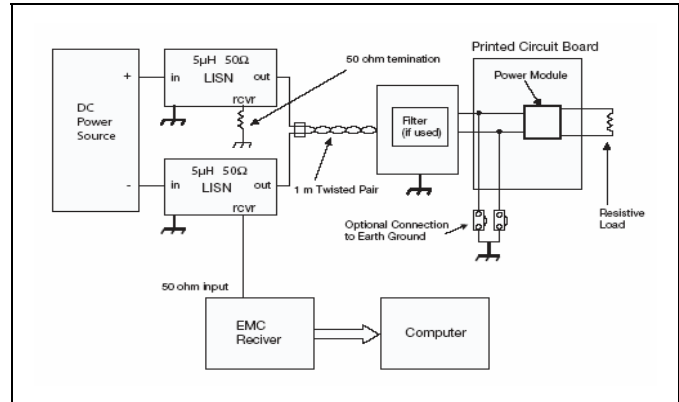
EMI without filter

**External filter (class B)**

Required external input filter in order to meet class B in EN 55022, CISPR 22 and FCC part 15J.



EMI with filter



Test set-up

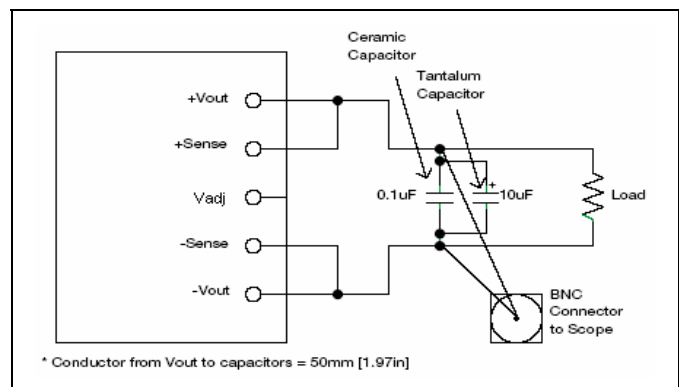
**Layout recommendation**

The radiated EMI performance of the DC/DC converter will depend on the PCB layout and ground layer design. It is also important to consider the stand-off of the DC/DC converter. If a ground layer is used, it should be connected to the output of the DC/DC converter and the equipment ground or chassis.

A ground layer will increase the stray capacitance in the PCB and improve the high frequency EMC performance.

**Output ripple and noise**

Output ripple and noise measured according to figure below. Oscilloscope input impedance 50 Ω.



Output ripple and noise test setup



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**Operating information**

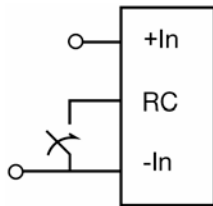
**Input Voltage**

The input voltage range 36 to 75Vdc meets the requirements of the European Telecom Standard ETS 300 132-2 for normal input voltage range in -48 and -60 Vdc systems, -40.5 to -57.0 V and -50.0 to -72 V respectively. At input voltages exceeding 75 V, the power loss will be higher than at normal input voltage and  $T_{ref}$  must be limited to absolute max +100°C. The absolute maximum continuous input voltage is 75Vdc.

**Turn-off Input Voltage**

The DC/DC converters monitor the input voltage and will turn on and turn off at predetermined levels. The minimum hysteresis between turn on and turn off input voltage is 1V.

**Remote Control (RC)**



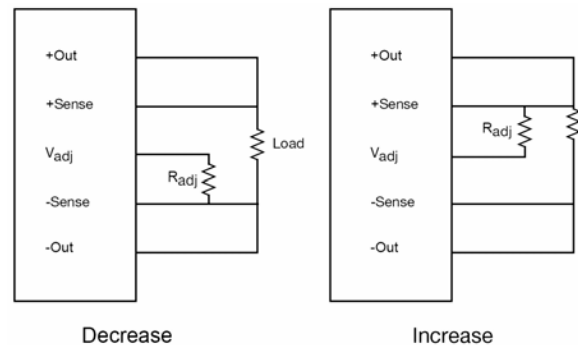
The products are fitted with a remote control function referenced to the primary negative input connection (- In), with negative and positive logic options available. The RC function allows the converter to be turned on/off by an external device like a semiconductor or mechanical switch.

The maximum required sink current is 1 mA. When the RC pin is left open, the voltage generated on the RC pin is 3.0 –6.5 V.

The second option is “positive logic” remote control, which can be ordered by adding the suffix “P” to the end of the part number. The converter will turn on when the input voltage is applied with the RC pin open. Turn off is achieved by connecting the RC pin to the - In. To ensure safe turn off the voltage difference between RC pin and the - In pin shall be less than 1V. The converter will restart automatically when this connection is opened.

**Output Voltage Adjust ( $V_{adj}$ )**

The DC/DC converters have a Output Voltage Adjust pin ( $V_{adj}$ ). This pin can be used to adjust the output voltage above or below Output voltage initial setting. When increasing the output voltage, the voltage at the output pins (including any remote sense compensation ) must be kept below the threshold of the over voltage protection, (OVP) to prevent the converter from shutting down. At increased output voltages the maximum power rating of the converter remains the same, and the max output current must be decreased correspondingly. To increase the voltage the resistor should be connected between the  $V_{adj}$  pin and +Sense pin. The resistor value of the Output voltage adjust function is according to information given under the Output section for the respective product. To decrease the output voltage, the resistor should be connected between the  $V_{adj}$  pin and -Sense pin.



**Remote Sense**

The DC/DC converters have remote sense that can be used to compensate for voltage drops between the output and the point of load. The sense traces should be located close to the PCB ground layer to reduce noise susceptibility. The remote sense circuitry will compensate for up to 10% voltage drop between output pins and the point of load.

If the remote sense is not needed +Sense should be connected to +Out and -Sense should be connected to -Out.

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## Operating information, cont.

### Input And Output Impedance

The impedance of both the input source and the load will interact with the impedance of the DC/DC converter. It is important that the input source has low characteristic impedance. The converters are designed for stable operation without external capacitors connected to the input or output. The performance in some applications can be enhanced by addition of external capacitance as described under External Decoupling Capacitors. If the input voltage source contains significant inductance, the addition of a 100  $\mu$ F capacitor across the input of the converter will ensure stable operation. The capacitor is not required when powering the DC/DC converter from an input source with an inductance below 10  $\mu$ H.

### External Decoupling Capacitors

When powering loads with significant dynamic current requirements, the voltage regulation at the point of load can be improved by addition of decoupling capacitors at the load. The most effective technique is to locate low ESR ceramic and electrolytic capacitors as close to the load as possible, using several parallel capacitors to lower the effective ESR. The ceramic capacitors will handle high-frequency dynamic load changes while the electrolytic capacitors are used to handle low frequency dynamic load changes. Ceramic capacitors will also reduce any high frequency noise at the load.

It is equally important to use low resistance and low inductance PCB layouts and cabling.

External decoupling capacitors will become part of the control loop of the DC/DC converter and may affect the stability margins. As a "rule of thumb", 100  $\mu$ F/A of output current can be added without any additional analysis.

The recommended absolute maximum value of output capacitance is 10 000  $\mu$ F. For further information please contact your local Ericsson Power Modules representative.

### Parallel Operation

Two converters may be paralleled for redundancy if the total power is equal or less than  $P_O$  max. It is not recommended to parallel the converters without using external current sharing circuits.

### Over Temperature Protection (OTP)

The converters are protected from thermal overload by an internal over temperature shutdown circuit. When the baseplate or case temperature exceeds 110°C the converter will shut down immediately (latching). The converter can be restarted by cycling the input voltage or using the remote control function.

### Over Voltage Protection (OVP)

The converters have output over voltage protection that will shut down the converter in over voltage conditions. The converter can be restarted by cycling the input voltage or using the remote control function.

### Over Current Protection (OCP)

The converters include current limiting circuitry for protection at continuous overload.

The output voltage will decrease towards zero for output currents in excess of max output current (max  $I_O$ ). The converter will resume normal operation after removal of the overload. The load distribution should be designed for the maximum output short circuit current specified.

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**Thermal Consideration**

**General**

The converters are designed to operate in different thermal environments and sufficient cooling must be provided to ensure reliable operation.

Cooling is achieved mainly by conduction, from the pins to the host board, and convection, which is dependant on the airflow across the converter. Increased airflow enhances the cooling of the converter.

The Output Current Derating graph found in the Output section for each model provides the available output current vs. ambient air temperature and air velocity at  $V_{in} = 53 V$ .

The DC/DC converter is tested on a 254 x 254 mm, 16-layer test board mounted vertically in a wind tunnel with a cross-section of 305 x 305 mm.

Proper cooling of the DC/DC converter can be verified by measuring the temperature at positions P1. The temperature at these positions should not exceed the max values provided in the table below.

Note that the max value is the absolute maximum rating (non destruction) and that the electrical Output data is guaranteed up to  $T_{ref} + 100^{\circ}C$ .

Position	Device	Designation	max value
P <sub>1</sub>	Baseplate	T <sub>ref</sub>	100° C

**Definition of reference temperature (T<sub>ref</sub>)**

The reference temperature is used to monitor the temperature limits of the product. Temperatures above maximum T<sub>ref</sub> are not allowed and may cause degradation or permanent damage to the product. T<sub>ref</sub> is also used to define the temperature range for normal operating conditions.

T<sub>ref</sub> is defined by the design and used to guarantee safety margins, proper operation and high reliability of the module.

**Ambient Temperature Calculation**

By using the thermal resistance the maximum allowed ambient temperature can be calculated.

1. The power loss is calculated by using the formula  $((1/\eta) - 1) \times \text{output power} = \text{power losses (Pd)}$ .  
 $\eta = \text{efficiency of converter. E.g } 89.5 \% = 0.895$

2. Find the thermal resistance (Rth) in the Thermal Resistance graph found in the Output section for each model.  
 Calculate the temperature increase ( $\Delta T$ ).  
 $\Delta T = R_{th} \times P_d$

3. Max allowed ambient temperature is:  
 Max Tref -  $\Delta T$ .

E.g PKJ4518 PIT at 2m/s:

1.  $((\frac{1}{0.86}) - 1) \times 54 W = 8.8 W$

2.  $8.8 W \times 2.1^{\circ}C/W = 18.5^{\circ}C$

3.  $110^{\circ}C - 18.5^{\circ}C = \text{max ambient temperature is } 91.5^{\circ}C$

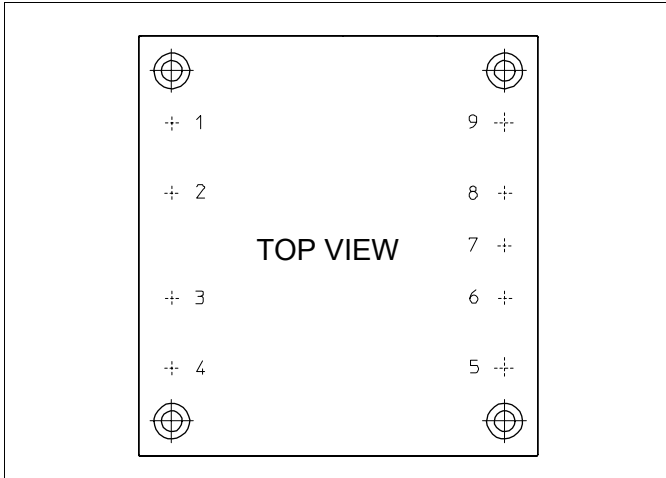
The actual temperature will be dependent on several factors such as the PCB size, number of layers and direction of airflow.

PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

EN/LZT 146 309 R1B February 2006

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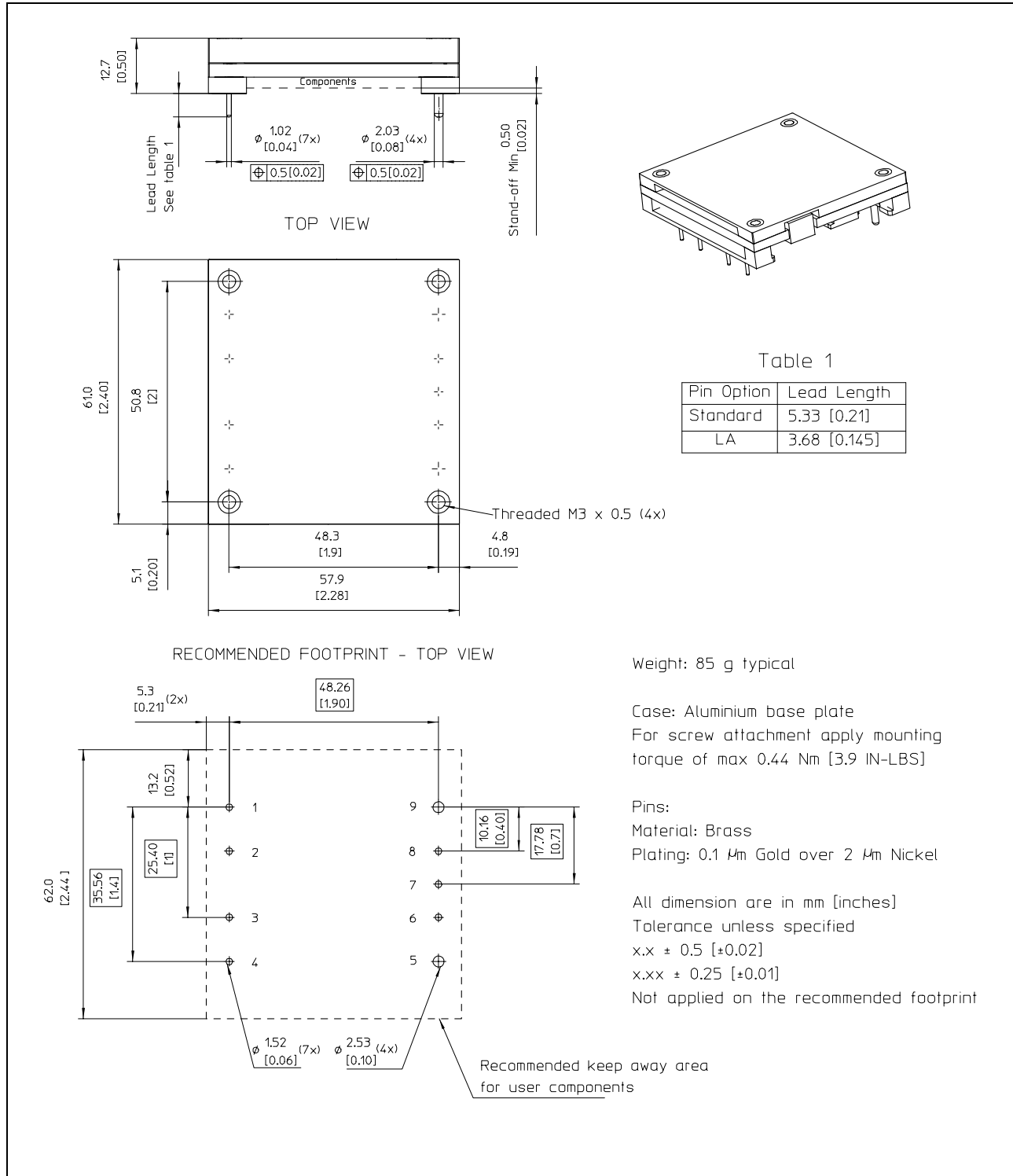
**Connections**



Pin	Designation	Function
1	+In	Positive Input
2	RC	Remote Control
3	Case	Connected to baseplate
4	-In	Negative Input
5	-Out	Negative Output
6	-Sen	Negative Sense
7	V <sub>adj</sub>	Output voltage adjust
8	+Sen	Positive Sense
9	+Out	Positive Output

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**Mechanical information**



**TOP VIEW**

Lead Length See table 1

Stand-off Min [0.02] 0.50

Components

$\phi$  1.02 (7x) [0.041]

$\phi$  2.03 (4x) [0.081]

$\phi$  0.5 [0.02]

**RECOMMENDED FOOTPRINT - TOP VIEW**

Threaded M3 x 0.5 (4x)

48.3 [1.9]

4.8 [0.19]

57.9 [2.28]

5.1 [0.20]

61.0 [2.40]

50.8 [2]

12.7 [0.50]

10.50

5.3 (2x) [0.21]

48.26 [1.90]

13.2 [0.52]

25.40 [1]

35.56 [1.4]

62.0 [2.44]

10.16 [0.40]

17.78 [0.7]

1 2 3 4 5 6 7 8 9

$\phi$  1.52 (7x) [0.06]

$\phi$  2.53 (4x) [0.10]

Recommended keep away area for user components

**Table 1**

Pin Option	Lead Length
Standard	5.33 [0.21]
LA	3.68 [0.145]

Weight: 85 g typical

Case: Aluminium base plate  
For screw attachment apply mounting torque of max 0.44 Nm [3.9 IN-LBS]

Pins:  
Material: Brass  
Plating: 0.1  $\mu$ m Gold over 2  $\mu$ m Nickel

All dimension are in mm [inches]  
Tolerance unless specified  
x.x  $\pm$  0.5 [ $\pm$ 0.02]  
x.xx  $\pm$  0.25 [ $\pm$ 0.01]  
Not applied on the recommended footprint

PKJ 4000 PI series  
DC/DC converters, Input 36-75 V, Output 30A/150W

EN/LZT 146 309 R1B February 2006  
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**Soldering Information – Through hole mounting**

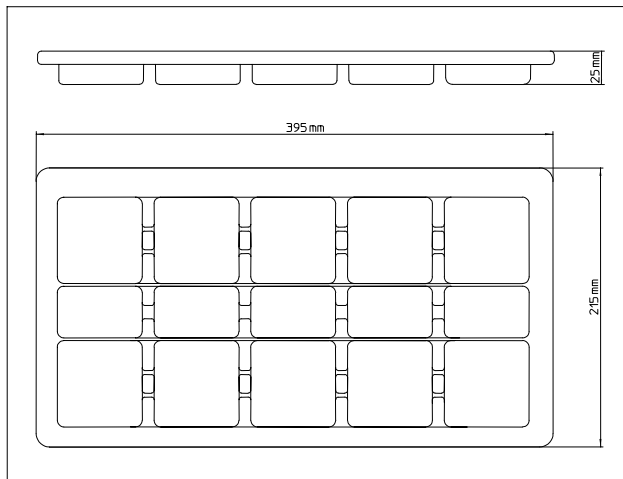
The PKJ series DC/DC converters are intended for through hole mounting in a PCB. When wave soldering is used max temperature on the pins is specified to 260°C for 10 seconds. Maximum preheat rate of 4°C/s and temperature of max 150°C is suggested. When hand soldering, care should be taken to avoid direct contact between the hot soldering iron tip and the pins for more than a few seconds in order to prevent overheating.

No-clean flux is recommended to avoid entrapment of cleaning fluids in cavities inside of the DC/DC power module. The residues may affect long time reliability and isolation voltage.

**Delivery package information**

The products are delivered antistatic clamshells

Tray properties	
<b>Material</b>	PET
<b>Surface resistance</b>	10E5 to 10E12 ohms/square
<b>Bake ability</b>	The clamshells can not be baked
<b>Clamshell capacity</b>	10 converters/clamshell
<b>Box capacity</b>	50 converters (5 full clamshells/box)



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**Product Qualification Specification**

Characteristics			
External visual inspection	IPC-A-610D		
Change of temperature	IEC 60068-2-14 Na	Temperature range Duration Cycle	-40 °C--+100 °C 0.5 h 300
Cold	IEC 60068-2-1 Ad	Temperature range Duration Input Voltage Load	-40 °C--+125 °C 72 h Minimum No
Damp heat	IEC 60068-2-3 Ca	Temperature Humidity Input Voltage Duration	+85 °C 85 % RH Maximum 1000 hours
Dry heat	IEC 60068-2-2 Ba	Time Temperature	1000 hours 125 °C
Immersion in cleaning solvents	IEC 60068-2-45 XA Method 2	Water Glycol ether Isopropyl alcohol	+55 ±5 °C +35 ±5 °C +35 ±5 °C
Mechanical shock	IEC 60068-2-27 Ea	Peak acceleration Duration	50 g 3 ms
Resistance to soldering heat (not in operation, without board)	IEC 60068-2-20 Tb	Solder Temperature Duration	260 °C 10-13 s
Robustness of terminations	IEC 60068-2-21 Ua		
Solder ability (Precondition 85°C/85%RH; 240h)	IEC 60068-2-20 Ta	SnPb Eutectic Pb Free	235±5 °C 270±5 °C
Vibration, broad-band random	IEC 60068-2-34 Ed	Frequency Acceleration Duration in each direction	10...500 Hz 0.025 g <sup>2</sup> /Hz 10 min
Sinusoidal	IEC 60068-2-6 Fc	Frequency Amplitude Acceleration No. of cycles	10-500 Hz 0.75 mm 10 g 10 in each axis