

VN02AN

HIGH SIDE SMART POWER SOLID STATE RELAY

TYPE	VDSS	R _{DS(on})	Ιουτ	Vcc
VN02AN	60 V	0.35 Ω	7 A	36 V

- OUTPUT CURRENT (CONTINUOUS): 7A @ $T_c=25^{\circ}C$
- LOGIC LEVEL 5V COMPATIBLE INPUT
- THERMAL SHUT-DOWN
- UNDER VOLTAGE PROTECTION
- OPEN DRAIN DIAGNOSTIC OUTPUT
- FAST DEMAGNETIZATION OF INDUCTIVE LOAD

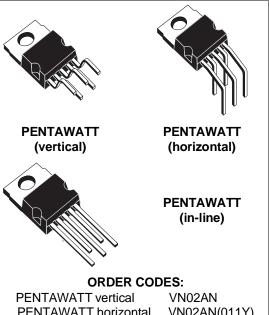
DESCRIPTION

The VN02AN is a monolithic device made using STMicroelectronics VIPower Technology, intended for driving resistive or inductive loads with one side grounded.

Built-in thermal shut-down protects the chip from over temperature and short circuit.

The diagnostic output indicates an over temperature status.

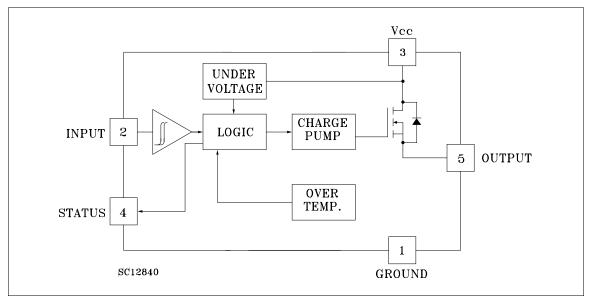
Fast turn-off of inductive load is achieved by negative (-18 V) load voltage at turn-off.



PENTAWATT horizontal PENTAWATT in-line

VN02AN(011Y) VN02AN(012Y)

BLOCK DIAGRAM

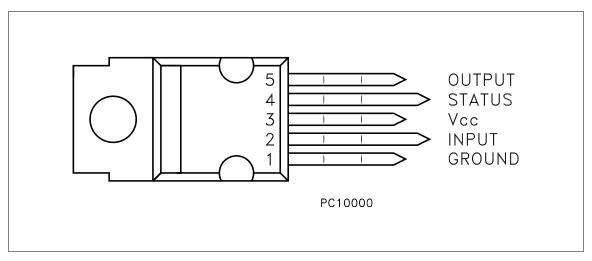


July 1998

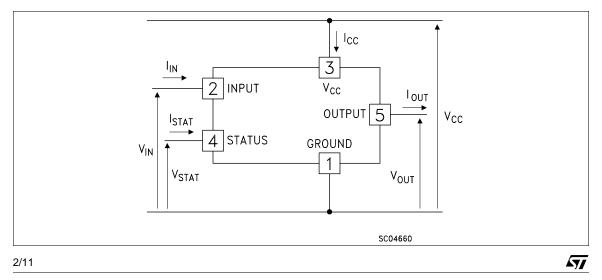
ABSOLUTE MAXIMUM RATING

Symbol	Parameter	Value	Unit
V _{(BR)DSS}	Drain-Source Breakdown Voltage	60	V
IOUT	Output Current (cont.)	7	А
I _R	Reverse Output Current	-7	А
lin	Input Current	±10	mA
-V _{CC}	Reverse Supply Voltage	-4	V
ISTAT	Status Current (sink)	±10	mA
Vesd	Electrostatic Discharge (1.5 kΩ, 100 pF)	2000	V
Ptot	Power Dissipation at $T_c \le 25$ °C	31	W
Tj	Junction Operating Temperature	-40 to 150	°C
T _{stg}	Storage Temperature	-55 to 150	°C

CONNECTION DIAGRAMS



CURRENT AND VOLTAGE CONVENTIONS



THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-case	Max	4	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient	Max	60	°C/W

ELECTRICAL CHARACTERISTICS (V_{CC} = 9 to 36 V; $T_{case} = 25$ °C unless otherwise specified) POWER

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vcc *	Supply Voltage	-40 °C < T _j < 125 °C	7		36	V
Ron	On State Resistance	$I_{OUT} = 3 A$ $I_{OUT} = 1 A$ $V_{CC} = 30 V$ $T_j = 125 °C$			0.35 0.6	Ω Ω
Is	Supply Current	$\begin{array}{llllllllllllllllllllllllllllllllllll$			1 9 7	mA mA mA

SWITCHING

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)}	Turn-on Delay Time Of Output Current	I _{OUT} = 3 A Resistive Load Input Rise Time < 0.1 μs		15		μs
tr	Rise Time Of Output Current	l _{OUT} = 3 A Resistive Load Input Rise Time < 0.1 μs		15		μs
$t_{d(off)}$	Turn-off Delay Time Of Output Current	I _{OUT} = 3 A Resistive Load Input Rise Time < 0.1 μs		14		μs
t _f	Fall Time Of Output Current	I _{OUT} = 3 A Resistive Load Input Rise Time < 0.1 μs		4.5		μs
(di/dt) _{on}	Turn-on Current Slope	I _{OUT} = 3 A 25 °C < T _j < 125 °C I _{OUT} = I _{OV} 25 °C < T _j < 125 °C			0.5 1	A/μs A/μs
(di/dt) _{off}	Turn-off Current Slope	Iout = 3 A 25 °C < T _j < 125 °C Iout = Iov 25 °C < T _j < 125 °C 25 °C < T _j < 125 °C			1.5 4	A/μs A/μs
Vdemag	Inductive Load Clamp Voltage	I _{OUT} = 3 A -40 °C < T _j < 125 °C	-24	-18	-14	V

LOGIC INPUT (-40 $^oC \leq T_j \leq$ 125 oC unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
VIL	Input Low Level Voltage				0.8	V
V _{IH}	Input High Level Voltage		2		(*)	V
VI(hyst.)	Input Hysteresis Voltage			0.5		V
l _{in}	Input Current	$V_{IN} = 5 V$ $V_{IN} = 2 V$ $V_{IN} = 0.8 V$	25	250	600 300	μΑ μΑ μΑ
VICL	Input Clamp Voltage	I _{IN} = 10 mA I _{IN} = -10 mA	5.5	6 -0.7	-0.3	V V

-

ELECTRICAL CHARACTERISTICS (continued)

PROTECTION AND DIAGNOSTICS (-40 $^oC \le T_j \le 125 \ ^oC$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{STAT}	Status Voltage Output Low	I _{STAT} = 1.6 mA			0.4	V
ISTAT	Status Leakage Current	V _{STAT} = 5 V			10	μA
Vusd	Under Voltage Shut Down		3.5	6	7	V
V_{SCL}	Status Clamp Voltage	$I_{STAT} = 10 \text{ mA}$ $I_{STAT} = -10 \text{ mA}$	5.5	6 -0.7	-0.3	V V
I _{OV}	Over Current	$R_{LOAD} < 10 m\Omega$		15		Α
l _{av}	Average Current In Short Circuit	$R_{LOAD} < 10 \text{ m}\Omega$ $T_c = 85 \ ^{\circ}C$		0.6		A
IDOFF	Leakage Current	V _{CC} = 30 V			1	mA
T _{TSD}	Thermal Shut-down Temperature		140			°C
T _R	Reset Temperature		125			°C

(*) The Vih is internally clamped at about 6V. It is possible to connect this pin to a higher voltage via an external resistor calculated to not exceed 10 mA at the input pin.

TRUTH TABLE

	INPUT	DIAGNOSTIC	OUTPUT
Normal Operation	L H	H H	L H
Over-temperature	Н	L	L
Under-voltage	Х	Н	L

Figure 1: Waveforms

INPUT	NORMAL OPERATION	INPUT STATUS SWITCH ON OFF 140 °C 140 °C 125 °C
INPUT	UNDER VOLTAGE	SC04691

4/11

57

FUNCTIONAL DESCRIPTION

The device has a diagnostic output which indicates over temperature conditions.

The truth table shows input, diagnostic output status and output voltage level in normal operation and fault conditions. The output signals are processed by internal logic.

To protect the device against short circuit and over current conditions, the thermal protection turns the integrated Power MOS off at a minimum junction temperature of 140 °C. When the temperature returns to 125 °C the switch is automatically turned on again. To ensure the protection in all V_{CC} conditions and in all the junction temperature range it is necessary to limit the voltage drop across Drain and Source (pin 3 and 5) at 28V according to:

 $V_{ds} = V_{CC} - I_{OV} * (R_i + R_w + R_l)$

where:

R_i = internal resistence of Power Supply

R_w = Wires resistance

R_I = Short Circuit resistance

Driving inductive loads, an internal function of the device ensures the fast demagnetization with typical voltage (V_{demag}) of -18V.

This function allows the reduction of the power dissipation according to the formula:

 $P_{dem} = 0.5 * L_{load} * (I_{load})^2 * [(V_{CC} + V_{dem})/V_{dem}] * f$

100nF 5μΗ $5 \mathrm{m} \Omega$ Vcc OUTPUT $I_{nom} = 100A$ 0.1F IN $< 10 \mathrm{m}\,\Omega$ ESR<10m Ω GND SC04710 57

Figure 2: Over Current Test Circuit

where f = Switching Frequency

Based on this formula it is possible to know the value of inductance and/or current to avoid a thermal shut-down.

PROTECTING THE DEVICE AGAINST RE-VERSE BATTERY

The simpliest way to protect the device against a continuous reverse battery voltage (-36V) is to insert a Schottky diode between pin 1 (GND) and ground, as shown in the typical application circuit (Fig. 3). The consequences of the voltage drop across this diode are as follows:

If the input is pulled to power GND, a negative voltage of -V_f is seen by the device. (V_{il}, V_{ih} thresholds and Vstat are increased by Vf with respect to power GND).

The undervoltage shut-down level is increased by Vf.

If there is no need for the control unit to handle external analog signals referred to the power GND, the best approach is to connect the reference potential of the control unit to node [1] (see application circuit in fig. 4), which becomes the common signal GND for the whole control board avoiding shift of V_{ih}, V_{il} and V_{stat}. This solution allows the use of a standard diode.

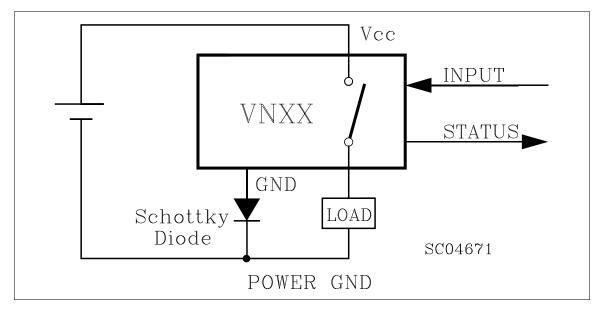
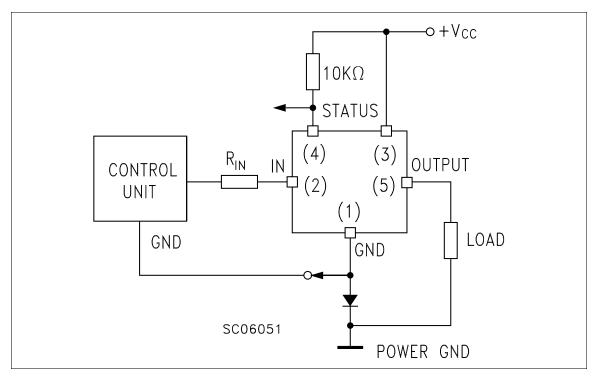


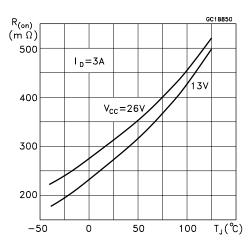
Figure 3: Typical Application Circuit With A Schottky Diode For Reverse Supply Protection

Figure 4: Typical Application Circuit With Separate Signal Ground

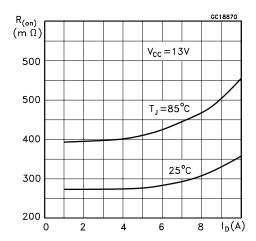


57

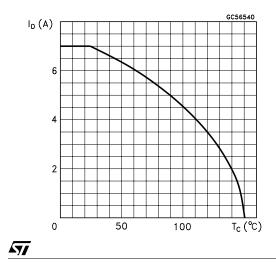
R_{DS(on)} vs Junction Temperature



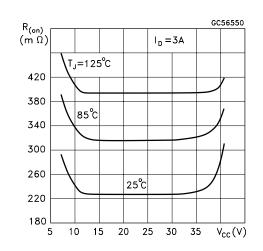
RDS(on) vs Output Current

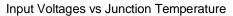


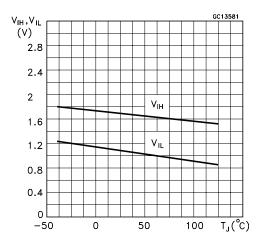




RDS(on) vs Supply Voltage



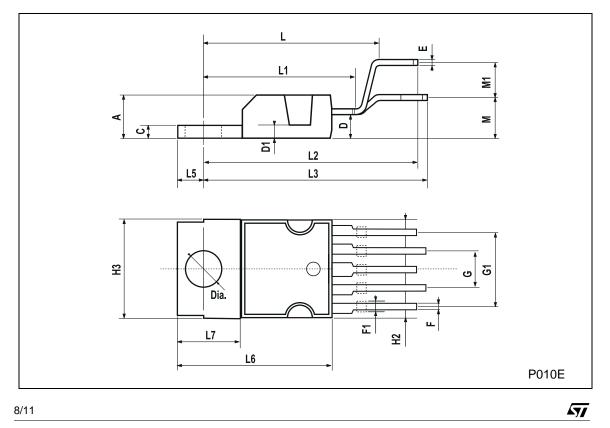




VN02AN

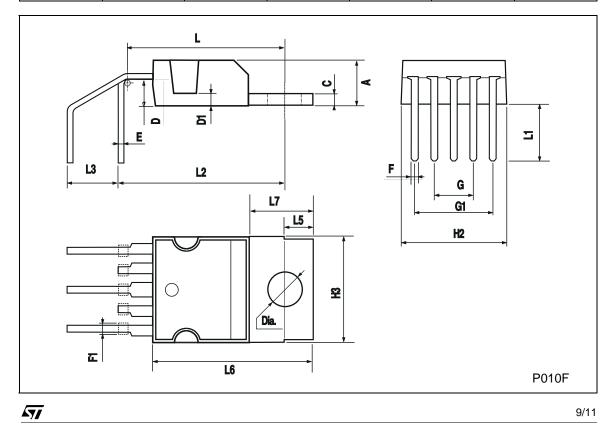
PENTAWATT (VERTICAL) MECHANICAL DATA

DIM.		mm			inch			
DIM.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
А			4.8			0.189		
С			1.37			0.054		
D	2.4		2.8	0.094		0.110		
D1	1.2		1.35	0.047		0.053		
Е	0.35		0.55	0.014		0.022		
F	0.8		1.05	0.031		0.041		
F1	1		1.4	0.039		0.055		
G	3.2	3.4	3.6	0.126	0.134	0.142		
G1	6.6	6.8	7	0.260	0.268	0.276		
H2			10.4			0.409		
H3	10.05		10.4	0.396		0.409		
L		17.85			0.703			
L1		15.75			0.620			
L2		21.4			0.843			
L3		22.5			0.886			
L5	2.6		3	0.102		0.118		
L6	15.1		15.8	0.594		0.622		
L7	6		6.6	0.236		0.260		
М		4.5			0.177			
M1		4			0.157			
Dia	3.65		3.85	0.144		0.152		



DIM.		mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А			4.8			0.189	
С			1.37			0.054	
D	2.4		2.8	0.094		0.110	
D1	1.2		1.35	0.047		0.053	
Е	0.35		0.55	0.014		0.022	
F	0.8		1.05	0.031		0.041	
F1	1		1.4	0.039		0.055	
G	3.2	3.4	3.6	0.126	0.134	0.142	
G1	6.6	6.8	7	0.260	0.268	0.276	
H2			10.4			0.409	
H3	10.05		10.4	0.396		0.409	
L	14.2		15	0.559		0.590	
L1	5.7		6.2			0244	
L2	14.6		15.2			0.598	
L3	3.5		4.1	0.137		0.161	
L5	2.6		3	0.102		0.118	
L6	15.1		15.8	0.594		0.622	
L7	6		6.6	0.236		0.260	
Dia	3.65		3.85	0.144		0.152	

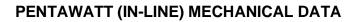
PENTAWATT (HORIZONTAL) MECHANICAL DATA

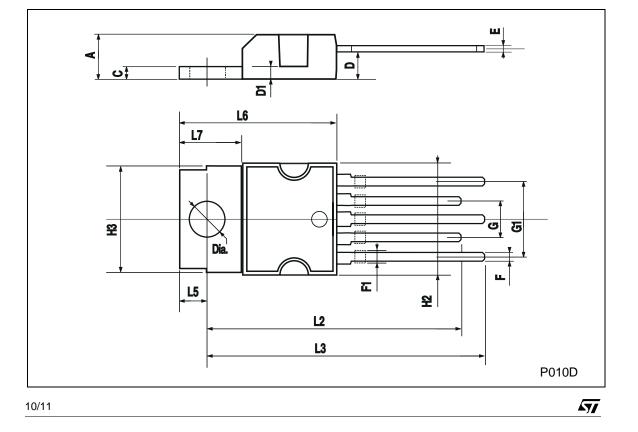


Downloaded from Elcodis.com	electronic components distributor
-----------------------------	-----------------------------------

VN02AN

DIM.	mm			inch		
	MIN	ТҮР	MAX	MIN	ТҮР	MAX
А			4.8			0.189
С			1.37			0.054
D	2.4		2.8	0.094		0.110
D1	1.2		1.35	0.047		0.053
E	0.35		0.55	0.014		0.022
F	0.8		1.05	0.031		0.041
F1	1		1.4	0.039		0.055
G	3.2	3.4	3.6	0.126	0.134	0.142
G1	6.6	6.8	7	0.260	0.268	0.276
H2			10.4			0.409
H3	10.05		10.4	0.396		0.409
L2	23.05	23.4	23.8	0.907	0.921	0.937
L3	25.3	25.65	26.1	0.996	1.010	1.028
L5	2.6		3	0.102		0.118
L6	15.1		15.8	0.594		0.622
L7	6		6.6	0.236		0.260
Diam.	3.65		3.85	0.144		0.152





Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics. The ST logo is a trademark of STMicroelectronics

© 1998 STMicroelectronics – Printed in Italy – All Rights Reserved STMicroelectronics GROUP OF COMPANIES Australia - Brazil - Canada - China - France - Germany - Italy - Japan - Korea - Malaysia - Malta - Mexico - Morocco - The Netherlands -Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.

A7/