

PanelMatchTM

LXM1617-05-6x

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5V 6W CCFL Programmable Inverter Module

PRODUCTION DATASHEET

DESCRIPTION

The LXM1617-05-6x is a Single Output 6W Direct Drive[™] CCFL (Cold energizes the lamp Cathode Fluorescent Lamp) Inverter specifically to ensure that no premature Module specifically designed for driving lamp degradation occurs, while allowing LCD backlight lamps. It is ideal for significant power savings at lower dim driving typical 12.1" to 15" TFT panels.

LXM1617 modules provide the designer with a vastly superior display the system battery or AC adapter directly brightness range. This brightness range is to high frequency, high-voltage waves achievable with virtually any LCD display. required to ignite and operate CCFL

externally programmable over a range of 5 available (LXM1617-12-6x). to 8mA in 1mA steps to allow the inverter to properly match to a wide array of LCD Microsemi's new LX1689 backlight panel lamp current specifications. The controller, which provides a number of modules include a dimming input that cost and performance advantages due to permits brightness control from a dc the controller's high level of integration. voltage source, a PWM signal or an external potentiometer.

RangeMAX Digital Technique provides flicker-free brightness and both open/shorted lamp protection control in any wide range typically (50:1+) with fault timeout. dimming application.

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com Protected By U.S. Patents: 5,923,129; 5,930,121; 6,198,234; Patents Pending

The resultant "burst drive" that was designed levels.

The modules convert DC voltage from The maximum output current is lamps. A 12V input inverter is also

The modules design is based on

Other benefits of this new topology are stable fixed-frequency operation, Dimming secondary-side strike-voltage regulation

KEY FEATURES

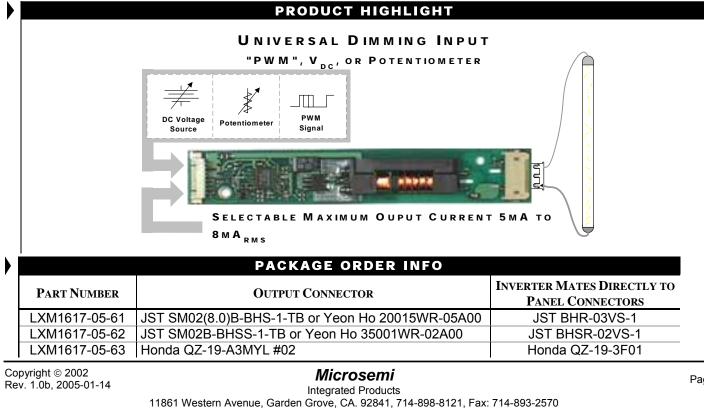
- Externally Programmable Maximum Output Current
- Easy to Use Brightness Control
- **Output Short-Circuit Protection** and Automatic Strike-Voltage
- RangeMAX Wide Range Dimming
- Regulation and Timeout Rated From -20 to 70° C
- UL60950 E175910

APPLICATIONS

- Notebook And Sub-Notebook Computers
- Portable Instrumentation
- Desktop Displays
- Industrial Display Controls

BENEFITS

- Smooth, Flicker Free 2%-100% • Full-Range Brightness Control
- Programmable output current allows inverter to mate with a wide variety of LCD panel's specifications
- Output Open Circuit Voltage Regulation Minimizes Corona Discharge For High Reliability



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ABSOLUTE MAXIMUM RATINGS (NOTE 1)

Input Signal Voltage (V _{IN1}) Input Power Output Voltage, no load Output Current Output Power	
Input Signal Voltage (SLEEP Input)	-0.3V to 5.5V
Input Signal Voltage (BRITE)	-0.3V to 5.5V
Ambient Operating Temperature, zero airflow	-0.3V to 5.5V
Storage Temperature Range	-20°C to 70°C

Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, will not function optimally.

Parameter	Symbol	Recommended Operating Conditions			Units	
i di dificiei	Symbol	Min	R.C.	Max	Onits	
Input Supply Voltage Range (Fully Regulated Lamp Current)	V _{IN1}	4.75	5	5.25	V	
Input Supply Voltage Range (Functional)		4.5	5	5.5		
Output Power	Po			6.0	W	
Linear BRITE Control Input Voltage Range	VBRT_ADJ	0.5	2.0	V _{IN}	V	
Lamp Operating Voltage	VLAMP	545	640	735*	V _{RMS}	
Lamp Current (Full Brightness)	IOLAMP	5		8	mA _{RMS}	
Operating Ambient Temperature Range	T _A	-20		70	°C	

* Total output power must not exceed 6W. Higher voltage lamps may require maximum output current to be set lower than 8mARMS

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of 25° C except where otherwise noted.

Parameter	Symbol	Test Conditions	LXM1617-05-6x			Units
Farailleter	Symbol	Test Conditions	Min	Тур	Max	Units
OUTPUT PIN CHARACTERISTICS						
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}$, SLEEP $\ge 2.0V$, $V_{IN1} = 5V_{DC}$ I _{SET1} = Ground, I _{SET2} = Ground	4.4	5	5.6	mA _{RMS}
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}$, SLEEP $\ge 2.0V$, $V_{IN1} = 5V_{DC}$ I _{SET1} = Ground, I _{SET2} = Open	5.4	6	6.6	mA _{RMS}
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 5V_{DC}$ $I_{SET1} = Open$, $I_{SET2} = Ground$	6.4	7	7.6	mA _{RMS}
Full Bright Lamp Current	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}$, SLEEP $\ge 2.0V$, $V_{IN1} = 5V_{DC}$ $I_{SET1} = Open$, $I_{SET2} = Open$	7.4	8	8.6	mA _{RMS}
Min. Average Lamp Current	I _{L(MIN)}	$V_{BRT_ADJ} \le 0.5V_{DC}$, SLEEP $\ge 2.0V$, $V_{IN1} = 5V_{DC}$ $I_{SET1} = I_{SET2} = Ground$		0.30		mA _{RMS}
Lamp Start Voltage	V _{LS}	-20°C < T _A < 70°C, V _{IN1} > 4.5V _{DC}	1400	1650		V_{RMS}
Operating Frequency	fo	$V_{BRT_{ADJ}}$ = 2.5 V_{DC} , $\overline{SLEEP} \ge 2.0V$, V_{IN1} = 5V	57	60	63	kHz
Burst Frequency	f _{BURST}	Output Burst Frequency	222	234	246	Hz

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LECTRICAL CHARACTERISTICS (CONTINUED)

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	Parameter			Symbol	Test Conditions	LXN	/1617-05	5-6x	Unit
		Parameter		Symbol	Test conditions	Min	Тур	Max	Unit
	BRITE IN	PUT							
	Input Current		I _{BRT}	$V_{BRT_{ADJ}} = 0V_{DC}$		-300		μA _D	
				$V_{BRT_{ADJ}} = 2.5 V_{DC}$		50	0.05	μΑ _D	
	Minimum Input for Max. Lamp Current		V _{BRT_ADJ}	I _{O(LAMP)} = Maximum Lamp Current	0.4	2.0	2.05		
	Maximum Input for Min. Lamp Current		V_{BRT_ADJ}	I _{O(LAMP)} = Minimum Lamp Current	0.4	0.5		V _{DC}	
	SLEEP INPUT			M					
	RUN Mode		V _{SLEEP}		2.0		V _{IN1}	V _{DC}	
	SLEEP Mode		$V_{\overline{\text{SLEEP}}}$		-0.3		0.8	V _{DC}	
	SET _{1,2} INF	PUT					1	1	
	SET _{1,2} Lov	w Threshold		VL				0.4	V
	Input Curr	rent		I _{SET}	V _{SET} ≤ 0.5V		-300		μA
	POWER 0	CHARACTERIS	TICS						
	Sleep Cur	rent		I _{IN(MIN)}	$V_{IN1} = 5V_{DC}, \ \overline{SLEEP} \le 0.8V$	0.0	5	50	μA _D
	Run Curre	ent		I _{RUN}	$V_{IN1} = 5V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $I_{SET1} = Open$ $I_{SET2} = Ground$, $V_{LAMP} = 640V_{RMS}$		1090		mΑ _c
	Efficiency		η	$V_{IN1} = 5V_{DC}, \ \overline{SLEEP} \ge 2.0V, I_{SET1} = Open$ $I_{SET2} = Ground, V_{LAMP} = 640V_{RMS}$		85		%	
				FUNC	TIONAL PIN DESCRIPTION				
	CONN	ΡιΝ	DESCRIPTION						
	N1 (Molex	(53261-0890)	Mates with	51021-080	00 housing, 50079-8100 pins. Mates with LX	(9501 input	cable as	sembly	
С	N1 (Molex CN1-1					(9501 input	cable as	sembly	
С		x 53261-0890) V _{IN1}			00 housing, 50079-8100 pins. Mates with LX upply (4.75V \leq V _{IN1} \leq 5.25V)	(9501 input	cable as	semply	
С	CN1-1	V _{IN1}	Main Inpu	Power Si	upply (4.75V \leq V _{IN1} \leq 5.25V)	(9501 input	cable as	sembly	
С	CN1-1 CN1-2			Power Si	upply (4.75V \leq V _{IN1} \leq 5.25V)	(9501 input	cable as	sembly	
C	CN1-1 CN1-2 CN1-3	V _{IN1}	Main Input	t Power Si	upply (4.75V \leq V _{IN1} \leq 5.25V)		cable as		
C	CN1-1 CN1-2 CN1-3 CN1-4	V _{IN1} GND	Main Input Power Sup ON/OFF C	t Power Si oply Retur Control. (0	upply (4.75V \leq V _{IN1} \leq 5.25V)	: ON	cable as		
C	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5	V _{IN1} GND SLEEP	Main Input Power Sup ON/OFF C Brightness	t Power Si oply Retur Control. (0' S Control (upply (4.75V \leq V _{IN1} \leq 5.25V) n V < SLEEP < 0.8 = OFF, SLEEP >= 2.0V =	ON			
C	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5 CN1-6	V _{IN1} GND SLEEP BRITE	Main Input Power Sup ON/OFF C Brightness SET ₁ MSE	t Power Si oply Retur Control. (0 S Control (Connecti	upply (4.75V \leq V _{IN1} \leq 5.25V) n V \leq SLEEP \leq 0.8 = OFF, SLEEP \geq 2.0V = 0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum lar	ON op current. urrent (see	Table 1)		
C	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5 CN1-5 CN1-6 CN1-7 CN1-8 N2 for LX	V _{IN1} GND SLEEP BRITE SET1 SET2 M1617-05-61	Main Input Power Sup ON/OFF C Brightness SET ₁ MSE SET ₂ LSB and -62	t Power Si oply Retur Control. (0' Control (Connecti Connecti	upply (4.75V \leq V _{IN1} \leq 5.25V) n V \leq SLEEP \leq 0.8 = OFF, SLEEP \geq 2.0V = 0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum larring this pin to ground decreases the output c	ON p current. urrent (see urrent (see	Table 1) Table 1)		
С (J	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5 CN1-5 CN1-6 CN1-7 CN1-8 N2 for LX	V _{IN1} GND SLEEP BRITE SET1 SET2 M1617-05-61	Main Input Power Sup ON/OFF C Brightness SET ₁ MSE SET ₂ LSB and -62 TB Yeon H	t Power Si oply Retur Control. (0 ¹ Control (Connecti Connecti o 20015W ge connect	upply (4.75V \leq V _{IN1} \leq 5.25V) n V \leq SLEEP $<$ 0.8 = OFF, SLEEP $>$ = 2.0V = 0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum lam ng this pin to ground decreases the output c ing this pin to ground decreases the output c (R-05A00 or SM02B-BHSS-1-TB Yeon Ho ction to high Side of lamp. Connect to lamp t	ON p current. urrent (see urrent (see 35001WR-0	Table 1) Table 1) 02A00)		ngth.
С (J	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5 CN1-5 CN1-6 CN1-7 CN1-8 N2 for LX IST SM02(V _{IN1} GND SLEEP BRITE SET1 SET2 M1617-05-61 (8.0)B-BHS-1-	Main Input Power Sup ON/OFF C Brightness SET ₁ MSE SET ₂ LSB and -62 TB Yeon H High volta DO NOT c	t Power Si oply Retur Control. (0 ¹ control (Connecti connecti o 20015W ge connec connect to n to low si	upply (4.75V \leq V _{IN1} \leq 5.25V) n V \leq SLEEP $<$ 0.8 = OFF, SLEEP $>$ = 2.0V = 0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum larr ng this pin to ground decreases the output c ing this pin to ground decreases the output c (R-05A00 or SM02B-BHSS-1-TB Yeon Ho ction to high Side of lamp. Connect to lamp t Ground.	ON p current. urrent (see urrent (see 35001WR-0 erminal with	Table 1) Table 1) 02A00) n shortes		ngth.
C (J	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5 CN1-5 CN1-6 CN1-7 CN1-8 N2 for LX IST SM02(CN2-1 CN2-2	V _{IN1} GND SLEEP BRITE SET1 SET2 M1617-05-61 (8.0)B-BHS-1- V _{HI}	Main Input Power Sup ON/OFF C Brightness SET ₁ MSE SET ₂ LSB and -62 TB Yeon H High volta DO NOT c Connectio DO NOT c	t Power Si oply Retur Control. (0) Control (Connecti Connecti o 20015W ge connect connect to connect to	upply (4.75V \leq V _{IN1} \leq 5.25V) n V \leq SLEEP $<$ 0.8 = OFF, SLEEP $>$ = 2.0V = 0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum larr ng this pin to ground decreases the output c ing this pin to ground decreases the output c /R-05A00 or SM02B-BHSS-1-TB Yeon Ho ction to high Side of lamp. Connect to lamp to Ground. ide of lamp. Connect to lamp terminal with lo Ground	ON p current. urrent (see urrent (see 35001WR-0 erminal with	Table 1) Table 1) 02A00) n shortes		ngth.
С (J	CN1-1 CN1-2 CN1-3 CN1-4 CN1-5 CN1-5 CN1-6 CN1-7 CN1-8 N2 for LX IST SM02(CN2-1 CN2-2	V _{IN1} GND SLEEP BRITE SET1 SET2 M1617-05-61 (8.0)B-BHS-1- V _{HI} V _{LO}	Main Input Power Sup ON/OFF C Brightness SET ₁ MSE SET ₂ LSB and -62 TB Yeon H High volta DO NOT c Connectio DO NOT c (Honda QZ-	t Power Si oply Retur Control. (0 ¹ Connecti Connecti connect to n to low si connect to 19-A3MYI ge connect	upply (4.75V \leq V _{IN1} \leq 5.25V) n V \leq SLEEP $<$ 0.8 = OFF, SLEEP $>$ = 2.0V = 0.5V to 2.0V _{DC}). 2.0V _{DC} gives maximum lam- ng this pin to ground decreases the output c ing this pin to ground decreases the output c (R-05A00 or SM02B-BHSS-1-TB Yeon Ho Stion to high Side of lamp. Connect to lamp to Ground. ide of lamp. Connect to lamp terminal with loc Ground #02) tion to high side of lamp. Connect to lamp to	ON p current. urrent (see urrent (see 35001WR-0 erminal with onger lead lo	Table 1) Table 1) 02A00) n shortes ength.	it lead le	

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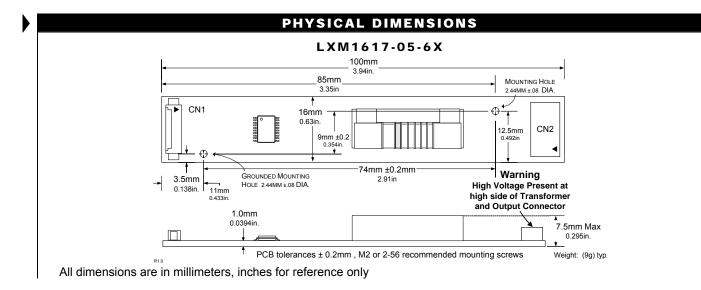
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TABLE 1 SETTING OUTPUT CURRENT

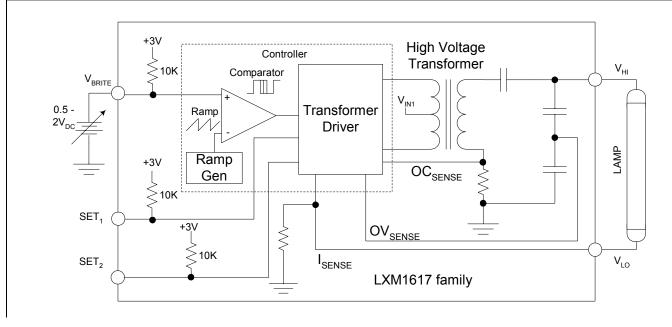
OUTPUT CURRENT SETTINGS

SET₁ (Pin 7)	SET ₂ (Pin 8)	Nominal Output Current
Open*	Open*	8.0mA
Open*	Ground	7.0mA
Ground	Open*	6.0mA
Ground	Ground	5.0mA

* If driven by a logic signal it should be open collector or open drain only, not a voltage source.



SIMPLIFIED BLOCK DIAGRAM



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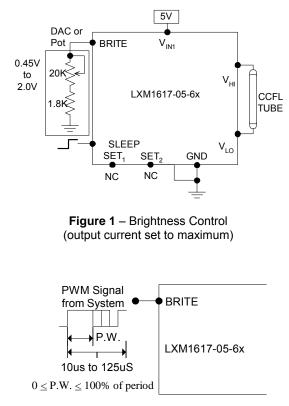


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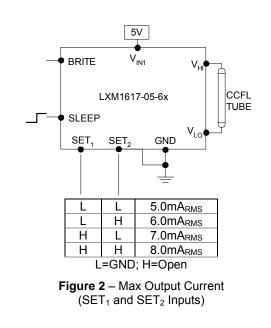
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TYPICAL APPLICATION







- The brightness control may be a voltage output DAC or other voltage source, a digital pot or 20K manual pot. The inverter contains an internal 10K pull-up to 3V to bias the pot add a 1.8K resistor to set the lower threshold voltage. A 3.3V Logic Level PWM signal from a micro-controller may also be used as shown in Figure 1A.
- If you need to turn the inverter ON/OFF remotely, connect to TTL logic signal to the SLEEP input.
- Connect V_{HI} to high voltage wire from the lamp. Connect V_{LO} to the low voltage wire (wire with thinner insulation). Never connect V_{LO} to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to V_{LO}. This wire is typically white.
- Use the SET₁ and SET₂ (see Figure 2) inputs to select the desired maximum output current. Using these two pins in combination allows the inverter to match a wide variety of panels from different manufactures. Generally the best lamp lifetime correlates with driving the CCFL at the manufactures nominal current setting. However the SET₁ and SET₂ inputs allow the user the flexibility to adjust the current to the maximum allowable output current to increase panel brightness at the expense of some reduced lamp life.
- Although the SET pins are designed such that just leaving them open or grounding them is all that is needed to set the output current, they can also be actively set. Using a open collector or open drain logic signal will allow you to reduce the lamp current for situations where greater dim range is required, as an example in nighttime situations. In conjunction with a light sensor or other timer the panel could be set to higher brightness (maximum output current) for daytime illumination and lower brightness (minimum or typical output current) at nighttime. Since the dim ratio is a factor of both the burst duty cycle and the peak output current, using this technique the effective dim ratio can be increased greater than the burst duty cycle alone. Conversely the SET inputs could be used to overdrive the lamp temporarily to facilitate faster lamp warm up at initial lamp turn on. Of course any possible degradation on lamp life from such practices is the users responsibility since not all lamps are designed to be overdriven.
- The inverter has a built in fault timeout function. If the output is open (lamp disconnected or broken) or shorted the inverter will attempt to strike the lamp for several seconds. After about 2 to 4 seconds without success the inverter will shutdown. In order to restart the inverter it is necessary to toggle the sleep input or cycle the V_{IN1} input supply.

APPLICATION



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NOTES

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