Battery Disconnect Switch

FEATURES

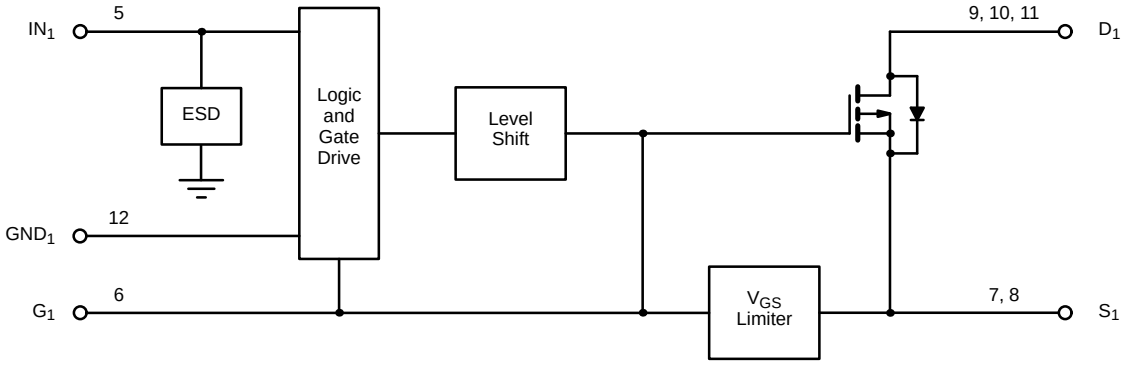
- Solution for Bi-Directional Blocking Bi-Directional Conduction Switch
- 6- to 30-V Operation
- Ground Referenced Logic Level Inputs
- Integrated Low $r_{DS(on)}$ MOSFET
- Level-Shifted Gate Drive with Internal MOSFET
- Two Independent Inputs
- Includes Precision Voltage Circuitry
- Ultra Low Power Consumption in Off State (Leakage Current Only)
- Logic Supply Voltage is Not Required

DESCRIPTION

The Si4719CY is two level-shifted p-channel MOSFETs. Operating together, these MOSFETs can be used as a reverse blocking switch for battery disconnect applications. It is a solution for multiple battery technology designs or designs that require isolation from the power bus during charging.

The Si4719CY is available in a 16-pin SOIC package and is rated for the commercial temperature range of –25 to 85°C.

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



Half a circuit shown here.

ABSOLUTE MAXIMUM RATINGS

Voltage Referenced to GND
V_S, V_D[a] ..... –0.3 V to 32 V
V_{SD} ..... –0.3 V to 30 V
V_{IN1}, V_{IN2} ..... –0.3 V to 15 V
V_{GS} ..... 20 V
Storage Temperature ..... –55 to 150°C

Power Dissipation[b] (t = 10 sec) ..... 2.4 W
(t = steady state) ..... 1.5 W

Notes
a. $V_{SD} \leq 30\ V_{DC}$
b. Device mounted with all leads soldered to 1" x 1" FR4 with laminated copper PC board.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING RANGE

V_S, V_D ..... 6 V to 30 V
V_{IN1}, V_{IN2} ..... 0 V to 13.2 V
I_{DS} ..... 0 A to 6 A

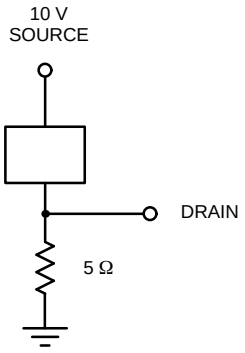
Operating Temperature Range ..... –25 to 85°C
Junction Temperature ..... –25 to 150°C

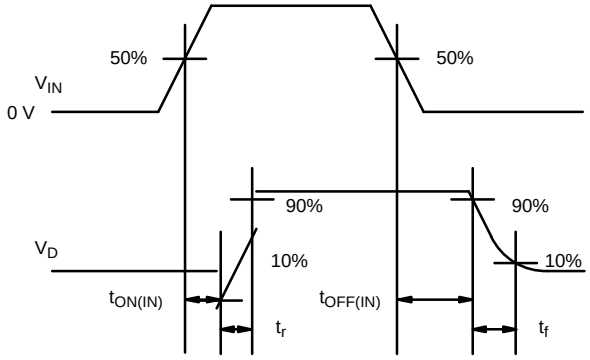
SPECIFICATIONS

Parameter	Symbol	Specific Test Conditions	Limits Temp[a]	Min[b]	Typ[c]	Max[b]	Unit
On-Resistance	r_{DS}	V_S = 10 V, I_D = 1 A, V_{IN} = H	Room		0.028	0.040	Ω
Leakage Current	$I_{DS(off)}$	V_{DS} = 10 V	Room			1	µA
Power Consumption	I_S $GND_{(off)}$	V_S = 21 V	Room			1	µA
	I_S $GND_{(on)}$	V_S = 21 V	Room		1.0	10	µA
Input Voltage Low	V_{INL}	V_S = 10 V and V_S = 21 V	Full			3.3	V
Input Voltage High	V_{INH}	V_S = 10 V and V_S = 21 V	Full	4.5			V
Input Current	I_{INH}	V_{IN} = 5.0 V	Room		25	50	µA
Turn-On Delay (IN to D or S)	$t_{ON(IN)}$	V_S = 10 V, R_L = 5 Ω, Test Circuit 1	Room	2.2	2.9	10	µs
Turn-Off Delay (IN to D or S)	$t_{OFF(IN)}$	V_S = 10 V, R_L = 5 Ω, Test Circuit 1	Room		1.15	2	µs
Break-Before-Make[d]	t_{BBM}		Room		1.15		µs
Rise Time	t_{RISE}		Room		0,73	1.4	µs
Fall Time	t_{FALL}		Room		24	50	ns
Voltage Across Pin 6 and 7	V_{GS}	V_S = 30 V	Room		10.2	18	V
Forward Diode	V_{SD}	I_D = –1 A	Room			1.1	V

Notes
a. Room = 25°C, Full = as determined by the operating temperature suffix.
b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum.
c. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
d. Guaranteed by design, not subject to production testing.

TIMING DIAGRAMS

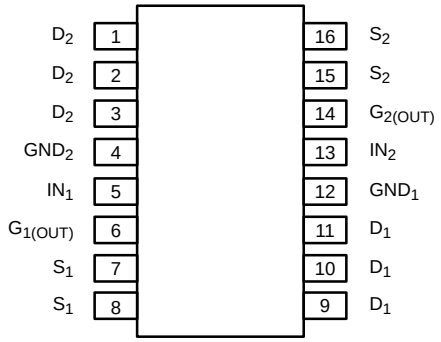




TEST CIRCUIT 1

PIN CONFIGURATION

SO-16



Top View

Order Number: Si4719CY

TRUTH TABLE

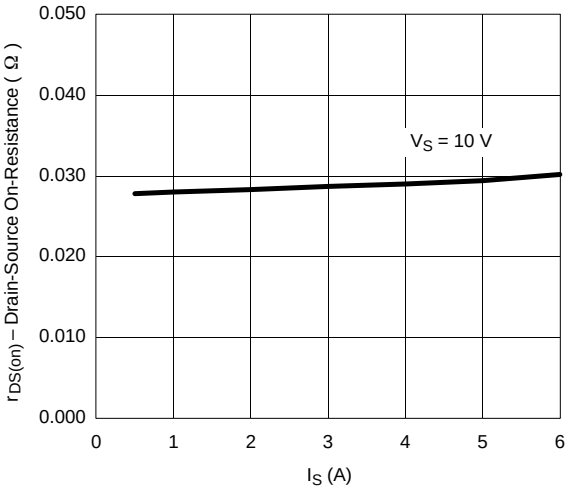
V_{IN1}	V_{IN2}	Switch 1	Switch 2
0	0	Off	Off
0	1	Off	On
1	0	On	Off
1	1	On	On

PIN DESCRIPTION

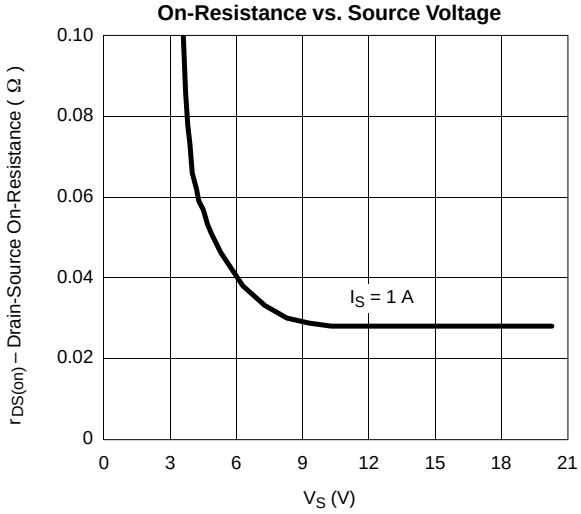
Pin Number	Symbol	Description
1, 2, 3	D_2	Drain connection for MOSFET-2.
4, 12	GND	Ground
5	IN_1	Logic input, IN_1. High level turns on the switch.
6	$G_{1(OUT)}$	Gate output to MOSFET-1.
7, 8	S_1	Source connection for MOSFET-1
9, 10, 11	D_1	Drain connection for MOSFET-1.
13	IN_2	Logic input, IN_2. High level turns on the switch.
14	$G_{2(OUT)}$	Gate output to MOSFET-2.
15, 16	S_2	Source connection for MOSFET-2.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

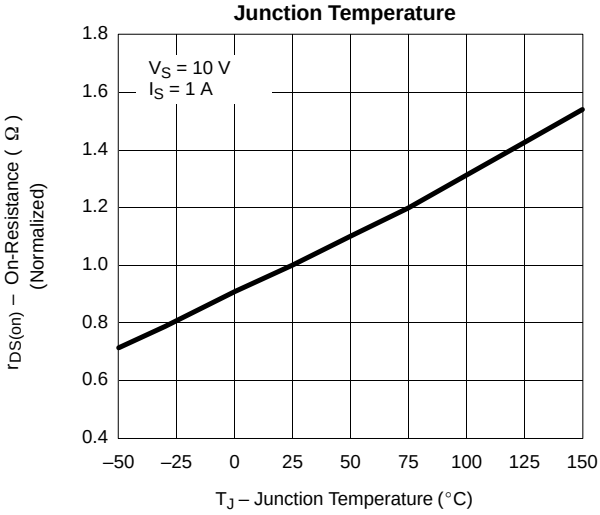
On-Resistance vs. Drain Current



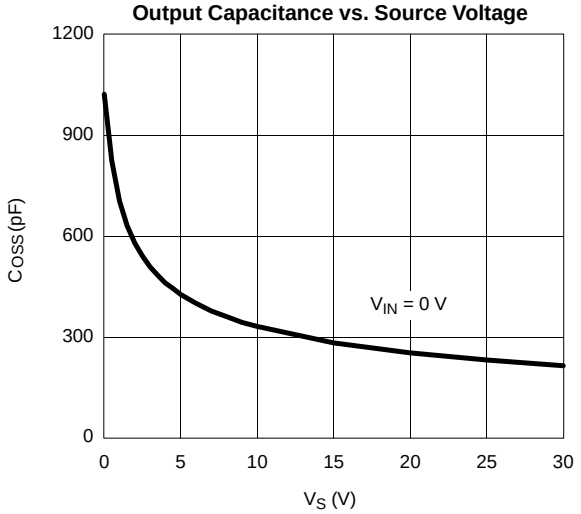
On-Resistance vs. Source Voltage



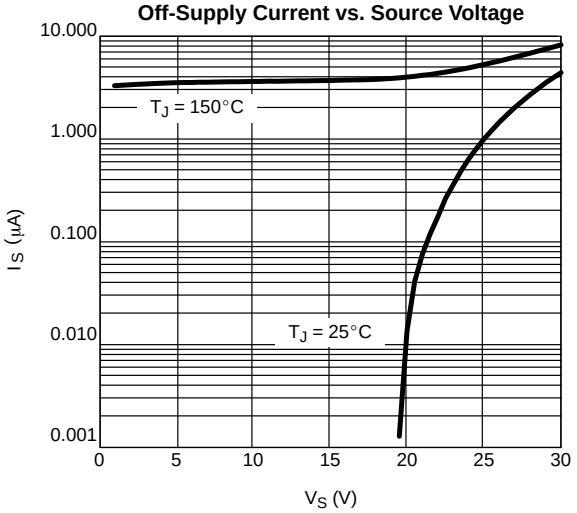
Normalized On-Resistance vs. Junction Temperature



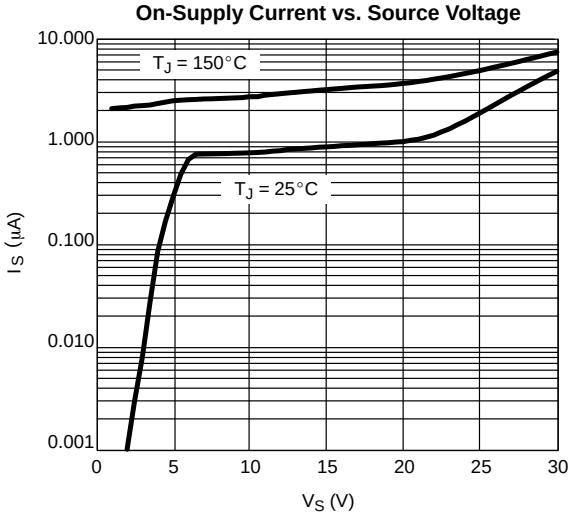
Output Capacitance vs. Source Voltage



Off-Supply Current vs. Source Voltage

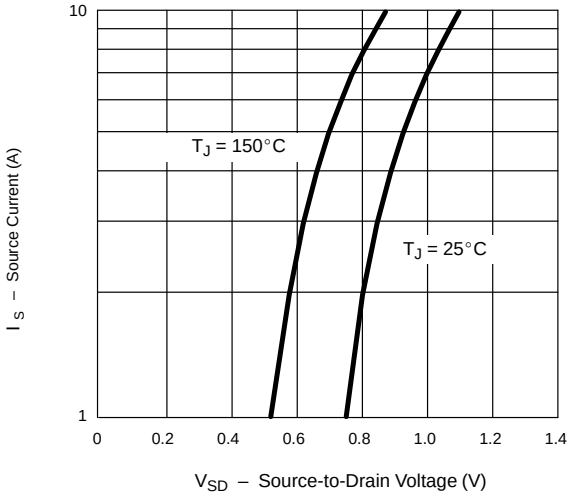


On-Supply Current vs. Source Voltage

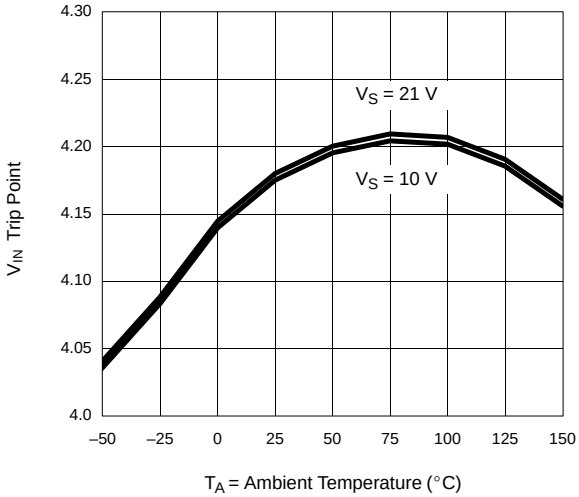


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

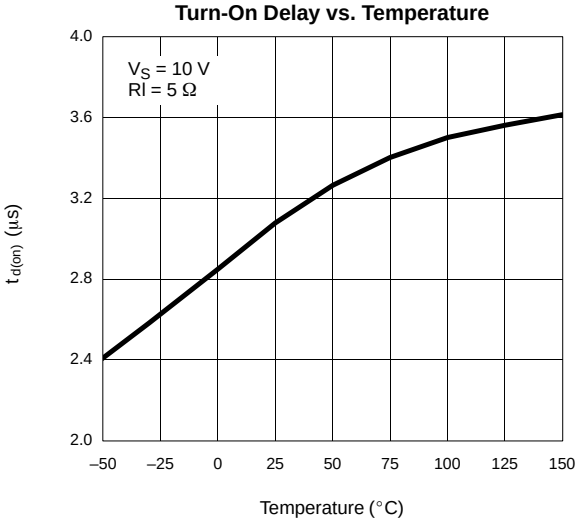
Drain-Source Diode Forward Voltage



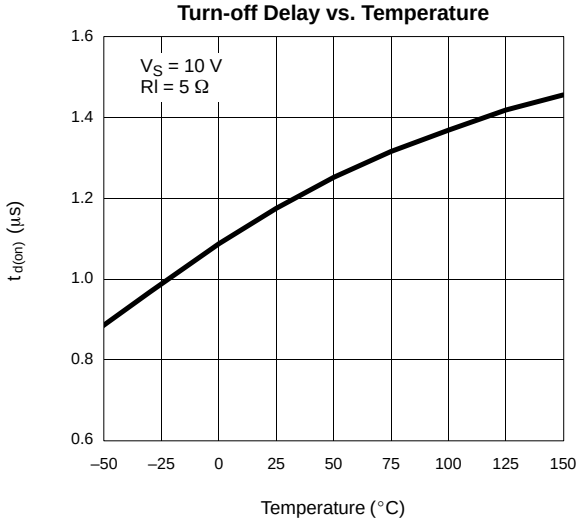
Input Voltage Trip Point vs. Temperature



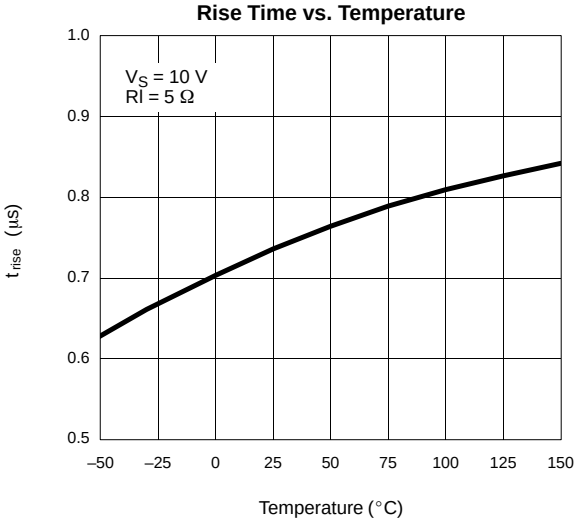
Turn-On Delay vs. Temperature



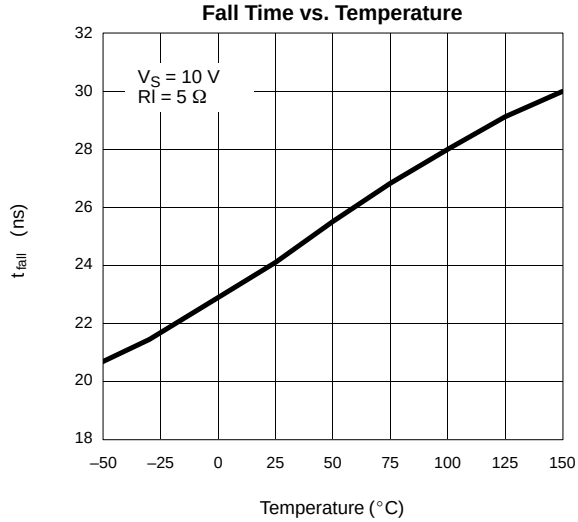
Turn-off Delay vs. Temperature



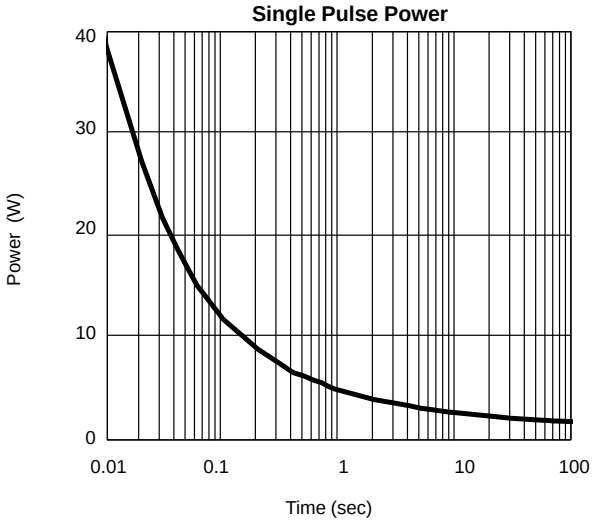
Rise Time vs. Temperature

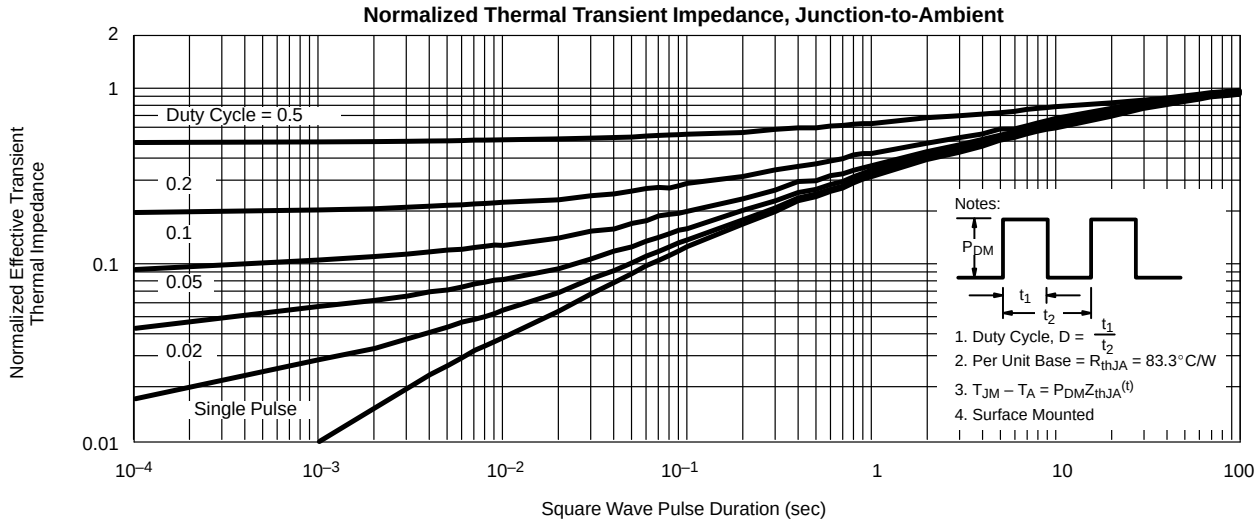


Fall Time vs. Temperature



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





APPLICATION DRAWINGS

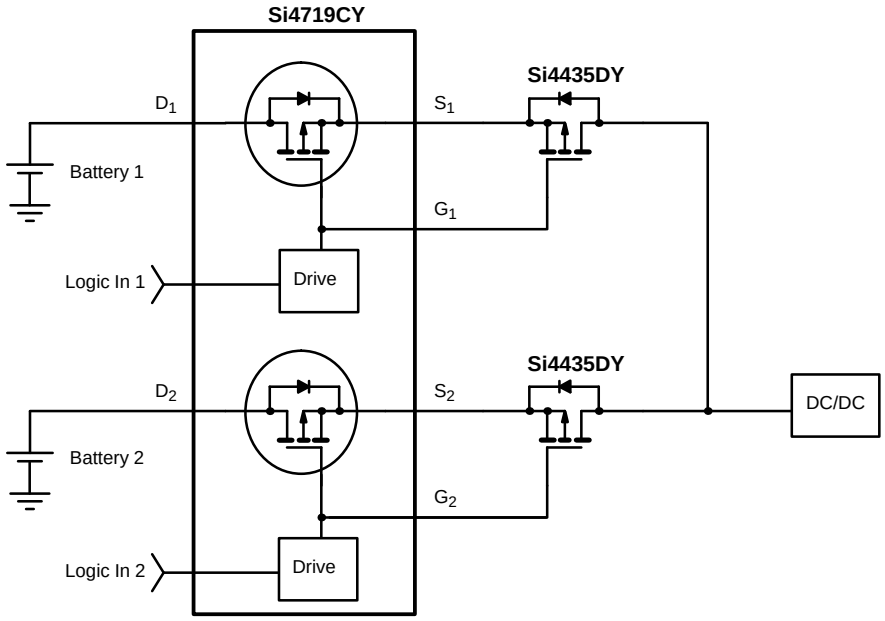


FIGURE 1

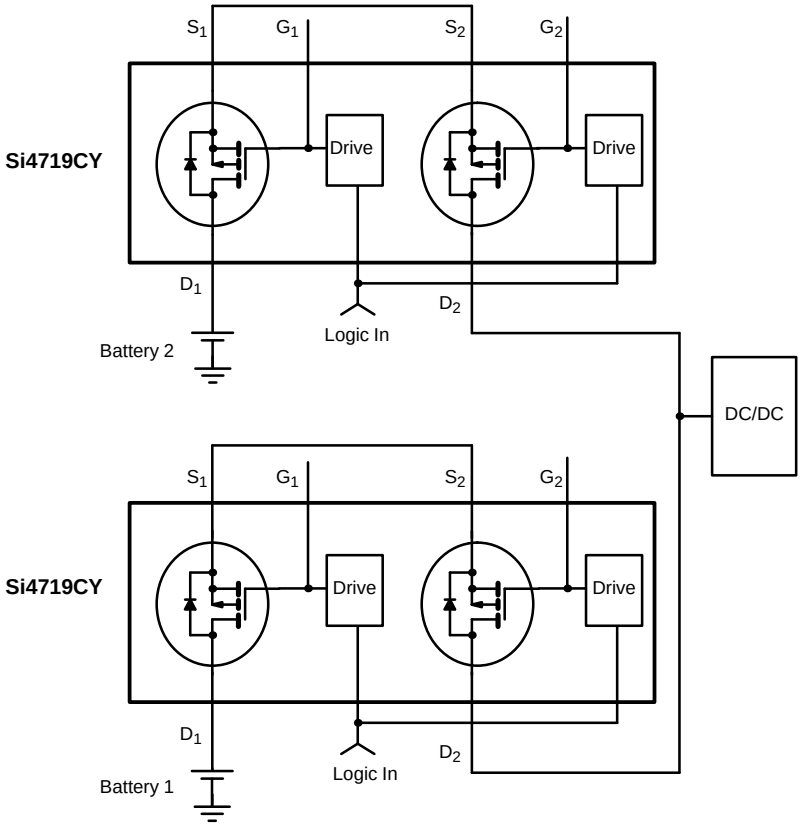


FIGURE 2

APPLICATION DRAWINGS

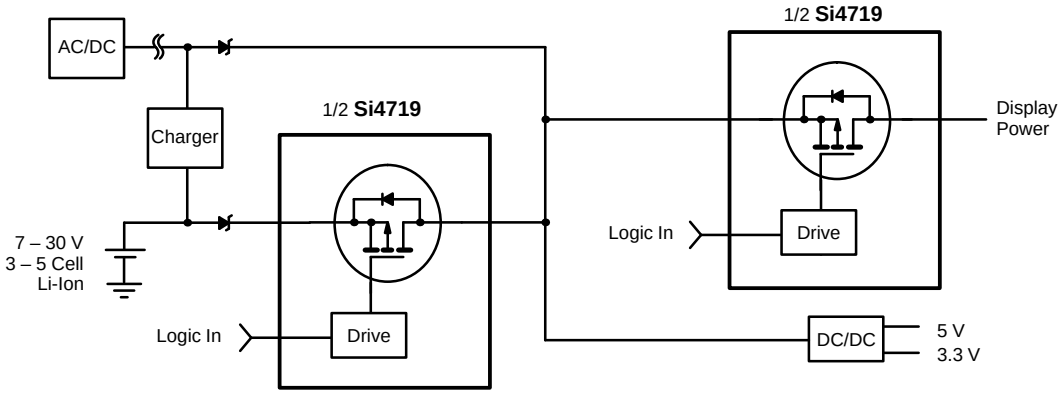


FIGURE 3: Low-Cost Laptop PC

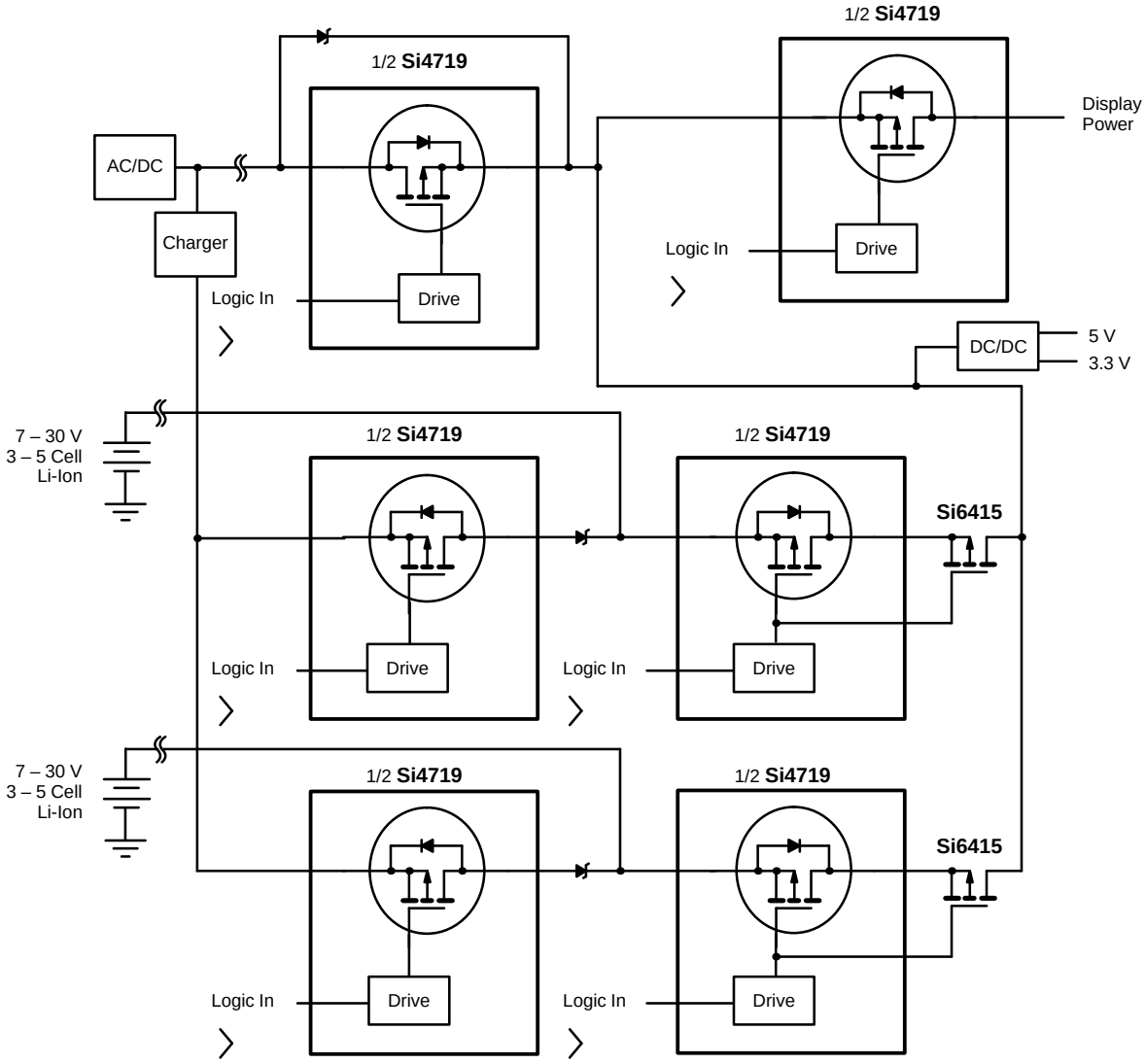


FIGURE 4: High-Performance Laptop PC

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