Microse GRATED PRODUCTS

PanelMatch™

LXM1623-05-44

5V Dual 4W CCFL Programmable Inverter Module

PRODUCTION DATASHEET

DESCRIPTION

The LXM1623-05-44 is a Dual 4W Output Direct Drive[™] CCFL (Cold Technique Cathode Fluorescent Lamp) Inverter brightness control in any wide range Module specifically designed to be typically (50:1+) dimming application. compatible with the Sharp LQ104S1DG21 / 51 10.4", LQ121S1DG31 / 41 12.1" as energizes well as the Samsung LTM121SI-T01 specifically to ensure that no premature 12.1" or similar dual lamp displays that lamp degradation occurs, while allowing both individual have connectors on the top side of the panel

LXM1623 modules provide the designer with a vastly superior display the system battery or AC adapter directly brightness range. This brightness range is to high frequency, high-voltage waves achievable with virtually any LCD display. required to ignite and operate CCFL

dimming input that permits brightness available (LXM1623-12-44). control from either, a DC voltage source, a PWM signal or external potentiometer.

externally programmable (through the cost and performance advantages due to input connector) over a range of 5.2 to the controller's high level of integration. 6.7mA in 0.5mA steps. This allows the inverter to match the panel's lamp current are stable fixed-frequency operation, specifications, or it can be used to secondary-side strike-voltage regulation, purposely drive the lamps at a lower and both open/shorted lamp protection current to save power or lower nominal with fault timeout. brightness.

RangeMAXTM Digital Dimming provides flicker-free

The resultant "burst drive" that the lamp is designed lamp output significant power savings at lower dim levels.

The modules convert DC voltage from The modules are available with a lamps. A 12V input inverter is also

The modules design utilizes Microsemi's LX1689 backlight The maximum output current is controller, which provides a number of

Other benefits of this new topology

KEY FEATURES

- Externally Programmable
- Maximum Output Current Easy to Use Brightness Control
- RangeMAX Wide Range Dimming
- Output Open & Short-Circuit Protection and Automatic Strike-
- Voltage Regulation and Timeout **Fixed Frequency Operation**
- Rated From -20 to 70°C

APPLICATIONS

- LCD's Requiring Both Output Connectors on One Side of Panel
- Sharp LQ104S1DG21/51 and LQ121S1DG31 / 41
- Samsung LTM121SI-T01
- **Desktop Displays**
- Industrial Display Controls

BENEFITS

- Smooth, Flicker Free 2%-100% Full-Range Brightness Control
- Programmable Output Current Allows Inverter to Mate With a Wide Variety of LCD Panel's Specifications
- Output Open Circuit Voltage **Regulation Minimizes Corona** Discharge For High Reliability

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com Protected By U.S. Patents: 5,923,129; 5,930,121; 6,198,234; Patents Pending **PRODUCT HIGHLIGHT UNIVERSAL DIMMING INPUT** "PWM", V_{DC}, OR POTENTIOMETER \neq PWM DC Voltage Potentiomete Signal Source հղու SELECTABLE MAXIMUM OUTPUT CURRENT 5.2MA TO 6.7MA RMS PACKAGE ORDER INFO **INVERTER MATES DIRECTLY** PART NUMBER **OUTPUT CONNECTOR** TO PANEL CONNECTORS JST SM02(8.0)B-BHS-1-TB or LXM1623-05-44 JST BHR-03VS-1 Yeon Ho 20015WR-05A00 Copyright © 2002 Microsemi Rev. 1.0a, 2005-01-17 Integrated Products 11861 Western Avenue, Garden Grove, CA. 92841, 714-898-8121, Fax: 714-893-2570

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ABSOLUTE MAXIMUM RATINGS (NOTE 1)

Input Signal Voltage (V _{IN1})	-0.3V to 6.5V
Input Power	
Output Voltage, no load	Internally Limited to 1800V _{RMS}
Output Current	
Output Power (each output)	
Input Signal Voltage (SLEEP Input)	-0.3V to 5.5V
Input Signal Voltage (BRITE)	-0.3V to 5.5V
Ambient Operating Temperature, zero airflow	
Operating Relative Humidity, non-condensing	
Storage Temperature Range	-40°C to 85°C

Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, will not function optimally.

Parameter	Symbol	Recommended Operating Conditions			Units	
Falameter	Symbol	Min	R.C.	Max	Onits	
Input Supply Voltage Range (Fully Regulated Lamp Current)	V _{IN1}	4.75	5	5.25	V	
Input Supply Voltage Range (Functional)		4.5	5	5.5		
Output Power (each output)	Po		3.5	4.0	W	
Linear BRITE Control Input Voltage Range	V _{BRT_ADJ}	0.5		2.0	V	
Lamp Operating Voltage	VLAMP	450	530	610	V _{RMS}	
Lamp Current (Full Brightness)	IOLAMP	5.2		6.7	mA _{RMS}	
Operating Ambient Temperature Range	T _A	-20		70	°C	

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of 25° C except where otherwise noted.

Parameter	Symbol	Test Conditions	LXM1623-05-44			Units	
Falameter	Symbol	Test conditions	Min	Тур	Max		
OUTPUT PIN CHARACTERISTICS							
Full Bright Lamp Current (each output)	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}$, SLEEP $\ge 2.0V$, $V_{IN1} = 5V_{DC}$ $I_{SET1} = Ground$, $I_{SET2} = Ground$	4.7	5.2	5.7	mA _{RM}	
Full Bright Lamp Current (each output)	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}, \overline{SLEEP} \ge 2.0V, V_{IN1} = 5V_{DC}$ $I_{SET1} = Ground, I_{SET2} = Open$	5.2	5.7	6.2	mA _{RM}	
Full Bright Lamp Current (each output)	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}, \overline{SLEEP} \ge 2.0V, V_{IN1} = 5V_{DC}$ $I_{SET1} = Open, I_{SET2} = Ground$	5.7	6.2	6.7	mA _{R№}	
Full Bright Lamp Current (each output)	I _{L(MAX)}	$V_{BRT_ADJ} \ge 2.0V_{DC}$, SLEEP $\ge 2.0V$, $V_{IN1} = 5V_{DC}$ $I_{SET1} = Open$, $I_{SET2} = Open$	6.2	6.7	7.2	mA _{R№}	
Output Current Lamp to Lamp Deviation	I _{LL%DEV}	$V_{BRT_ADJ} \ge 2.0V_{DC}, \overline{SLEEP} \ge 2.0V, V_{IN1} = 5V_{DC}$ $I_{SET1} = Open, I_{SET2} = Open$		3	10	%	
Min. Average Lamp Current (each output)	I _{L(MIN)}	$V_{BRT_{ADJ}} \le 0.5V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $V_{IN1} = 5V_{DC}$ $I_{SET1} = I_{SET2} = Ground$		0.30		mA _{R№}	
Lamp Start Voltage	V_{LS}	$-20^{\circ}\text{C} < \text{T}_{\text{A}} < 70^{\circ}\text{C}, \text{ V}_{\text{IN1}} > 4.5\text{V}_{\text{DC}}$	1450	1600		V _{RMS}	
Operating Frequency	fo	V_{BRT_ADJ} = 2.5 V_{DC} , SLEEP \geq 2.0V, V_{IN1} = 5V	57	60	63	kHz	
Burst Frequency	f _{BURST}	Output Burst Frequency	222	234	246	Hz	

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	Parameter	Symbol	Test Conditions	LXM1623-05-44			Linit
				Min	Тур	Max	Units
•	BRITE INPUT						
	Input Current	I _{BRT}	$V_{BRT_{ADJ}} = 0V_{DC}$		-300		μA _D
		IBRI	$V_{BRT_{ADJ}} = 3V_{DC}$		50		μA _D
	Minimum Input for Max. Lamp Current	$V_{\text{BRT}_\text{ADJ}}$	I _{O(LAMP)} = Maximum Lamp Current		2.0	2.05	VDC
	Maximum Input for Min. Lamp Current	$V_{\text{BRT}_{\text{ADJ}}}$	I _{O(LAMP)} = Minimum Lamp Current	0.4	0.5		VDO
SLEEP INPUT							
	RUN Mode	V		2.0		V _{IN1}	VDO
	SLEEP Mode	V		-0.3		0.8	VDC
SET1,2 INPUT							
	SET _{1,2} Low Threshold	VL				0.4	V
	Input Current	I _{SET}	$V_{SET} \le 0.4V$		-300		μA
•	POWER CHARACTERISTICS						
	Sleep Current	I _{IN(MIN)}	$V_{IN1} = 5V_{DC}, \ \overline{SLEEP} \le 0.8V$	0.0	10	50	μA _D
	Run Current	I _{IN(RUN)}	$V_{IN1} = 5V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $I_{SET1} = Open$ $I_{SET2} = Ground$, $V_{LAMP} = 500V_{RMS}$		1460		mA
	Efficiency	η	$V_{IN1} = 5V_{DC}$, $\overline{SLEEP} \ge 2.0V$, $I_{SET1} = Open$ $I_{SET2} = Ground, V_{LAMP} = 500V_{RMS}$		82		%

FUNCTIONAL PIN DESCRIPTION

CONN	ΡιΝ	DESCRIPTION					
CN1 (Molex	CN1 (Molex 53261-0890) Mates with 51021-0800 housing, 50079-8100 pins. Mates with LX9501 input cable assembly						
CN1-1	V _{IN1}	Main Input Power Supply (4.75 \leq V _{IN1} \leq 5.25V)					
CN1-2	V IN 1						
CN1-3	GND	Power Supply Return					
CN1-4	GND						
CN1-5	SLEEP	ON/OFF Control. (0V < SLEEP < 0.8 = OFF, SLEEP >= 2.0V = ON					
CN1-6	BRITE	Brightness Control (0.5V to $2.0V_{DC}$). $2.0V_{DC}$ gives maximum lamp current.					
CN1-7	SET ₁	SET ₁ MSB Connecting this pin to ground decreases the output current (see Table 1)					
CN1-8	SET ₂	SET ₂ LSB Connecting this pin to ground decreases the output current (see Table 1)					
CN2, CN3 for LXM1623-05-44 (JST SM02(8.0)B-BHS-1-TB or Yeon Ho 20015WR-05A00)							
CN2-1 CN3-1	V _{HI}	High voltage connection to high Side of lamp. Connect to lamp terminal with shortest lead length. DO NOT connect to Ground.					
CN2-2 CN3-2	V _{LO}	Connection to low side of lamp. Connect to lamp terminal with longer lead length. DO NOT connect to Ground					

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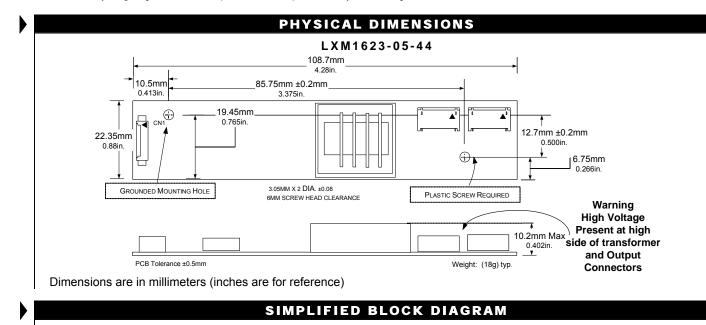
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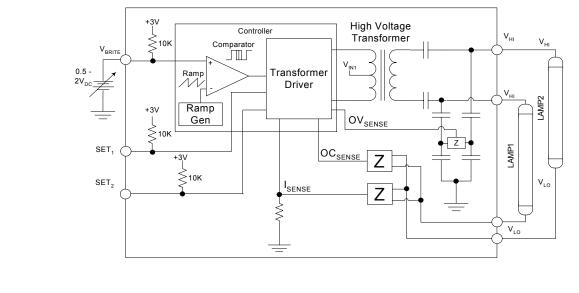
TABLE 1

OUTPUT CURRENT SETTINGS

SET₁ (Pin 7)	SET₂ (Pin 8)	Nominal Output Current
Open*	Open*	6.7mA
Open*	Ground	6.2mA
Ground	Open*	5.7mA
Ground	Ground	5.2mA

* If driven by a logic signal it should be open collector or open drain only, not a voltage source.





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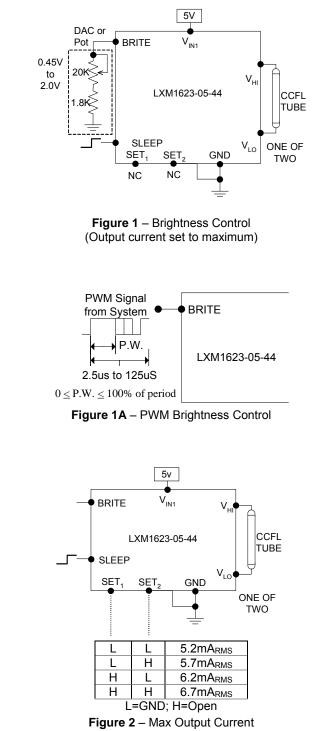


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TYPICAL APPLICATION



 $⁽SET_1 \text{ and } SET_2 \text{ Inputs})$

- The brightness control may be a voltage output DAC or other voltage source, a digital pot or 20K manual pot. The inverter contains an internal 10K pull-up to 3V to bias the pot, add a 1.8K resistor to set the lower threshold voltage. A 3.3V Logic Level PWM signal from a micro-controller may also be used as shown in Figure 1A.
- If you need to turn the inverter ON/OFF remotely, connect to TTL logic signal to the SLEEP input.
- Connect V_{HI} to high voltage wire from the lamp. Connect V_{LO} to the low voltage wire (wire with thinner insulation). Never connect V_{LO} to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to V_{LO}. This wire is typically white.
- Use the SET₁ and SET₂ (see Figure 2) inputs to select the desired maximum output current. Using these two pins in combination allows the inverter to match a wide variety of panels from different manufactures. Generally, the best lamp lifetime correlates with driving the CCFL at the manufactures nominal current setting. However, the SET₁ and SET₂ inputs allow the user the flexibility to adjust the current to the maximum allowable output current to increase panel brightness at the expense of some reduced lamp life.
- Although the SET pins are designed such that just leaving them open or grounding them is all that is needed to set the output current, they can also be actively set. Using a open collector or open drain logic signal will allow you to reduce the lamp current for situations where greater dim range is required, as an example in nighttime situations. In conjunction with a light sensor or other timer the panel could be set to higher brightness (maximum output current) for daytime illumination and lower brightness (minimum or typical output current) at nighttime. Since the dim ratio is a factor of both the burst duty cycle and the peak output current, using this technique the effective dim ratio can be increased greater than the burst duty cycle alone. Conversely, the SET inputs could be used to overdrive the lamp temporarily to facilitate faster lamp warm up at initial lamp turn on. Of course, any possible degradation on lamp life from such practices is the users responsibility since not all lamps are designed to be overdriven.
- The inverter has a built in fault timeout function. If the output is open (lamp disconnected or broken) or shorted the inverter will attempt to strike the lamp for several seconds. After about 3 seconds without success the inverter will shutdown. In order to restart the inverter it is necessary to toggle the sleep input or cycle the V_{IN1} input supply

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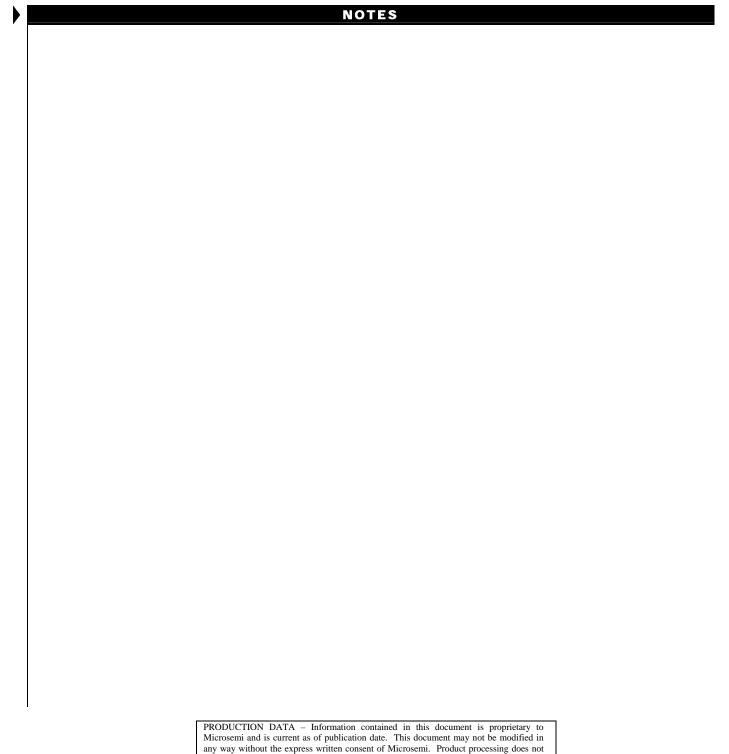


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