



### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Features**

Low On-Resistance:

 $70m\Omega$  @  $V_{GS}$  = -10V,  $I_D$  = -3.8A  $120m\Omega$  @  $V_{GS}$  = -4.5V,  $I_D$  = -3.0A

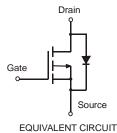
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

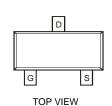
### **Mechanical Data**

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
  Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)

SOT-23







## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characterist	tic		Symbol	Value	Units	
Drain-Source Voltage			$V_{DSS}$	-30	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V		
Drain Current (Note 1) $V_{GS} = -10V$ Steady $T_A = 25^{\circ}C$ State $T_A = 70^{\circ}C$		I <sub>D</sub>	-3.8 -2.9	А		
Pulsed Drain Current (Note 3)		Pulsed Drain Current (Note 3)			А	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	$P_{D}$	1.08	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = 25°C (Note 1)	$R_{ hetaJA}$	115	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

Notes: 1. Device mounted on FR-4 PCB on 2 oz., 0.5 in.  $^2$  copper pads and t  $\leq$ 5 sec.

- No purposefully added lead.
- 3. Pulse width  $\leq 10 \mu S$ , Duty Cycle  $\leq 1\%$ .
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

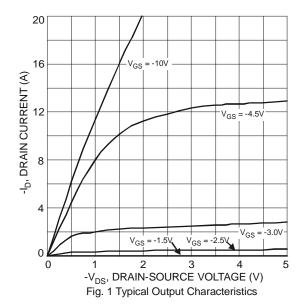


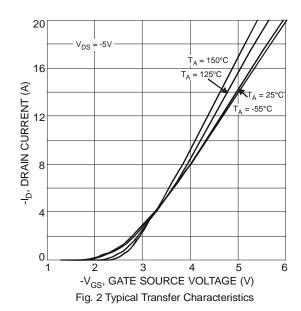
# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30		_	V	$V_{GS} = 0V, I_{D} = -250 \mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-800	nA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>			±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 5)						_	
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.0	-1.8	-2.1	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>	_	56 98	70 120	mΩ	$V_{GS} = -10V, I_D = -3.8A$ $V_{GS} = -4.5V, I_D = -3.0A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	3.6	_	S	$V_{DS} = -5V, I_{D} = -2.7A$	
Diode Forward Voltage (Note 5)	V <sub>SD</sub>	_	_	-1.26	V	$V_{GS} = 0V, I_S = -2.7A$	
DYNAMIC CHARACTERISTICS (Note 6)				•	•		
Input Capacitance	Ciss	_	336	1008	pF		
Output Capacitance	Coss	_	70	210	pF	$V_{DS} = -25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	Crss		49	147	pF	]	
Gate Resistance	$R_G$	1	4.6	_	Ω	$V_{GS} = 0V V_{DS} = 0V, f = 1MHz$	
SWITCHING CHARACTERISTICS (Note 6)							
Total Gate Charge	Qg	_	4.0	8.0	nC	$V_{DS} = -15V$ , $V_{GS} = -4.5V$ , $I_{D} = -3.8A$	
	Ü	_	7.8	_		V 45V V 40V	
Gate-Source Charge	Qgs		1.0	_		$V_{DS} = -15V$ , $V_{GS} = -10V$ , $I_{D} = -3.8A$	
Gate-Drain Charge	$Q_{gd}$		2.5	_		ID = -3.0A	
Turn-On Delay Time	t <sub>d(on)</sub>	_	6.0	12.0			
Rise Time	t <sub>r</sub>		5.0	10.0	ns	$V_{DS} = -15V, V_{GS} = -10V,$	
Turn-Off Delay Time	t <sub>d(off)</sub>		17.6	35.2	115	$I_D = -1A, R_G = 6.0\Omega$	
Fall Time	t <sub>f</sub>		9.5	19.0			

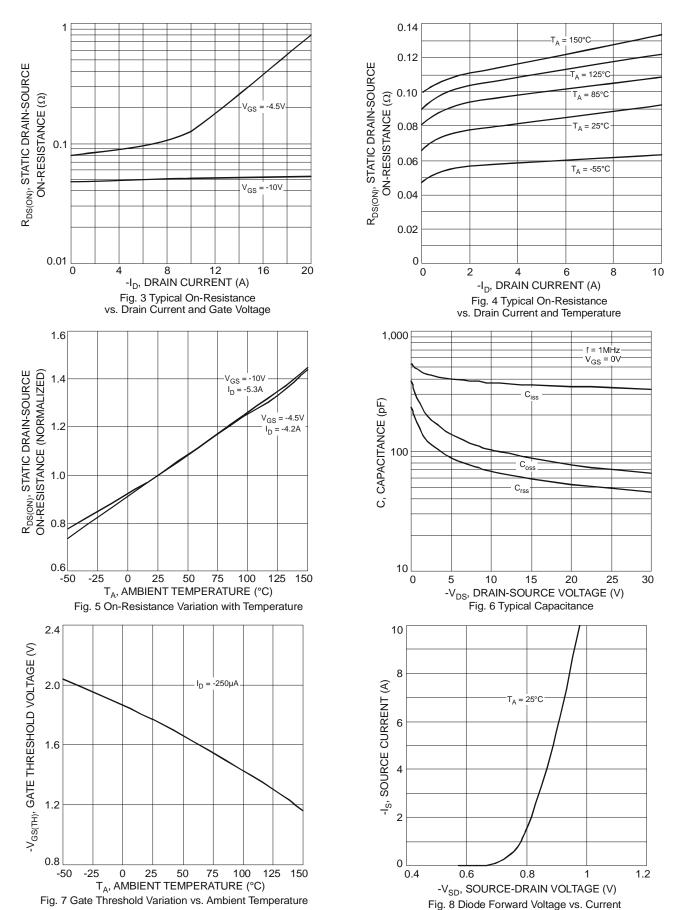
Notes: 5. Short duration pulse test used to minimize self-heating effect.

<sup>6.</sup> Guaranteed by design. Not subject to production testing.









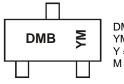


## **Ordering Information** (Note 7)

Part Number	Case	Packaging
DMP3098L-7	SOT-23	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**



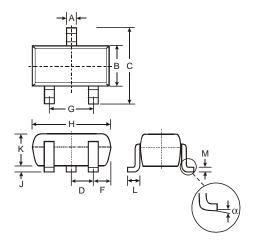
DMB = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

Date Code Key

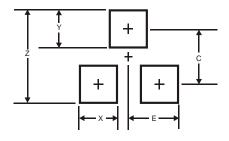
Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

# **Package Outline Dimensions**



SOT-23					
Dim	Min	Max			
Α	0.37	0.51			
В	1.20	1.40			
C	2.30	2.50			
D	0.89	1.03			
F	0.45	0.60			
G	1.78	2.05			
Ι	2.80	3.00			
7	0.013	0.10			
K	0.903	1.10			
L	0.45	0.61			
M	0.085	0.180			
α	0°	8°			

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35



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