

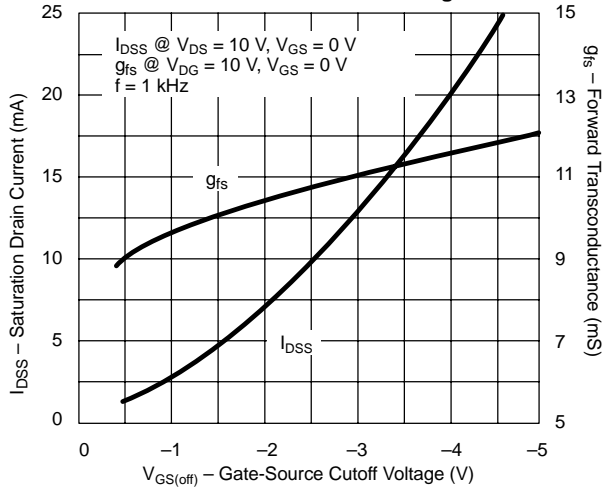
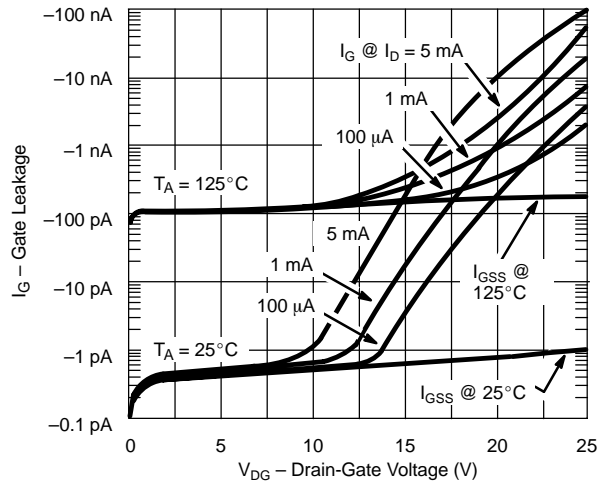
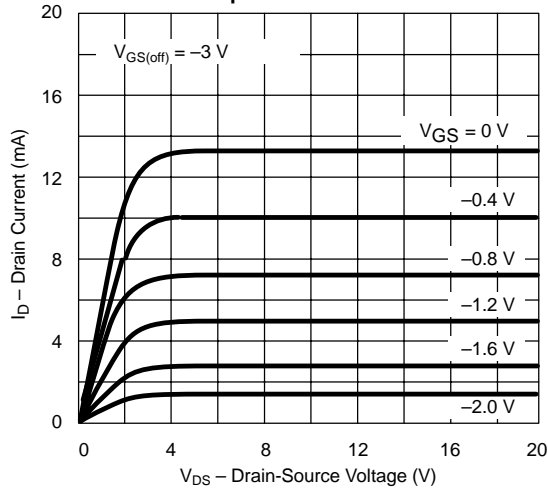
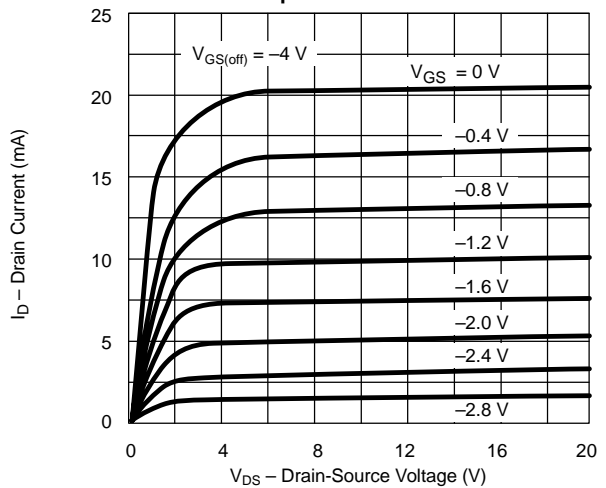
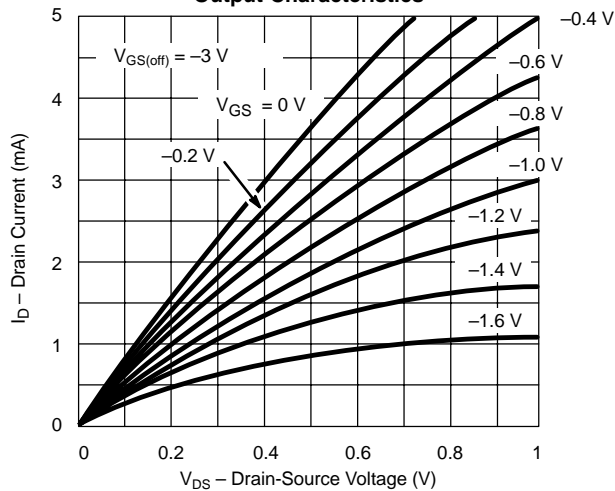
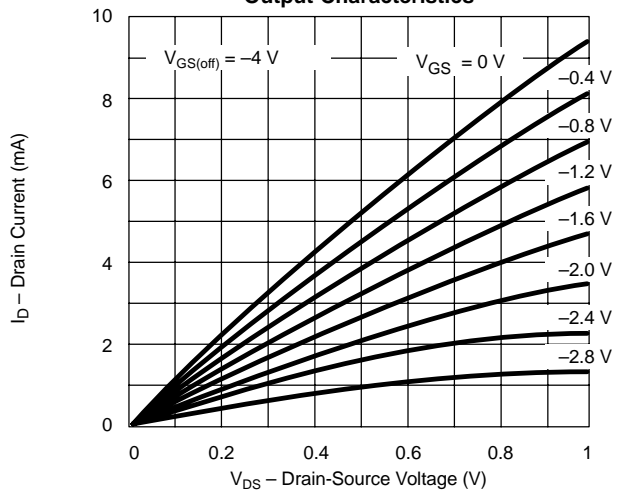


SPECIFICATIONS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ ^a	Max	
Static						
Gate-Source Breakdown Voltage	V _{(BR)GSS}	I _G = -1 μA, V _{DS} = 0 V	-25	-35		V
Gate-Source Cutoff Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 nA	-1	-3.5	-6	
Saturation Drain Current ^b	I _{DSS}	V _{DS} = 10 V, V _{GS} = 0 V	6	15	30	mA
Gate Reverse Current	I _{GSS}	V _{GS} = -15 V, V _{DS} = 0 V		-1	-500	pA
			T _A = 125 °C		-0.2	
Gate Operating Current	I _G	V _{DG} = 10 V, I _D = 5 mA		-1	-500	pA
			T _A = 125 °C		-0.2	
Gate-Source Forward Voltage	V _{GS(F)}	I _G = 1 mA, V _{DS} = 0 V		0.7		V
Dynamic						
Common-Source Forward Transconductance	g _{fs}	V _{DS} = 10 V, I _D = 5 mA f = 1 kHz	4.5	6	9	mS
Common-Source Output Conductance	g _{os}				20	200
Common-Source Forward Transconductance	g _{fs}	V _{DS} = 10 V, I _D = 5 mA f = 100 MHz		5.5		mS
Common-Source Output Conductance	g _{os}				30	
Common-Source Input Capacitance	C _{iss}	V _{DS} = 10 V, I _D = 5 mA f = 1 MHz		3.5		pF
Common-Source Reverse Transfer Capacitance	C _{rss}				1	
Equivalent Input Noise Voltage	e _n	V _{DS} = 10 V, I _D = 5 mA f = 10 kHz		4		nV/ √Hz
Matching						
Differential Gate-Source Voltage	V _{GS1} - V _{GS2}	V _{DG} = 10 V, I _D = 5 mA		7	20	mV
Gate-Source Voltage Differential Change with Temperature	$\frac{\Delta V_{GS1} - V_{GS2} }{\Delta T}$	V _{DG} = 10 V, I _D = 5 mA T _A = -55 to 125 °C		10		μV/°C
Saturation Drain Current Ratio ^c	$\frac{I_{DSS1}}{I_{DSS2}}$	V _{DS} = 10 V, V _{GS} = 0 V		0.98		
Transconductance Ratio ^c	$\frac{g_{fs1}}{g_{fs2}}$	V _{DS} = 10 V, I _D = 5 mA f = 1 kHz		0.98		
Common Mode Rejection Ratio	CMRR	V _{DG} = 10 to 15 V, I _D = 5 mA		90		dB

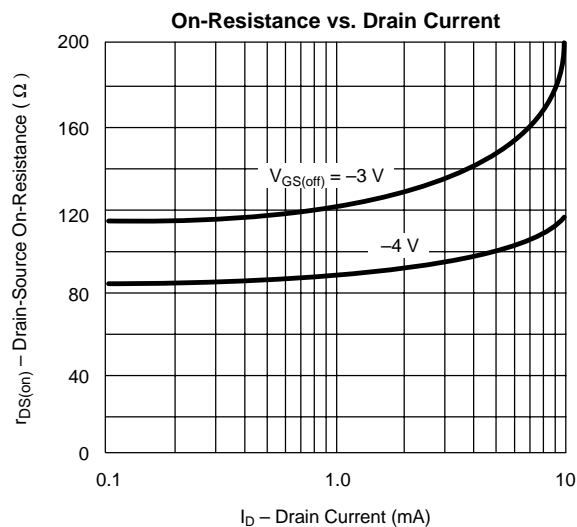
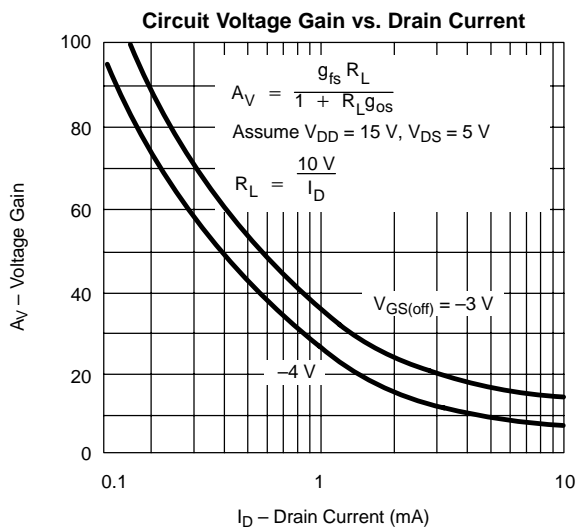
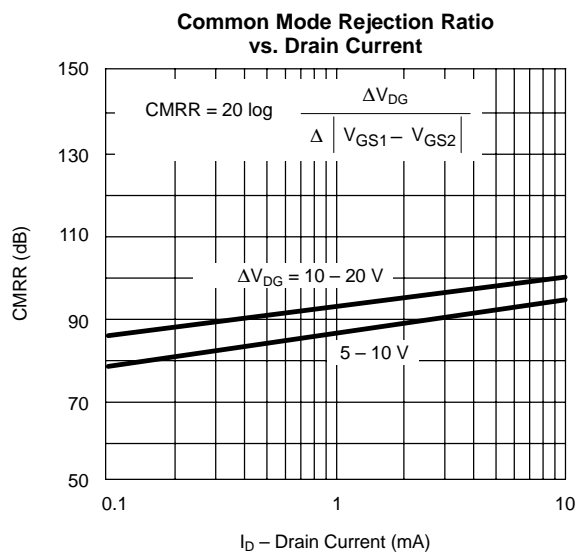
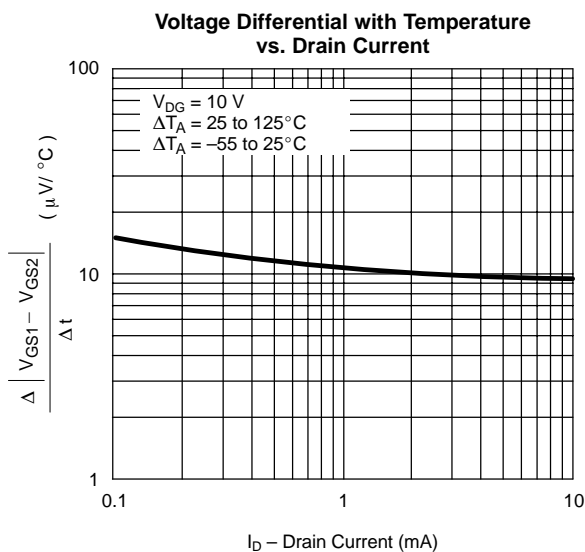
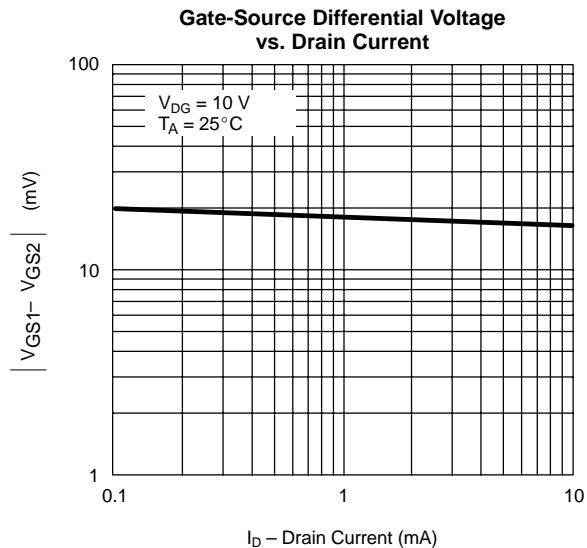
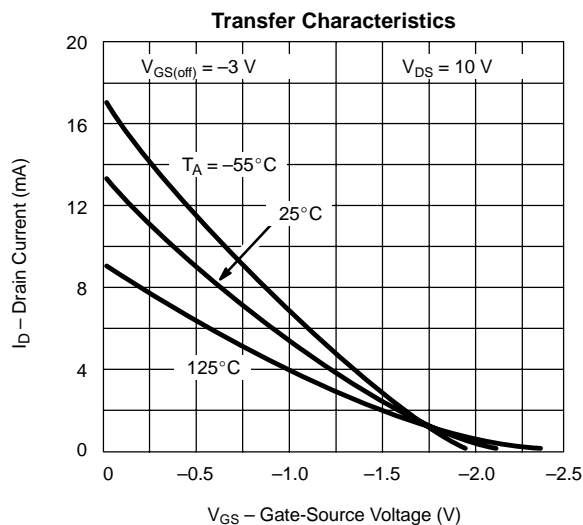
Notes

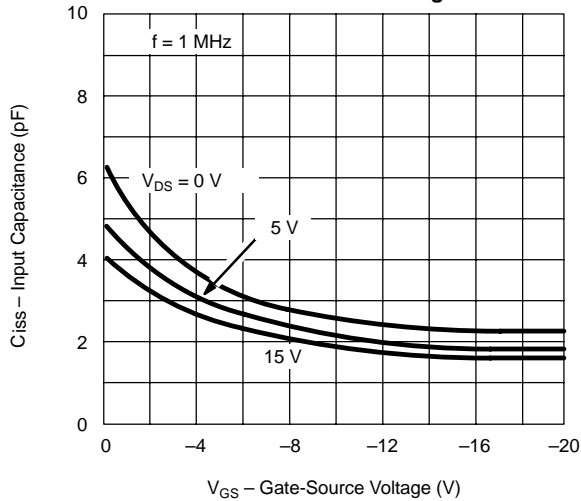
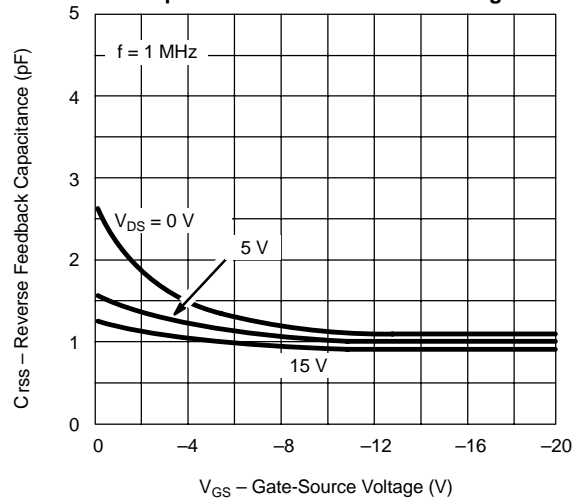
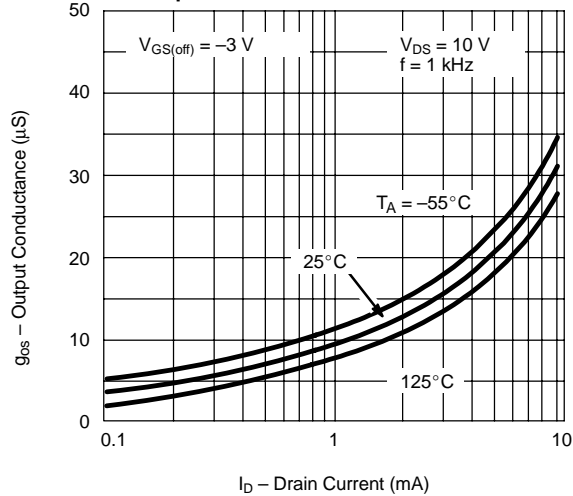
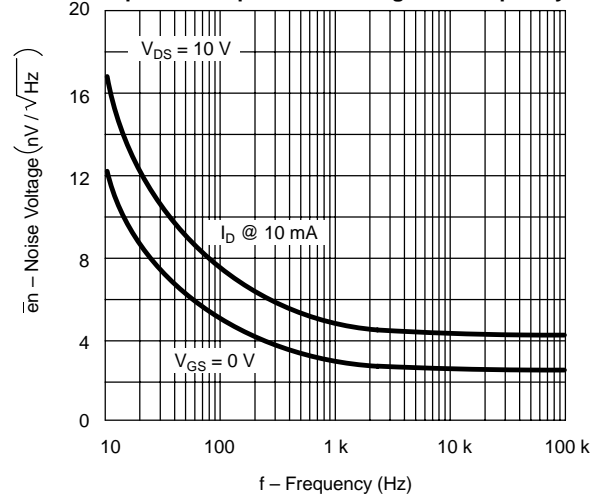
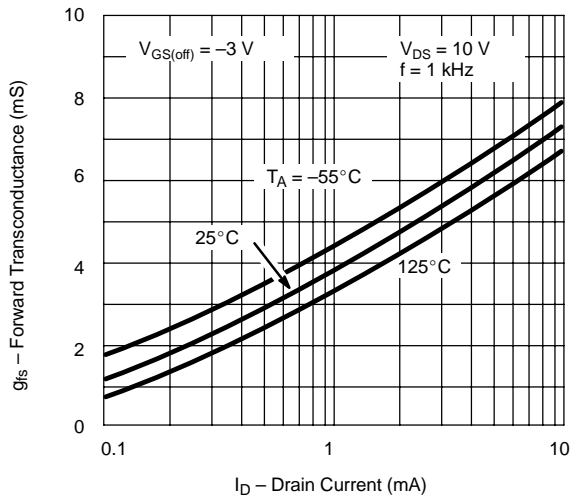
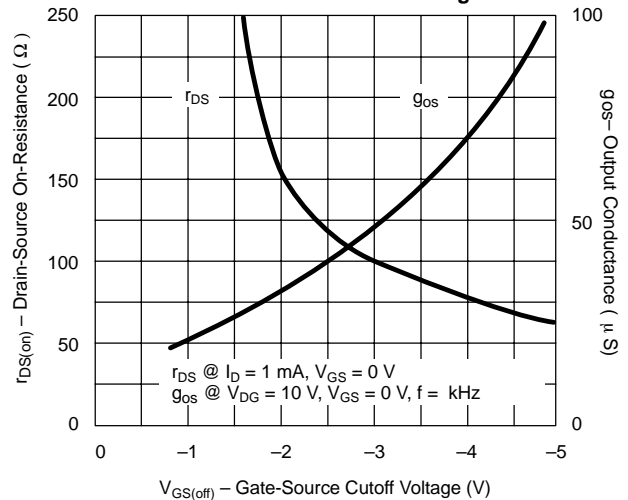
- Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
- Pulse test: PW ≤ 300 μs duty cycle ≤ 3%.
- Assumes smaller value in the numerator.

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TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)
Drain Current and Transconductance vs. Gate-Source Cutoff Voltage

Gate Leakage Current

Output Characteristics

Output Characteristics

Output Characteristics

Output Characteristics


TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)



TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)
Common-Source Input Capacitance vs. Gate-Source Voltage

Common-Source Reverse Feedback Capacitance vs. Gate-Source Voltage

Output Conductance vs. Drain Current

Equivalent Input Noise Voltage vs. Frequency

Common-Source Forward Transconductance vs. Drain Current

On-Resistance and Output Conductance vs. Gate-Source Cutoff Voltage




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