

Memory Module Specification

KVR400D2S3/512

512MB 64M x 64-Bit DDR2-400 CL3 200-Pin SODIMM

DESCRIPTION:

This document describes ValueRAM's 64M x 64-bit (512MB) DDR2-400 CL3 SDRAM (Synchronous **DRAM**) memory module. The components on this module include eight 64M x 8-bit (16M x 8-bit x 4 Bank) DDR2-400 SDRAM in FBGA packages. This 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

FEATURES:

✓ Clock Cycle Time (tCK) CL=3

Row Cycle Time (tRC)

Refresh to Active/Refresh Command Time (tRFC)

Row Active Time (tRAS)

Single Power Supply of

5ns (min.) / 8ns (max.)

5ns (min.) / 75ns

40ns (min.) / 70,000ns (max.)

+1.8V (+/-.1V)

Single Power Supply of +1.8 v (+/- .1 v)

Power 1.404 W (operating)

☑ UL Rating 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 30.00mm.

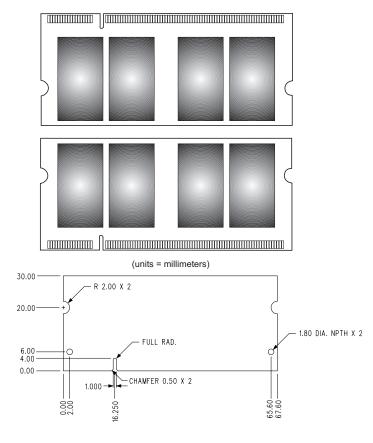


Figure 1: KVR400D2S3/512 200-Pin SODIMM