

Memory Module Specification

KVR100X64SC2/64 100MHz SDRAM 144-Pin SODIMM Memory Module

DESCRIPTION:

This document describes ValueRAM's CL2 8M x 64-bit (64MB) SDRAM (Synchronous DRAM) PC100 Small Outline DIMM memory module. The components on this module include eight 4M x 16-bit (1M x 16-bit x 4 Bank / PC100 components) 4K Refresh SDRAM in TSOP packages. This 144-pin SODIMM uses gold contact fingers and requires +3.3V. The electrical and mechanical specifications are as follows:

FEATURES:

- \checkmark CLK Cycle Time (tCC)
- \mathbf{A} Row Cycle Time (tRC)
- \checkmark CLK to Output (tSAC)
- \mathbf{Y} Single Power Supply of
- Dever Power
- **ULRating**

min.= 10ns (CAS Latency = 2 / max. = 1000ns 70ns max. = 6ns (CAS Latency = 2) +3.3V (+/-.3V) 2.101 W (operating) 94 V - 0

Note: The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 1.250" (31.75mm).

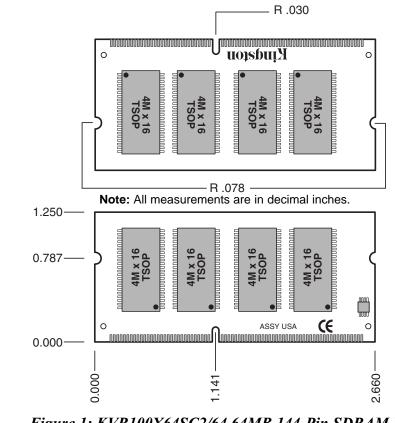


Figure 1: KVR100X64SC2/64 64MB 144-Pin SDRAM Module

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