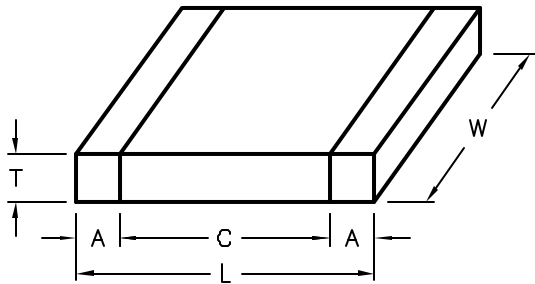


DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1893	A	RELEASED	EO	8/25/08	JN	04/16/08	JN	04/16/08


 RoHS
Compliant

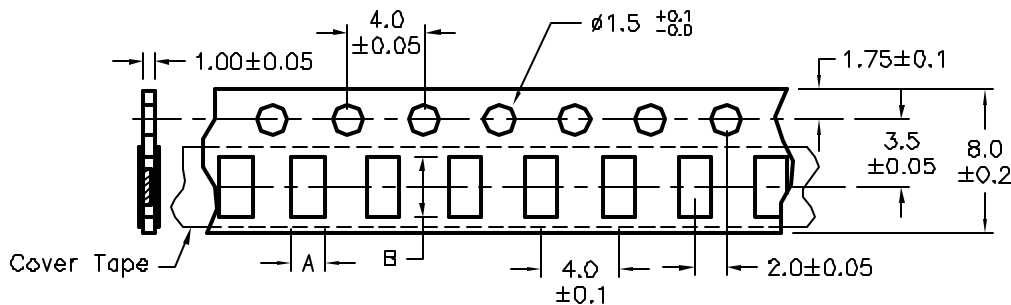
Multicomp P/N	Capacitance	Tolerance (± %)	Dielectric	EIA Size
MCS1206W223K1HRN-P4-RH	0.022 μF	10	X7R	1206
MCS1206W473K1HRN-P4-RH	0.047 μF	10	X7R	1206
MCS1206W104K1HRN-P4-RH	0.1 μF	10	X7R	1206
MCS1206W103K1HRN-P4-RH	0.01 μF	10	X7R	1206
MCS1206N100J1HRN-P4-RH	10 pF	5	NPO	1206
MCS1206N101J1HRN-P4-RH	100 pF	5	NPO	1206
MCS1206N102J1HRN-P4-RH	1000 pF	5	NPO	1206
MCS1206N222J1HRN-P4-RH	2200 pF	5	NPO	1206
MCS1206N271J1HRN-P4-RH	270 pF	5	NPO	1206
MCS1206N561J1HRN-P4-RH	560 pF	5	NPO	1206
MCS0805W104K1HRN-P4-RH	0.1 μF	10	X7R	0805
MCS0805Z104M1HRN-P4-RH	0.1 μF	20	Z5U	0805



EIA Size	Dimensions				A
	L	W	T (Max.)	C	
0805	2.0±0.15	1.25±0.15	1.25	0.55	0.25 ~ 0.75
1206	3.2±0.20	1.60±0.20	1.27	1.4	

EIA Size	Paper Tape Dimensions	
	A	B
0805	1.65±0.05	2.4±0.05
1206	2.00±0.10	3.5±0.10

Packaging: Paper Tape



SPC-F004.DWG

TOLERANCES: UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.	DRAWN BY:	DATE:	DRAWING TITLE:			
	EKLAS ODISH	8/25/08	Multilayer Ceramic Chip Capacitors			
	CHECKED BY:	DATE:	SIZE	DWG. NO.	ELECTRONIC FILE	REV
	Jason Nash	04/16/08	A	TA-800	TA-800.DWG	A
	APPROVED BY:	DATE:	SCALE:	U.O.M.:	SHEET:	
	Jason Nash	04/16/08	NTS	Millimeters	1 OF 2	

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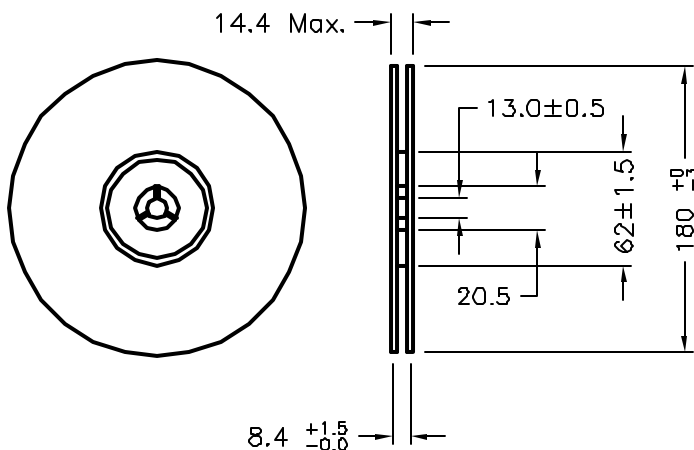
Electrical Characteristics

NPO

Characterizes	Test Conditions	Requirement
Operating Temperature Range		-55°C ~ +125°C
Temperature Coefficient (TC)	With respect to 25°C with operating temperature range	C < 10pf, ±150ppm/°C C = 10pF, ±30ppm/°C
Dissipative Factor (Tan δ)	With respect to 25°C C = 1000pF, 1Vrms/1MHz C > 1000pF, 1Vrms/1KHz	C < 10pf Tanδ = 10(3/C+0.7)×10E-4 or 30×10E-4 whichever is less C = 10pF Tanδ = 10×10E-4
Insulation Resistance (IR)	Vr (rated voltage) = 500V at Vr for 1 minute Vr (rated voltage) = 500V at 500V for 1 minute	Rins > 100GΩ or Rins xC = 1000S whichever is less
Dielectric Withstanding Voltage	Vr (rated voltage) = 100V at 2.5Vr for 5 seconds Vr (rated voltage) = 200/250V at 1.5Vr+100V for 5 seconds Vr (rated voltage) = 500V at 1.2Vr for 5 seconds	No Breakdown

X7R/Z5U

Characterizes	Test Conditions	Requirement
Operating Temperature Range		X7R = -55°C ~ +125°C Z5U = +10°C ~ +85°C
Temperature Coefficient (TC)	With respect to 25°C with operating temperature range	X7R = ±15% Z5U = +30% ~ -80%
Dissipative Factor (Tan δ)	With respect to 25°C C = 1000pF, 1Vrms/1MHz C > 1000pF, 1Vrms/1KHz	X7R = 50V Tanδ = 2.5% 25V Tanδ = 3.5% 16V Tanδ = 5% Z5U = 50V Tanδ = 4% 25V Tanδ = 6%
Insulation Resistance (IR)	Vr (rated voltage) = 500V at Vr for 1 minute Vr (rated voltage) = 500V at 500V for 1 minute	Rins > 10GΩ or Rins xC = 1000S whichever is less
Dielectric Withstanding Voltage	Vr (rated voltage) = 100V at 2.5Vr for 5 seconds Vr (rated voltage) = 200/250V at 1.5Vr+100V for 5 seconds Vr (rated voltage) = 500V at 1.2Vr for 5 seconds	No Breakdown



Reel

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SPC-F004.DWG

SIZE DWG. NO.

A

TA-800

ELECTRONIC FILE

TA-800.DWG

REV

A

DOC. NO. SPC-F004 * Effective: 7/8/02 * DCP No: 1398

SCALE: NTS

U.O.M.: Millimeters

SHEET: 2 OF 2