

International  
**IOR** Rectifier

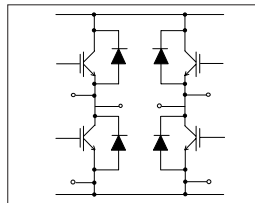
25MT060WFA

"FULL-BRIDGE" IGBT MTP

Warp Speed IGBT

#### Features

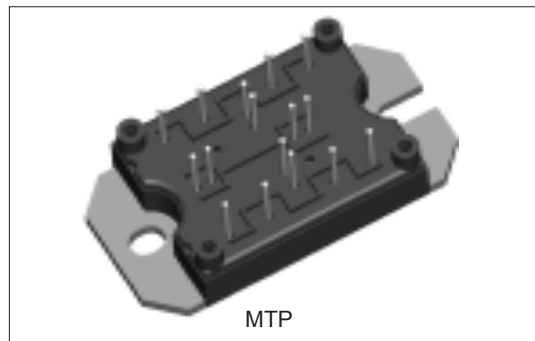
- Gen. 4 Warp Speed IGBT Technology
- HEXFRED™ Antiparallel Diodes with UltraSoft Reverse Recovery
- Very Low Conduction and Switching Losses
- Optional SMT Thermistor
- Al<sub>2</sub>O<sub>3</sub> DBC
- Very Low Stray Inductance Design for High Speed Operation



50 A  
 $V_{CES} = 600V$

#### Benefits

- Optimized for Welding, UPS and SMPS Applications
- Operating Frequencies > 20 kHz Hard Switching, >200 kHz Resonant Mode
- Low EMI, requires Less Snubbing
- Direct Mounting to Heatsink
- PCB Solderable Terminals
- Very Low Junction-to-Case Thermal Resistance
- UL pending



#### Absolute Maximum Ratings

Parameters		Max	Units	
$V_{CES}$	Collector-to-Emitter Voltage	600	V	
$I_C$	Continuous Collector Current	@ $T_C = 72^\circ\text{C}$	50	
		@ $T_C = 100^\circ\text{C}$	32	
$I_{CM}$	Pulsed Collector Current	200	A	
$I_{LM}$	Peak Switching Current	200		
$I_F$	Diode Continuous Forward Current	@ $T_C = 100^\circ\text{C}$		25
$I_{FM}$	Peak Diode Forward Current	200		
$V_{GE}$	Gate-to-Emitter Voltage	$\pm 20$		V
$V_{ISOL}$	RMS Isolation Voltage, Any Terminal to Case, $t = 1$ min	2500	W	
$P_D$	Maximum Power Dissipation	@ $T_C = 25^\circ\text{C}$		195
		@ $T_C = 100^\circ\text{C}$	78	

**Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)**

Parameters	Min	Typ	Max	Units	Test Conditions
V <sub>(BR)CES</sub> Collector-to-Emitter Breakdown Voltage	600			V	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250μA
ΔV <sub>(BR)CES</sub> /ΔT <sub>J</sub> Temperature Coeff. of Breakdown Voltage		+0.6		V/°C	V <sub>GE</sub> = 0V, I <sub>C</sub> = 4mA (25-125°C)
V <sub>CE(ON)</sub> Collector-to-Emitter Saturation Voltage		2.22	3.14	V	V <sub>GE</sub> = 15V, I <sub>C</sub> = 25A
		2.43	3.25		V <sub>GE</sub> = 15V, I <sub>C</sub> = 50A
		1.65	1.93		V <sub>GE</sub> = 15V, I <sub>C</sub> = 25A T <sub>J</sub> = 150°C
		2.08	2.45		V <sub>GE</sub> = 15V, I <sub>C</sub> = 50A T <sub>J</sub> = 150°C
V <sub>GE(th)</sub> Gate Threshold Voltage	3		6		V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 250μA
ΔV <sub>GE(th)</sub> /ΔT <sub>J</sub> Temperature Coeff. of Threshold Voltage		-17		mV/°C	V <sub>CE</sub> = V <sub>GE</sub> , I <sub>C</sub> = 250μA (25-125°C)
g <sub>fe</sub> Transconductance		43		S	V <sub>CE</sub> = 100V, I <sub>C</sub> = 25A, PW = 80μs
I <sub>CES</sub> Zero Gate Voltage Collector Current <sup>(1)</sup>			250	μA	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V, T <sub>J</sub> = 25°C
			10	mA	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V, T <sub>J</sub> = 150°C
I <sub>GES</sub> Gate-to-Emitter Leakage Current			±250	nA	V <sub>GE</sub> = ± 20V
V <sub>FM</sub> Diode Forward Voltage Drop		1.36	1.64	V	I <sub>C</sub> = 25A
		1.57	1.93		I <sub>C</sub> = 50A
		1.19	1.42		I <sub>C</sub> = 25A, T <sub>J</sub> = 150°C
		1.48	1.80		I <sub>C</sub> = 50A, T <sub>J</sub> = 150°C

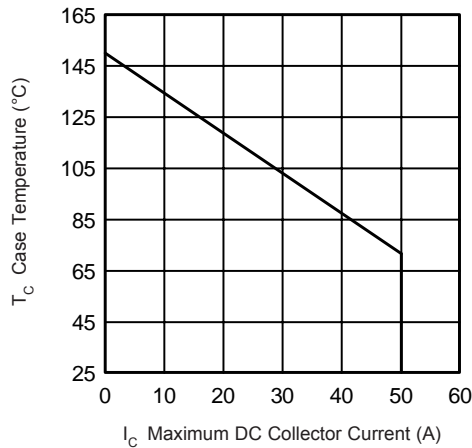
(1) I<sub>CES</sub> includes also opposite leg overall leakage**Switching Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)**

Parameters	Min	Typ	Max	Units	Test Conditions
Q <sub>g</sub> Total Gate Charge (turn-on)		175	263	nC	I <sub>C</sub> = 25A
Q <sub>ge</sub> Gate-Emitter Charge (turn-on)		27	41		V <sub>CC</sub> = 480V
Q <sub>gc</sub> Gate-Collector Charge (turn-on)		71	107		V <sub>GE</sub> = 15V
E <sub>on</sub> Turn-On Switching Loss		134	201	μJ	R <sub>g</sub> = 5Ω, I <sub>C</sub> = 25A
E <sub>off</sub> Turn-Off Switching Loss		415	623		V <sub>CC</sub> = 480V
E <sub>ts</sub> Total Switching Loss		549	824		V <sub>GE</sub> = ±15V
E <sub>on</sub> Turn-On Switching Loss		391	586	μJ	R <sub>g</sub> = 5Ω, I <sub>C</sub> = 25A
E <sub>off</sub> Turn-Off Switching Loss		492	738		V <sub>CC</sub> = 480V
E <sub>ts</sub> Total Switching Loss		883	1324		V <sub>GE</sub> = ±15V, T <sub>J</sub> = 125°C
C <sub>ies</sub> Input Capacitance		3610	5415	pF	V <sub>GE</sub> = 0V
C <sub>oes</sub> Output Capacitance		714	1071		V <sub>CC</sub> = 30V
C <sub>res</sub> Reverse Transfer Capacitance		58	87		f = 1.0 MHz
t <sub>rr</sub> Diode Reverse Recovery Time		50		ns	V <sub>R</sub> = 200V, I <sub>C</sub> = 25A
I <sub>rr</sub> Diode Peak Reverse Current		4.5		A	di/dt = 200A/μs
Q <sub>rr</sub> Diode Recovery Charge		112		nC	
di <sub>(rec)</sub> /dt Diode Peak Rate of Fall of Recovery During t <sub>b</sub>		250		A/μs	

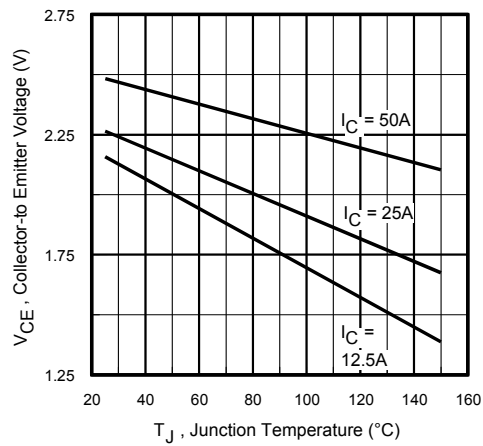
**Thermal- Mechanical Specifications**

Parameters		Min	Typ	Max	Units
$T_J$	Operating Junction Temperature Range	- 40		150	°C
$T_{STG}$	Storage Temperature Range	- 40		125	
$R_{thJC}$	Junction-to-Case	IGBT		0.64	°C/ W
		Diode		0.9	
$R_{thCS}$	Case-to-Sink (Heatsink Compound Thermal Conductivity = 1 W/mK)		0.06		
	Clearance <sup>(2)</sup> (external shortest distance in air between two terminals)	5.5			mm
	Creepage <sup>(2)</sup> (shortest distance along external surface of the insulating material between 2 terminals)	8			
	Weight		66		g

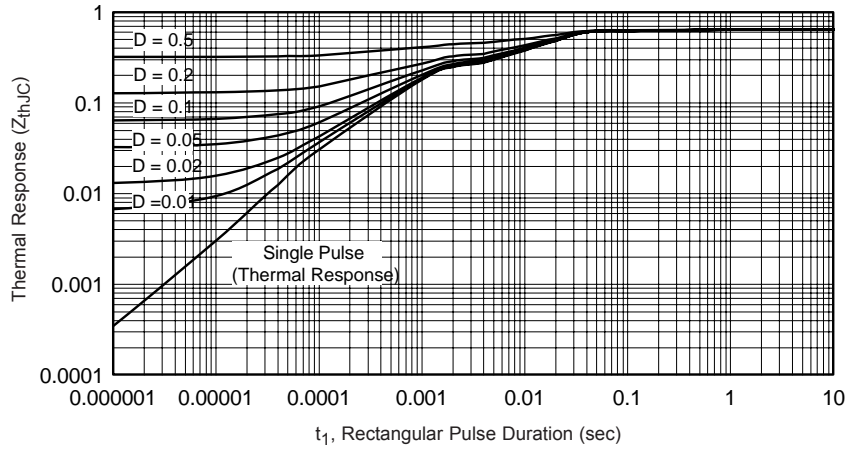
(2) Standard version only i.e. without optional thermistor



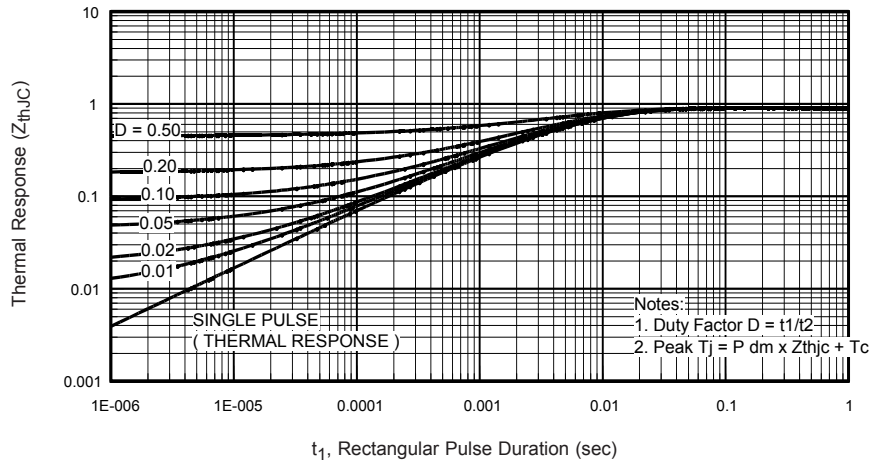
**Fig. 4** - Maximum Collector Current vs. Case Temperature



**Fig. 5** - Typical Collector-to-Emitter Voltage vs. Junction Temperature

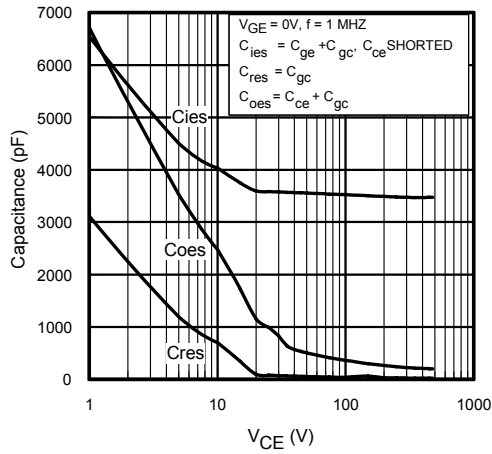


**Fig. 6a** Maximum Transient Thermal Impedance, Junction-to-Case (IGBT)

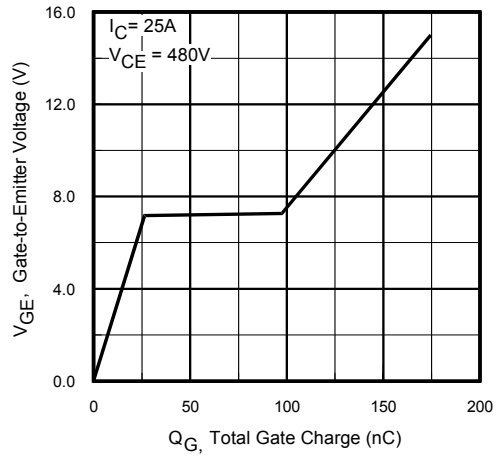


**Fig. 6b** Maximum Transient Thermal Impedance, Junction-to-Case (DIODE)

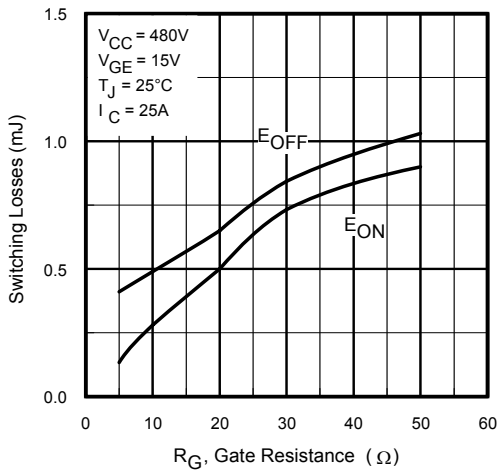
$t_1$ , Thermal Response



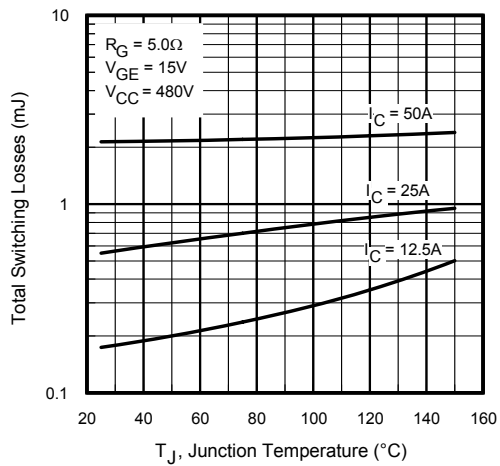
**Fig. 7** - Typical Capacitance vs. Collector-to-Emitter Voltage



**Fig. 8** - Typical Gate Charge vs. Gate-to-Emitter Voltage



**Fig. 9** - Typical Switching Losses vs. Gate Resistance



**Fig. 10** - Typical Switching Losses vs. Junction Temperature

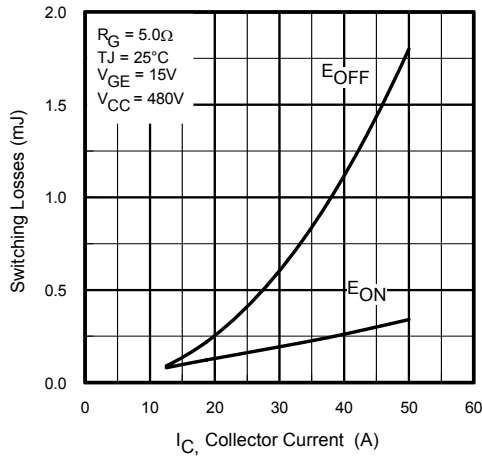


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

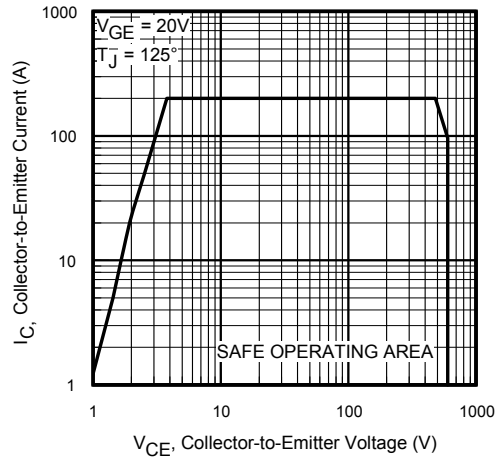


Fig. 12 - Turn-Off SOA

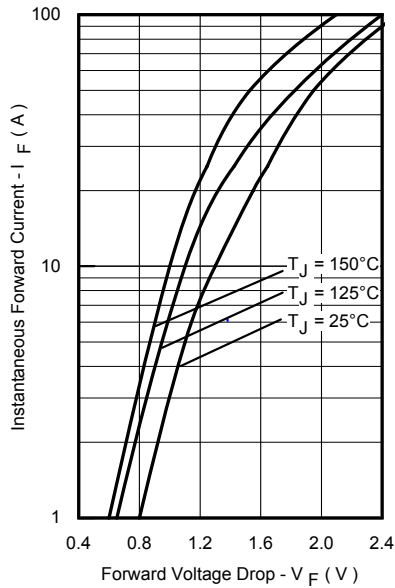


Fig. 13 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

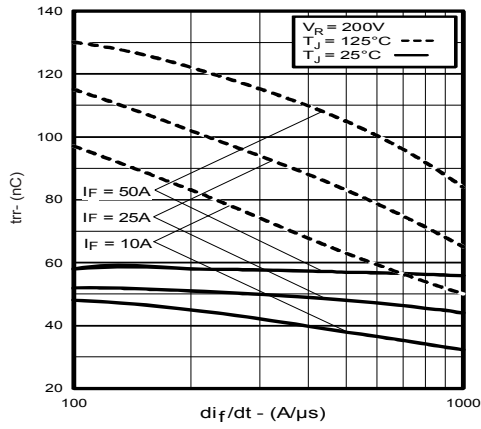


Fig. 14 - Typical Reverse Recovery vs.  $di/dt$

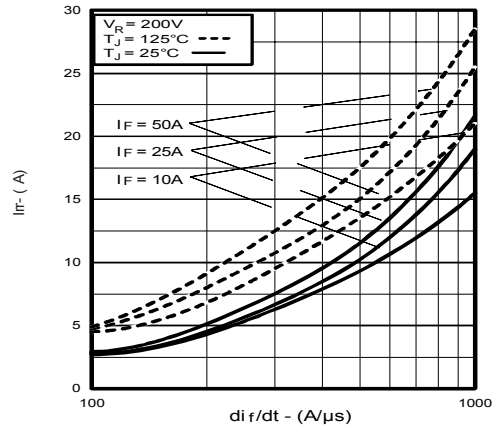


Fig. 15 - Typical Recovery Current vs.  $di/dt$

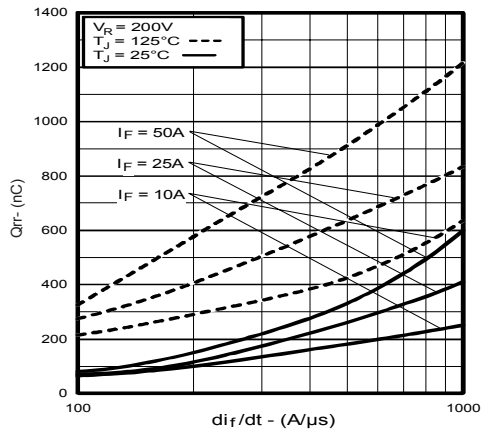


Fig. 16 - Typical Stored Charge vs.  $di/dt$

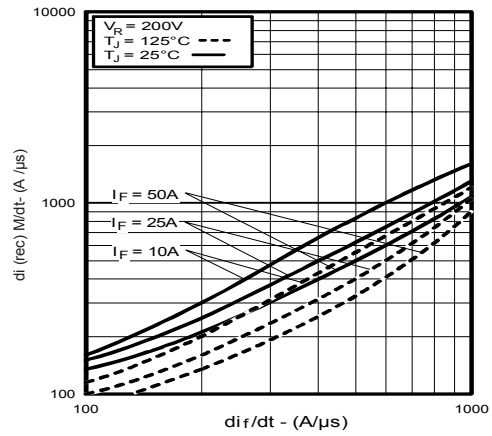
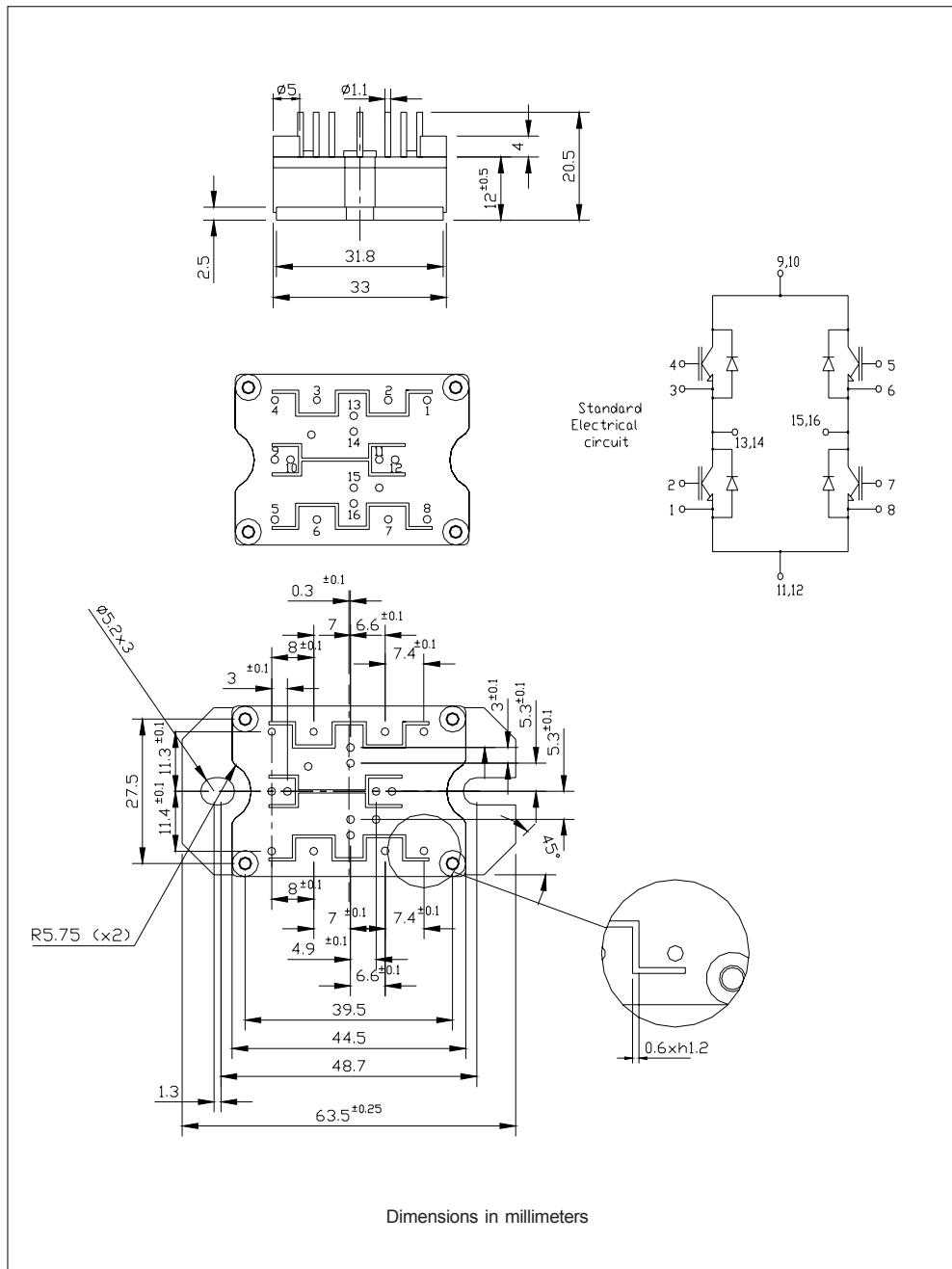


Fig. 17 - Typical  $di_{(rec)M}/dt$  vs.  $di/dt$

**Outline Table**





### Ordering Information Table

Device Code					
<b>25</b>	<b>MT</b>	<b>060</b>	<b>W</b>	<b>F</b>	<b>A</b>
①	②	③	④	⑤	⑥
<b>1</b>	-	Current rating	(25 = 25A)		
<b>2</b>	-	Essential Part Number			
<b>3</b>	-	Voltage code	(060 = 600V)		
<b>4</b>	-	Speed/ Type	(W = Warp® IGBT)		
<b>5</b>	-	Circuit Configuration	(F = Full Bridge)		
<b>6</b>	-	A = Al <sub>2</sub> O <sub>3</sub> DBC Substrate			

Data and specifications subject to change without notice.  
This product has been designed and qualified for Industrial Level.  
Qualification Standards can be found on IR's Web site.

International  
**IR** Rectifier

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06/06



## Notice

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