

TDA2320

PREAMPLIFIER FOR INFRARED REMOTE CONTROL SYSTEMS

NOT FOR NEW DESIGN

- WIDE SUPPLY VOLTAGE RANGE: 4 TO 20V
- SINGLE OR SPLIT SUPPLY OPERATION
- VERY LOW CURRENT CONSUMPTION: 0.8mA
- VERY LOW DISTORTION: 0.03% TYPICA



The TDA2320 is a monolithic integrated circuit in Dip package especially designed to amplify the IR signal in remote controlled TV or radio sets. It directly interfaces the digital control circuitry.

The TDA2320 incorporates a two-stage amplifier with excellent sensitivity and high noise immunity. It can work with a single 5V supply voltage and flash or carrier transmission modes as provided for example by the M709A/M710A/MOS transmitters.

The TDA2320 is particularly intended to be used in conjunction with the M104 and M206 + M3870 remote control receivers.

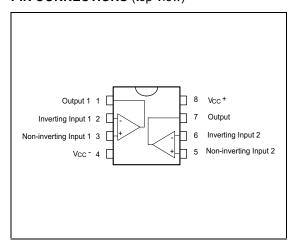
ORDER CODE

Part Number	Temperature Range	Pacl	kage		
Part Number	remperature hange	N D			
TDA2320	-40°C, +105°C	•	•		
Example: TDA2320N					

N = Dual in Line Package (DIP

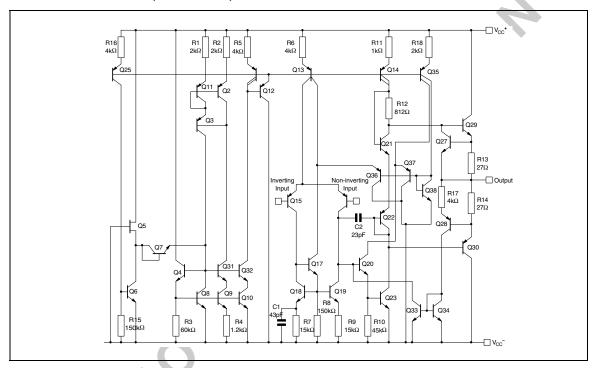


PIN CONNECTIONS (top view)



March 2005 1/8

SCHEMATIC DIAGRAM (1/2 TDA2320)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	20	V
p _{tot}	Total Power Dissipation at T _{amb} = 70°C ¹⁾	400	mW
T _{stg} ,T _j	Storage and Junction Temperature	-40 to 150	°C

^{1.} Power dissipation must be considered to ensure maximum junction temperature (Tj) is not exceeded.

ELECTRICAL CHARACTERISTICS

 $V_{CC} = 5V$, $T_{amb} = 25^{\circ}C$ (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit
V _{CC}	Supply Voltage	4		20	V
I _{CC}	Supply Current V _{CC} = 20V		0.8	2	mA
I _{ib}	Input Bias Current		100	500	nA
V _{io}	Input Offset Voltage $R_s \le 10k\Omega$		0.5		mV
l _{io}	Input Offset Current		15		nA
A _{vd}	Large Signal Voltage Gain f = 1kHz f = 100kHz	64	70 30		dB
V _{opp}	DC Output Voltage Swing		2.5		V
GBP	Gain-bandwidth Product f = 200kHz	1.5	3		MHz
SR	Slew Rate $R_L = 2k\Omega$		1.5		V/μs
e _n	Equivalent Input Noise Voltage f = 40kHz Rs = 10kΩ		20		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
PSRR	Power Supply Rejection Ratio f = 100Hz		80		dB



Figure 1: Supply Current versus Supply Voltage

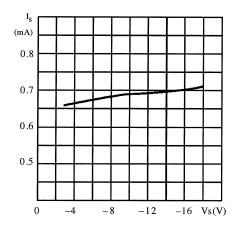


Figure 3: Output Short Circuit Current versus Ambient Temperature

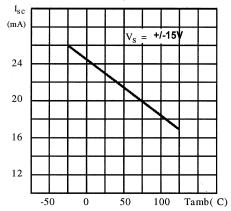


Figure 5 : Output Loop Gain versus Ambient Temperature

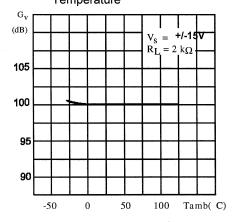


Figure 2 : Supply Current versus Ambient Temperature

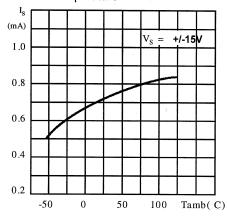


Figure 4 : Open Loop Frequency and Phase Response

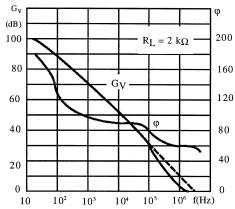
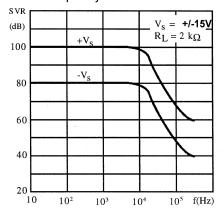


Figure 6 : Supply Voltage Rejection versus Frequency



 $oldsymbol{\mathcal{L}}_{2}$



Figure 7: Large Signal Frequency Response

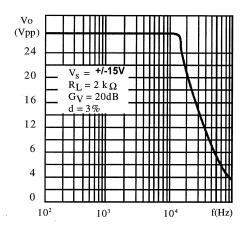


Figure 8 : Output Voltage Swing versus Load Resistance

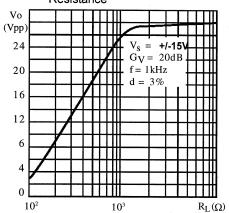


Figure 9: Total Input Noise versus Frequency

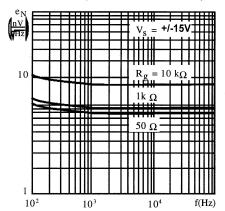


Figure 10 : Amplitude Response

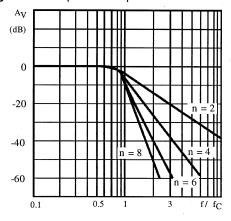


Figure 11: Amplitude Response (±1dB ripple)

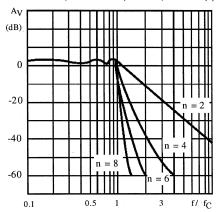


Figure 12: Filter Configuration

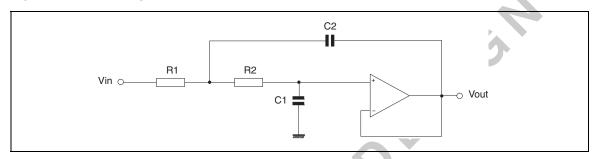


Figure 13: 5th Order Low-pass Filter (Butterworth) with Unity Gain configuration

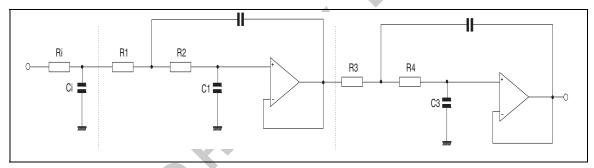
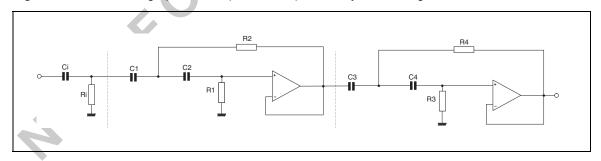


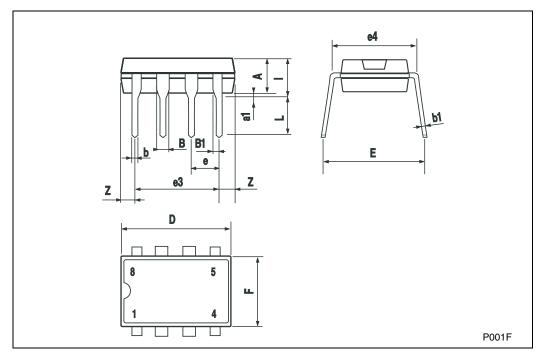
Figure 14: 5th Order High-pass Filter (Butterworth) with Unity Gain configuration



PACKAGE MECHANICAL DATA

Plastic DIP-8 MECHANICAL DATA

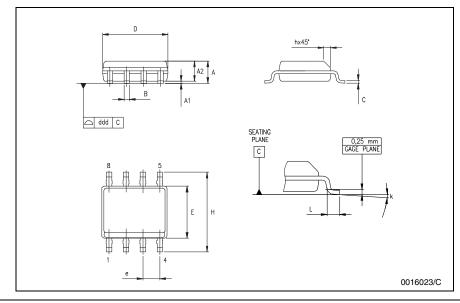
DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α		3.3			0.130	
a1	0.7			0.028		
В	1.39		1.65	0.055		0.065
B1	0.91		1.04	0.036		0.041
b		0.5			0.020	
b1	0.38		0.5	0.015		0.020
D			9.8			0.386
E		8.8			0.346	
е		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			7.1			0.280
I			4.8			0.189
L		3.3			0.130	
Z	0.44		1.6	0.017		0.063



PACKAGE MECHANICAL DATA

SO-8 MECHANICAL DATA

DIM	mm.			inch		
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
E	3.80		4.00	0.150		0.157
е		1.27			0.050	
Н	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k	8° (max.)					
ddd			0.1			0.04



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