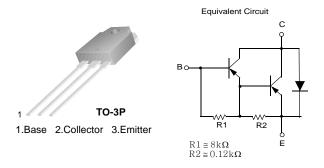


October 2009

TIP145 / TIP146 / TIP147 PNP Epitaxial Silicon Darlington Transistor

Features

- Monolithic Construction With Built In Base-Emitter Shunt Resistors
- High DC Current Gain : h_{FE} = 1000 @ V_{CE} = -4V, I_{C} = -5A (Min.)
- Industrial Use
- Complement to TIP140/141/142



Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Units		
V _{CBO}	Collector-Base Voltage : TIP145 : TIP146 : TIP147	- 60 - 80 - 100	V V V		
V _{CEO}	Collector-Emitter Voltage : TIP145 : TIP146 : TIP147	- 60 V - 80 V - 100 V			
V _{EBO}	Emitter-Base Voltage	- 5	V		
I _C	Collector Current (DC)	- 10	- 10 A		
I _{CP}	Collector Current (Pulse)	- 15 A			
I _B	Base Current (DC)	- 0.5	Α		
P _C	Collector Dissipation (T _C =25°C)	125	W		
TJ	Junction Temperature	150	150 °C		
T _{STG}	Storage Temperature	- 65 to +150	- 65 to +150 °C		

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

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$\textbf{Electrical Characteristics*} \ \, \text{T}_{\text{A}}\text{=-}25^{\circ}\text{C unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage : TIP145 : TIP146 : TIP147	I _C = - 30mA, I _B = 0	- 60 - 80 - 100			V V V
I _{CEO}	Collector Cut-off Current : TIP145 : TIP146 : TIP147	$V_{CE} = -30V, I_{B} = 0$ $V_{CE} = -40V, I_{B} = 0$ $V_{CE} = -50V, I_{B} = 0$			- 2 - 2 - 2	mA mA mA
I _{CBO}	Collector Cut-off Current : TIP145 : TIP146 : TIP147	$V_{CB} = -60V, I_{E} = 0$ $V_{CB} = -80V, I_{E} = 0$ $V_{CB} = -100V, I_{E} = 0$			- 1 - 1 - 1	mA mA mA
I _{EBO}	Emitter Cut-off Current	$V_{BE} = -5V, I_{C} = 0$			- 2	mA
h _{FE}	DC Current Gain	$V_{CE} = -4V, I_{C} = -5A$ $V_{CE} = -4V, I_{C} = -10A$	1000 500			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 5A, I _B = - 10mA I _C = - 10A, I _B = - 40mA			- 2 - 3	V V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = - 10A, I _B = - 40mA			- 3.5	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -4V, I_{C} = -10A$			- 3	V
t _D	Delay Time			0.15		μS
t _R	Rise Time	$V_{CC} = -30V, I_{C} = -5A$ $I_{B1} = -20mA, I_{B2} = 20mA$		0.55		μS
t _{STG}	Storage Time	$R_L = 6\Omega$		2.5		μS
t _F	Fall Time			2.5		μS

^{*} Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

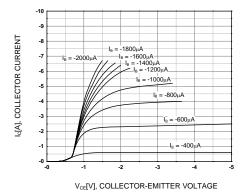


Figure 1. Static Characteristic

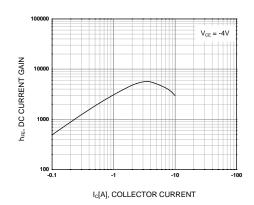


Figure 2. DC current Gain

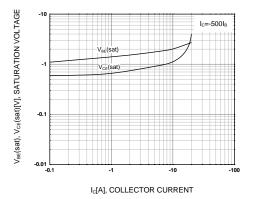


Figure 3. Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage

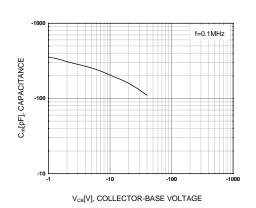


Figure 4. Collector Output Capacitance

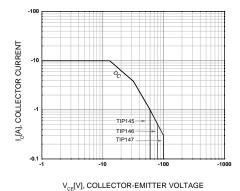


Figure 5. Safe Operating Area

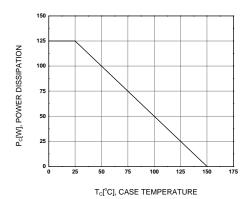


Figure 6. Power Derating

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