

# **1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER**

PowerDI<sup>®</sup>123

DFLS130L

### Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- High Current Capability and Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: PowerDl<sup>®</sup>123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



Top View

## **Maximum Ratings** $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	V
Average Forward Current @ T <sub>T</sub> = 121°C	I <sub>F(AV)</sub>	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	50	A

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	PD	1.67	W
Power Dissipation (Note 2)	PD	556	mW
Thermal Resistance Junction to Ambient (Note 1)	R <sub>0JA</sub>	60	°C/W
Thermal Resistance Junction to Ambient (Note 2)	R <sub>0</sub> JA	180	°C/W
Thermal Resistance Junction to Soldering (Note 3)	R <sub>θJS</sub>	10	°C/W
Operating Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-40 to +150	°C

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V <sub>(BR)R</sub>	30			V	I <sub>R</sub> = 1.0mA
	V <sub>F</sub>		0.210			I <sub>F</sub> = 0.1A
Forward Voltage		_	0.310	_		I <sub>F</sub> = 1.0A
		_	0.328	0.36		I <sub>F</sub> = 1.5A
Leakage Current (Note 5)	I <sub>R</sub>	_	0.260	_	mA	$V_{R} = 5V, T_{A} = 25^{\circ}C$
Leakage Current (Note 5)			_	1.0	IIIA	$V_{R} = 30V, T_{A} = 25^{\circ}C$
Total Capacitance	CT		76		рF	$V_R = 10V, f = 1.0MHz$

Notes: 1. Part mounted on 2"x2" GETEK board with 1"x1" copper pad, 25% anode, 75% cathode. T<sub>A</sub> = 25°C.

2. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

3. Theoretical R<sub>eJS</sub> calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

4. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead\_free.html.

5. Short duration pulse test used to minimize self-heating effect.

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= -40°C

20

25

30

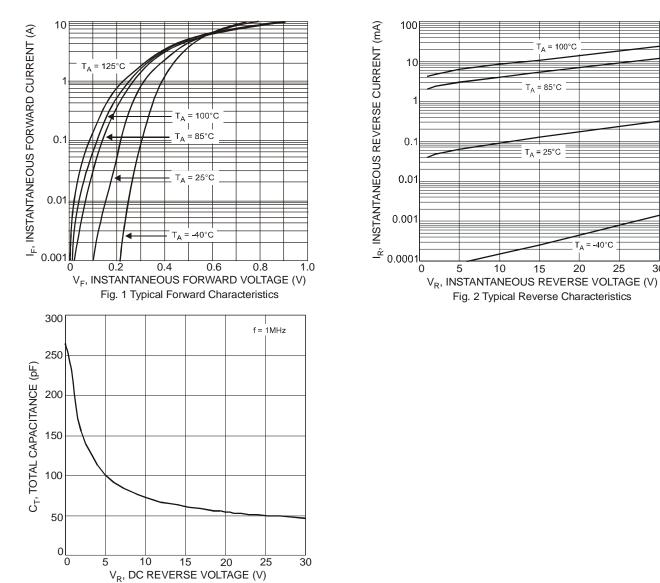


Fig. 3 Total Capacitance vs. Reverse Voltage

## Ordering Information (Note 6)

Part Number	Case	Packaging
DFLS130L-7	PowerDI <sup>®</sup> 123	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**

1			
	F03	ΥM	

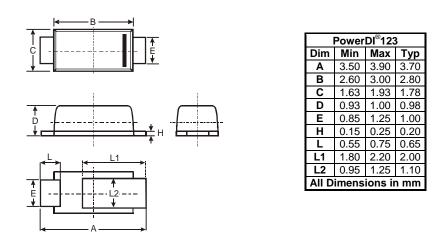
F03 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: R = 2004)M = Month (ex: 9 = September)

Date Code Key												
Year	2004	20	05	2006	2007	20	08	2009	2010	20	)11	2012
Code	R		S	Т	U	N	/	W	Х	,	Y	Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

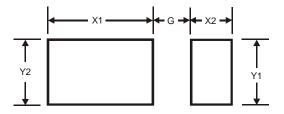
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# **Package Outline Dimensions**



# **Suggested Pad Layout**



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4



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