

# STC12IE90HV

# Emitter Switched Bipolar Transistor ESBT<sup>®</sup> 900 V - 12 A - 0.083 $\Omega$

Preliminary Data

### **General features**

V <sub>CS(ON)</sub>	Ι <sub>C</sub>	R <sub>CS(ON)</sub>
1V	12A	<b>0.083</b> Ω

- High voltage / high current Cascode configuration
- Low equivalent on resistance
- Very fast-switch up to 150 kHz
- Squared RBSOA up to 900V
- Very low  $C_{iss}$  driven by  $R_G = 47\Omega$
- Very low turn-off cross over time

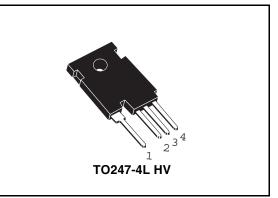
# **Applications**

■ Aux Smps For Three Phase Mains

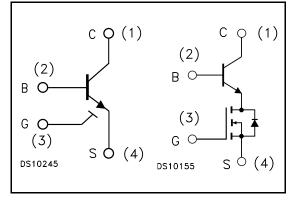
### Description

The STC12IE90HV is manufactured in Monolithic ESBT Technology, aimed to provide best performances in high frequency / high voltage applications.

It is designed for use in Gate Driven based topologies.



### Internal schematic diagrams



### **Order codes**

Part Number	Marking	Package	Packaging
STC12IE90HV	C12IE90HV	TO247-4L HV	Tube

#### January 2007

This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

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# 1 Electrical ratings

Table 1.	Absolute maximum rating			
Symbol	Parameter	Value	Unit	
V <sub>CS(SS)</sub>	Collector-source voltage ( $V_{BS} = V_{GS} = 0 V$ )	900	V	
V <sub>BS(OS)</sub>	Base-source voltage ( $I_C = 0$ , $V_{GS} = 0$ V)	30	V	
V <sub>SB(OS)</sub>	Source-base voltage ( $I_C = 0$ , $V_{GS} = 0$ V)	17	V	
V <sub>GS</sub>	Gate-source voltage	± 17	V	
۱ <sub>C</sub>	Collector current	12	А	
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5ms)	36	Α	
۱ <sub>B</sub>	Base current	6	А	
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5ms)	10	А	
P <sub>tot</sub>	Total dissipation at $T_c = 25^{\circ}C$	208	W	
T <sub>stg</sub>	Storage temperature	-40 to 150	°C	
Т <sub>Ј</sub>	Max. operating junction temperature	150	°C	

### Table 1. Absolute maximum rating

#### Table 2. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thj-case</sub>	Thermal resistance junction-case max	0.6	°C/W



# 2 Electrical characteristics

( $T_{case} = 25^{\circ}C$  unless otherwise specified)

able 5. Electrical characteristics						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>CS(SS)</sub>	Collector-source current $(V_{BS} = V_{GS} = 0)$	V <sub>CE</sub> = 900V			100	μA
I <sub>BS(OS)</sub>	Base-source current $(I_{C} = 0, V_{GS} = 0)$	V <sub>BS(OS)</sub> = 30V			10	μA
I <sub>SB(OS)</sub>	Source-base current $(I_{C} = 0, V_{GS} = 0)$	V <sub>SB(OS)</sub> = 17V			100	μA
IGS(OS)	Gate-source leakage	$V_{GS} = \pm 17V$			100	nA
V <sub>CS(ON)</sub>	Collector-source ON voltage	$V_{GS} = 10V$ $I_C = 12A$ $I_B = 2.4A$ $V_{GS} = 10V$ $I_C = 6A$ $I_B = 0.6A$		1 0.6		V V
h <sub>FE</sub>	DC current gain			5 15		
V <sub>BS(ON)</sub>	Base Source ON voltage	$V_{GS} = 10V I_C = 12A I_B = 2.4A$ $V_{GS} = 10V I_C = 6A I_B = 0.6A$		1.5 1.2		V V
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{BS} = V_{GS}$ $I_B = 250 \mu A$	2	3	4	V
C <sub>iss</sub>	Input capacitance	V <sub>CS</sub> =25V f =1MHz V <sub>GS</sub> =0V		520		pF
Q <sub>GS(tot)</sub>	Gate-source Charge	$V_{CS}=25V$ $V_{GS}=10V$ $V_{CB}=0V$ $I_{C}=4A$		21.3		nC
t <sub>s</sub> t <sub>f</sub>	INDUCTIVE LOAD Storage time Fall time	$\label{eq:VGS} \begin{array}{ll} V_{GS} = 10V & R_{G} = 47\Omega \\ V_{Clamp} = 720V & t_{p} = 4\mu s \\ I_{C} = 6A & I_{B} = 1.2A \end{array}$		610 10		ns ns
t <sub>s</sub> t <sub>f</sub>	INDUCTIVE LOAD Storage time Fall time	$V_{GS} = 10V$ $R_G = 47\Omega$ $V_{Clamp} = 720V$ $t_p = 4\mu s$ $I_C = 6A$ $I_B = 0.6A$		360 10		ns ns
V <sub>CSW</sub>	Maximum collector- source voltage switched without snubber	$R_{G} = 47\Omega$ $h_{FE} = 5$ $I_{C} = 12A$	900			V

Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>CS(dyn)</sub>	Collector-source dynamic voltage (500ns)	$\begin{split} & V_{CC} = V_{Clamp} = \!$		3.37		V
V <sub>CS(dyn)</sub>	Collector-source dynamic voltage (1µs)	$\begin{split} & V_{CC} = V_{Clamp} = \!$		1.75		v

Table 3. Electrical characteristics

# 2.1 Electrical characteristics (curves)

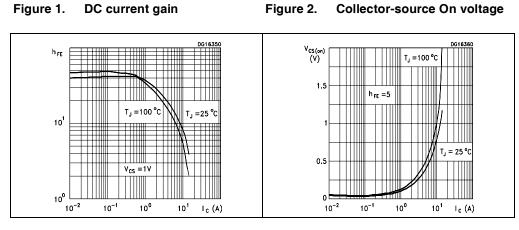


Figure 3. Collector-source On voltage Figure 4. Base-source On voltage

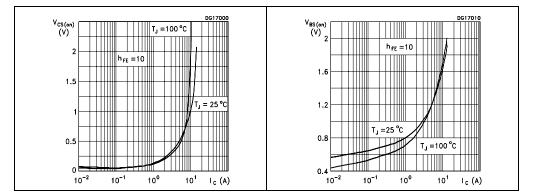


Figure 5.

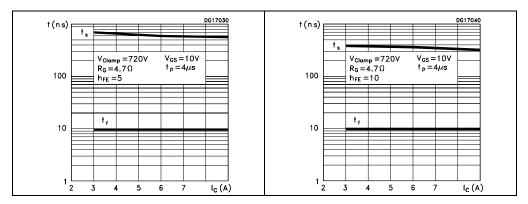
**Reverse biased SOA** 

**Dynamic collector-emitter** 

#### voltage DG15710 DG17020 V<sub>CE(sat)dyn</sub> $I_{c}(A)$ $h_{FE} = 5$ $V_{CC} = V_{Clamp} = 400V$ 6 $V_{GS} = 10V$ 12 5 $R_G = 4.7 \Omega$ $|_{Bpeack} = |_{C}$ 4 8 3 I<sub>C</sub>=9A 2 I<sub>c</sub>=6A $I_c = 3A$ 1 Т 0 200 400 600 800 $V_{CS} = V_{Clamp}(V)$ 0 4 8 12 16 t(μs)

Figure 6.



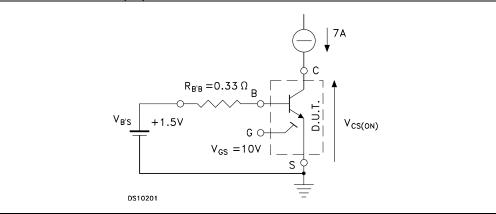


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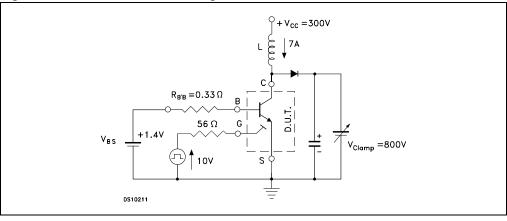


# 2.2 Test circuits

### Figure 9. Static V<sub>CS(ON)</sub> test circuits



#### Figure 10. Inductive load switching and RBSOA test circuit





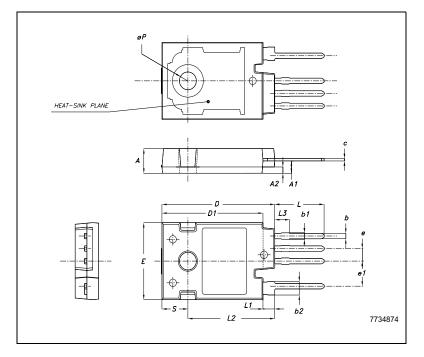
# 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



DIM.		mm.	
	MIN.	TYP	MAX.
A	4.85		5.15
A1	2.20	2.50	2.60
A2		1.27	
b	0.95	1.10	1.30
b2	2.50		2.90
С	0.40		0.80
D	23.85	24	24.15
D1		21.50	
E	15.45	15.60	15.75
е	2.54		
e1	5.08		
L	10.20		10.80
L1	2.20	2.50	2.80
L2		18.50	
L3		3	
øP	3.55		3.65
S		5.50	

**TO247-4L HV MECHANICAL DATA** 



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# 4 Revision history

### Table 4. Revision history

Date	Revision	Changes
16-Jan-2007	1	Initial release.



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