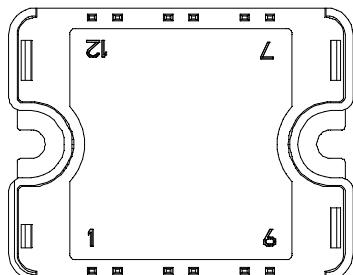
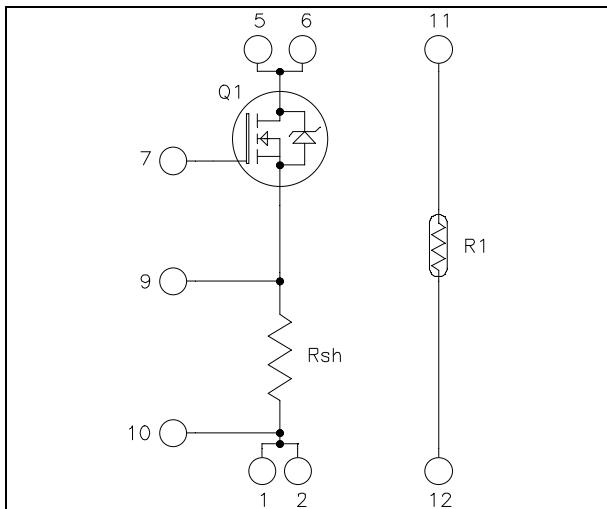




**Linear MOSFET
Power Module**

V_{DSS} = 100V
R_{DSon} = 09mΩ typ @ T_j = 25°C
I_D = 154A* @ T_c = 25°C



Pins 1/2 ; 5/6 must be shorted together

Application

- Electronic load dedicated to power supplies and battery discharge testing

Features

- Linear MOSFET
- Very low stray inductance
- Internal thermistor for temperature monitoring
- High level of integration
- AlN substrate for improved thermal performance

Benefits

- Direct mounting to heatsink (isolated package)
- easy series and parallels combinations for power and voltage improvements
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	100	V
I _D	Continuous Drain Current	T _c = 25°C	A
		T _c = 80°C	
I _{DM}	Pulsed Drain current	430	
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	10	mΩ
P _D	Maximum Power Dissipation ①	T _c = 25°C	W
I _{AR}	Avalanche current (repetitive and non repetitive)	100	A
E _{AR}	Repetitive Avalanche Energy	50	mJ
E _{AS}	Single Pulse Avalanche Energy	3000	

* Output current must be limited to 67A @ T_c=25°C and 47A @ T_c=80°C to not exceed the shunt specification.

① In saturation mode

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0\text{V}$, $V_{DS} = 100\text{V}$	$T_j = 25^\circ\text{C}$			100	μA
		$V_{GS} = 0\text{V}$, $V_{DS} = 80\text{V}$	$T_j = 125^\circ\text{C}$			500	
$R_{DS(on)}$	Drain – Source on Resistance	$V_{GS} = 10\text{V}$, $I_D = 69.5\text{A}$			9	10	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 2.5\text{mA}$		2		4	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30\text{ V}$, $V_{DS} = 0\text{V}$				± 100	nA

Dynamic Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}$ $V_{DS} = 25\text{V}$ $f = 1\text{MHz}$			9875		pF
C_{oss}	Output Capacitance				3940		
C_{rss}	Reverse Transfer Capacitance				1470		

Shunt Electrical Characteristics

<i>Symbol</i>	<i>Characteristic</i>			<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R_{sh}	Resistance value				4.4		$\text{m}\Omega$
T_{sh}	Tolerance				2		%
P_{sh}	Load capacity		$T_C=25^\circ\text{C}$			20	W
			$T_C=80^\circ\text{C}$			10	
I_{sh}	Current capacity		$T_C=25^\circ\text{C}$			67	A
			$T_C=80^\circ\text{C}$			47	

Temperature sensor PTC

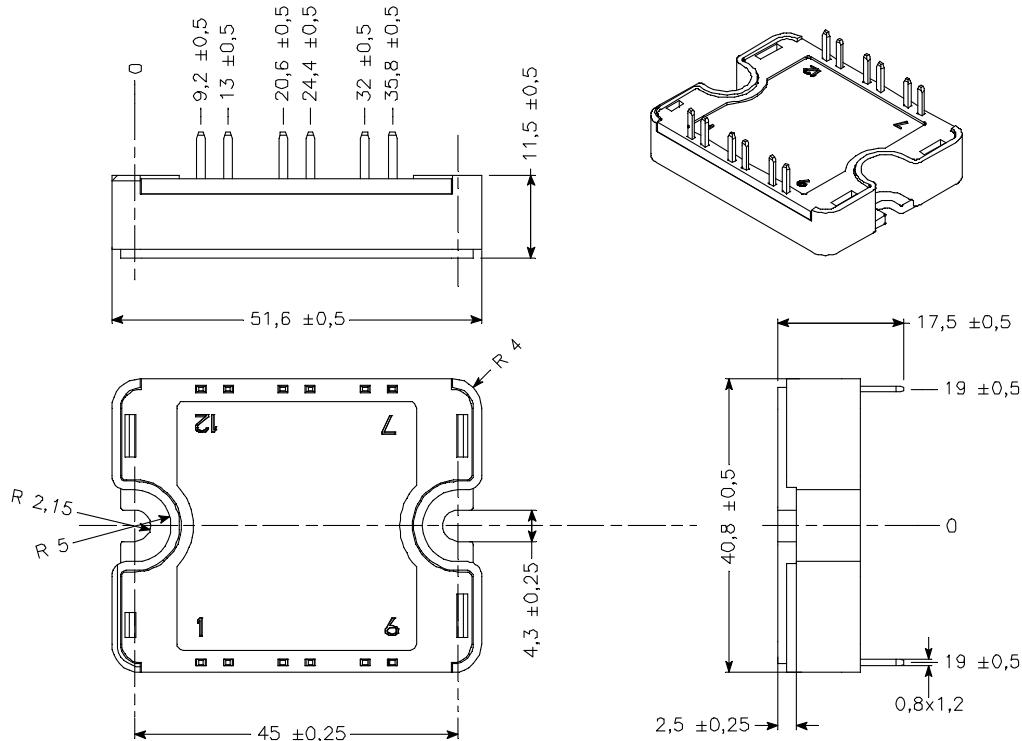
<i>Symbol</i>	<i>Characteristic</i>			<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R_{25}	Resistance @ 25°C			1980		2020	Ω
R_{100}/R_{25}	Resistance ratio	$\text{Tamb}=100^\circ\text{C} & 25^\circ\text{C}$		1.676	1.696	1.716	
R_{-55}/R_{25}	Resistance ratio	$\text{Tamb}=-55^\circ\text{C} & 25^\circ\text{C}$		0.48	0.49	0.50	
B	Temperature coefficient				7900		ppm/K

Thermal and package characteristics

<i>Symbol</i>	<i>Characteristic</i>			<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R_{thJC}	Junction to Case Thermal Resistance	MOSFET			0.26		$^\circ\text{C/W}$
V_{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, $I_{\text{isol}} < 1\text{mA}$, 50/60Hz		4000				V
T_j	Operating junction temperature range		-40		150		$^\circ\text{C}$
T_{STG}	Storage Temperature Range		-40		125		
T_c	Operating Case Temperature		-40		100		
Torque	Mounting torque	To heatsink	M4	2.5		4.7	N.m
Wt	Package Weight				80		g

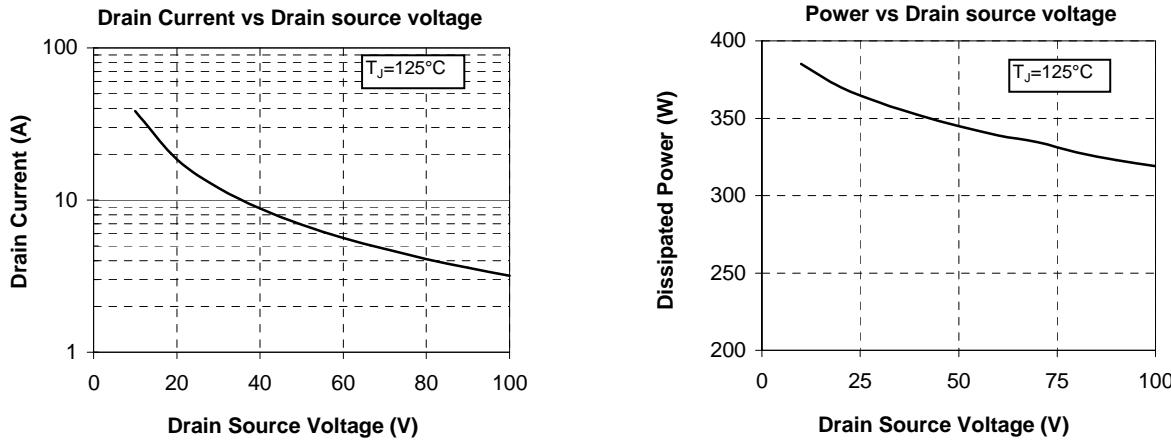


SP1 Package outline (dimensions in mm)



See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

Typical Performance Curve (linear mode)



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