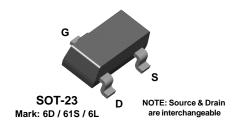


2N5457 2N5458 2N5459

**MMBF5457 MMBF5458 MMBF5459** 





# **N-Channel General Purpose Amplifier**

This device is a low level audio amplifier and switching transistors, and can be used for analog switching applications. Sourced from Process 55.

#### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
$V_{DG}$	Drain-Gate Voltage	25	V	
V <sub>GS</sub>	Gate-Source Voltage	- 25	V	
I <sub>GF</sub>	Forward Gate Current	10	mA	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C	

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

#### **Thermal Characteristics** TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units	
		2N5457-5459	*MMBF5457-5459		
P <sub>D</sub>	Total Device Dissipation	625	350	mW	
	Derate above 25°C	5.0	2.8	mW/°C	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W	

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

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# **N-Channel General Purpose Amplifier**

(continued)

Electrical	Characteristics	TA = 25°C unless otherwise noted

Symbol Parameter Test Conditions Min Typ Max Ur
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### **OFF CHARACTERISTICS**

V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_G = 10 \mu A, V_{DS} = 0$		- 25			V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -15 \text{ V}, V_{DS} = 0$				- 1.0	nA
		$V_{GS} = -15 \text{ V}, V_{DS} = 0, T_A = 0$	= 100°C			- 200	nA
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	$V_{DS} = 15 \text{ V}, I_{D} = 10 \text{ nA}$	5457	- 0.5		- 6.0	V
			5458	- 1.0		- 7.0	V
			5459	- 2.0		- 8.0	V
V <sub>GS</sub>	Gate-Source Voltage	$V_{DS} = 15 \text{ V}, I_{D} = 100 \mu\text{A}$	5457		- 2.5		V
		$V_{DS} = 15 \text{ V}, I_{D} = 200 \mu\text{A}$	5458		- 3.5		V
		$V_{DS} = 15 \text{ V}, I_D = 400 \mu\text{A}$	5459		- 4.5		V

## **ON CHARACTERISTICS**

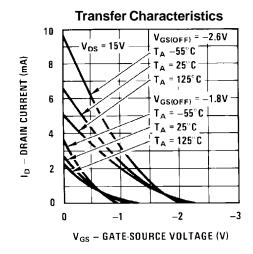
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current*	$V_{DS} = 15 \text{ V}, V_{GS} = 0$	5457	1.0	3.0	5.0	mΑ
			5458	2.0	6.0	9.0	mΑ
			5459	4.0	9.0	16	mΑ

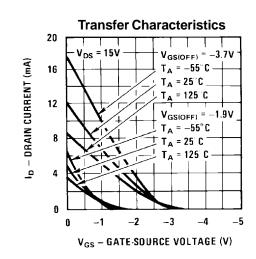
### SMALL SIGNAL CHARACTERISTICS

9 <sub>fs</sub>	Forward Transfer Conductance*	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ kHz}$				
		5457 5458	1000 1500		5000 5500	μmhos μmhos
		5459	2000		6000	μmhos
gos	Output Conductance*	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ kHz}$		10	50	μmhos
Ciss	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz}$		4.5	7.0	pF
Crss	Reverse Transfer Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ MHz}$		1.5	3.0	pF
NF	Noise Figure	$V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ kHz},$			3.0	dB
		$R_G = 1.0$ megohm, BW = 1.0 Hz				

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300 ms, Duty Cycle  $\leq$  2%

# **Typical Characteristics**

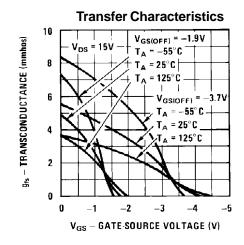


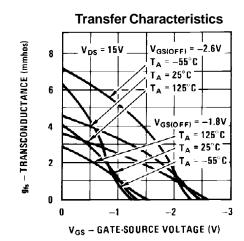


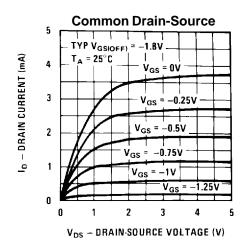
# **N-Channel General Purpose Amplifier**

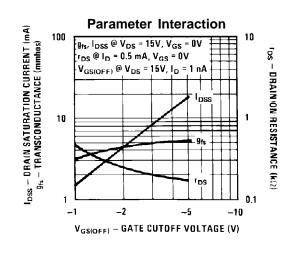
(continued)

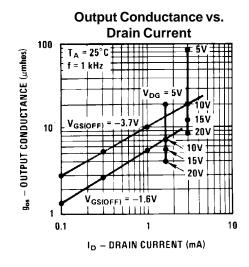
# Typical Characteristics (continued)

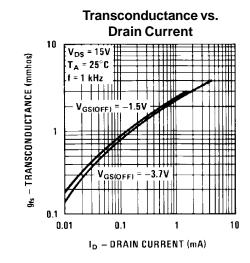








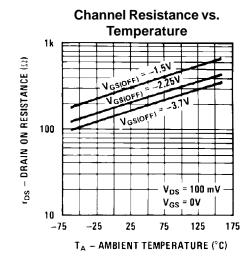


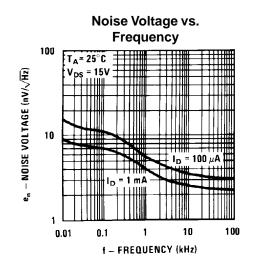


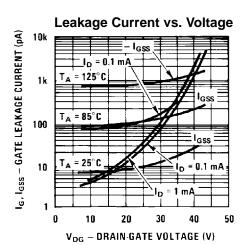
# **N-Channel General Purpose Amplifier**

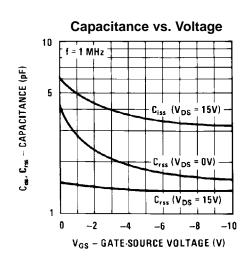
(continued)

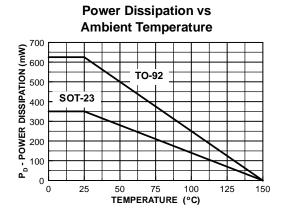
# Typical Characteristics (continued)











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Rev. G