

# ZXMN6A25G 60V SOT223 N-channel enhancement mode MOSFET

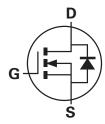
## Summary

V <sub>(BR)DSS</sub>	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)
60	0.050 @ V <sub>GS</sub> = 10V	6.7
	0.070 @ V <sub>GS</sub> = 4.5V	5.7



## **Description**

This new generation trench MOSFET from Zetex features a unique structure combining the benefits of low on-resistance and fast switching, making it ideal for high efficiency power management applications.

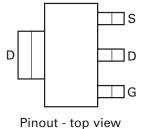


## **Features**

- · Low on-resistance
- · Fast switching speed
- · Low gate drive
- SOT223 package

## **Applications**

- · DC-DC converters
- · Power management functions
- · Disconnect switches
- · Motor control



## **Ordering information**

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A25GTA	7	12	1,000

## **Device marking**

ZXMN 6A25

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# **Absolute maximum ratings**

Parameter	Symbol	Limit	Unit	
Drain-source voltage	$V_{DSS}$	60	V	
Gate-source voltage		$V_{GS}$	±20	V
Continuous drain current	@ V <sub>GS</sub> = 10V; T <sub>amb</sub> = 25°C <sup>(b)</sup>	I <sub>D</sub>	6.7	Α
	@ $V_{GS} = 10V$ ; $T_{amb} = 70^{\circ}C^{(b)}$		5.4	А
	@ $V_{GS} = 10V$ ; $T_{amb} = 25^{\circ}C^{(a)}$		4.8	Α
Pulsed drain current <sup>(c)</sup>	I <sub>DM</sub>	28.5	Α	
Continuous source current	I <sub>S</sub>	5.7	А	
Pulsed source current (bod	I <sub>SM</sub>	28.5	Α	
Power dissipation at T <sub>amb</sub> :	P <sub>D</sub>	2	W	
Linear derating factor		16	mW/°C	
Power dissipation at T <sub>amb</sub>	P <sub>D</sub>	3.9	W	
Linear derating factor		31	mW/°C	
Operating and storage tem	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	°C	

## Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient	$R_{\Theta JA}$	62.5	°C/W
Junction to ambient	$R_{\Theta JA}$	32	°C/W

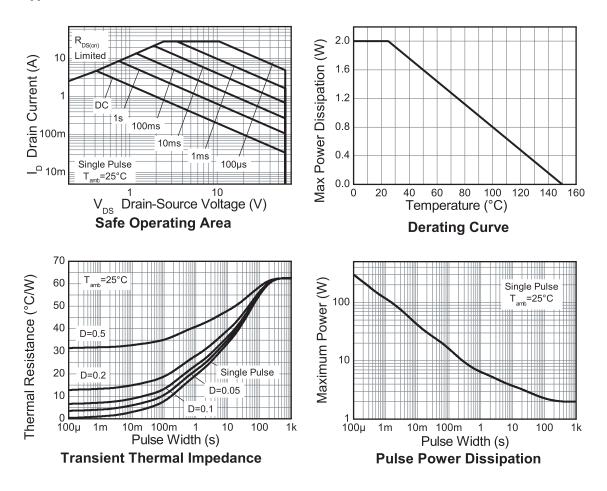
## NOTES:

<sup>(</sup>a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

<sup>(</sup>b) For a device surface mounted on FR4 PCB measured at t  $\leq$ 10 sec.

<sup>(</sup>c) Repetitive rating - 25mm x 25mm FR4 PCB, D=0.02, pulse width  $300\,\mu s$  - pulse width limited by maximum junction temperature.

# **Typical characteristics**



# Electrical characteristics (at T<sub>amb</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
STATIC						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	60			V	I <sub>D</sub> = 250μA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>			1.0	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> =0V
Gate-body leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Gate-source threshold voltage	V <sub>GS(th)</sub>	1			V	$I_D = 250 \mu A$ , $V_{DS} = V_{GS}$
Static drain-source on-state	R <sub>DS(on)</sub>			0.050	Ω	$V_{GS}$ = 10V, $I_{D}$ = 3.6A
resistance (*)				0.070	Ω	$V_{GS} = 4.5V, I_D = 3.0A$
Forward transconductance(*) (‡)	9 <sub>fs</sub>		10.2		S	$V_{DS} = 15V, I_{D} = 4.5A$
Dynamic <sup>(‡)</sup>						
Input capacitance	C <sub>iss</sub>		1063		рF	V <sub>DS</sub> = 30V, V <sub>GS</sub> =0V
Output capacitance	C <sub>oss</sub>		104		рF	f=1MHz
Reverse transfer capacitance	C <sub>rss</sub>		64		рF	
Switching (†) (‡)						
Turn-on-delay time	t <sub>d(on)</sub>		3.8		ns	V <sub>DD</sub> = 30V, I <sub>D</sub> = 1A
Rise time	t <sub>r</sub>		4.0		ns	R <sub>G</sub> ≅6.0W, V <sub>GS</sub> = 10V
Turn-off delay time	t <sub>d(off)</sub>		26.2		ns	
Fall time	t <sub>f</sub>		10.6		ns	
Gate charge	$Q_g$		11.0		nC	$V_{DS} = 30V, V_{GS} = 5V$ $I_{D} = 1.4A$
Total gate charge	Qg		20.4		nC	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V
Gate-source charge	Q <sub>gs</sub>		4.1		nC	I <sub>D</sub> = 1.4A
Gate Drain Charge	Q <sub>gd</sub>		5.1		nC	
Source-drain diode						
Diode forward voltage <sup>(*)</sup>	V <sub>SD</sub>		0.85	0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> = 5.5A, V <sub>GS</sub> =0V
Reverse recovery time (‡)	t <sub>rr</sub>		22.0		ns	T <sub>j</sub> =25°C, I <sub>S</sub> = 2.2A,
Reverse recovery charge <sup>(‡)</sup>	Q <sub>rr</sub>		21.4		nC	di/dt=100A/μs

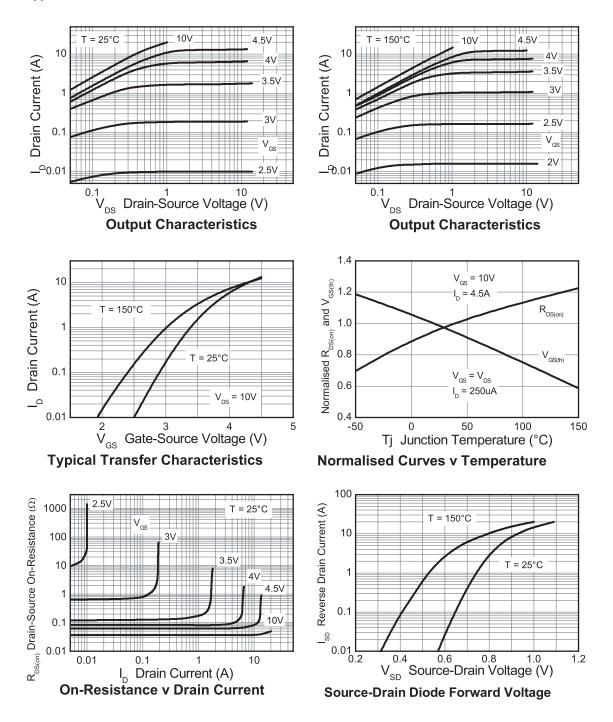
### NOTES:

<sup>(\*)</sup> Measured under pulsed conditions. Pulse width  $\leq$ 300 $\mu$ s; duty cycle  $\leq$ 2%.

<sup>(†)</sup> Switching characteristics are independent of operating junction temperature.

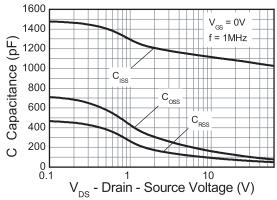
<sup>(‡)</sup> For design aid only, not subject to production testing.

## **Typical characteristics**

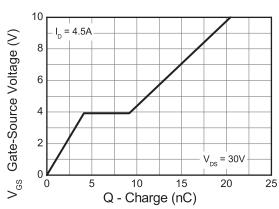


# ZXMN6A25G

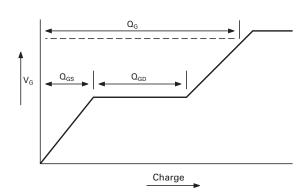
# **Typical characteristics**



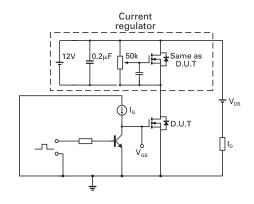
Capacitance v Drain-Source Voltage



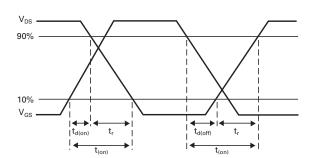
**Gate-Source Voltage v Gate Charge** 



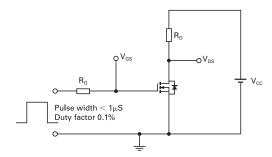
Basic gate charge waveform



Gate charge test circuit



**Switching time waveforms** 

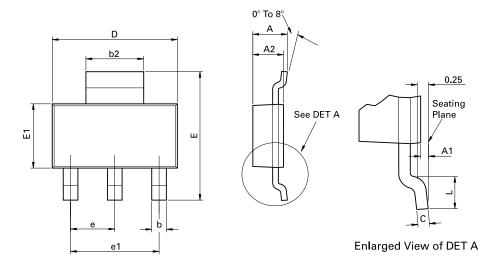


Switching time test circuit

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# ZXMN6A25G

# Package outline - SOT223



Conforms to JEDEC TO-261 AA Issue B

DIM	Millin	neters	Inc	hes	DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	-	1.80	-	0.071	е	2.30	BSC	0.090	5 BSC
A1	0.02	0.10	0.0008	0.004	e1	4.60	BSC	0.181	BSC
b	0.66	0.84	0.026	0.033	Е	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
С	0.23	0.33	0.009	0.013	Ĺ	0.90	-	0.355	-
D	6.30	6.70	0.248	0.264	-	-	-	-	-

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches.

## ZXMN6A25G

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