

Symbol	Parameter		FQB11P06 / FQI11P06	Units
V _{DSS}	Drain-Source Voltage		-60	V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		-11.4	А
	- Continuous (T _C = 100°C)		-8.05	А
I _{DM}	Drain Current - Pulsed	(Note 1)	-45.6	А
V _{GSS}	Gate-Source Voltage		± 25	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	160	mJ
I _{AR}	Avalanche Current	(Note 1)	-11.4	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	5.3	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	-7.0	V/ns
P _D	Power Dissipation $(T_A = 25^{\circ}C)^{*}$		3.13	W
	Power Dissipation $(T_C = 25^{\circ}C)$		53	W
	- Derate above 25°C		0.35	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

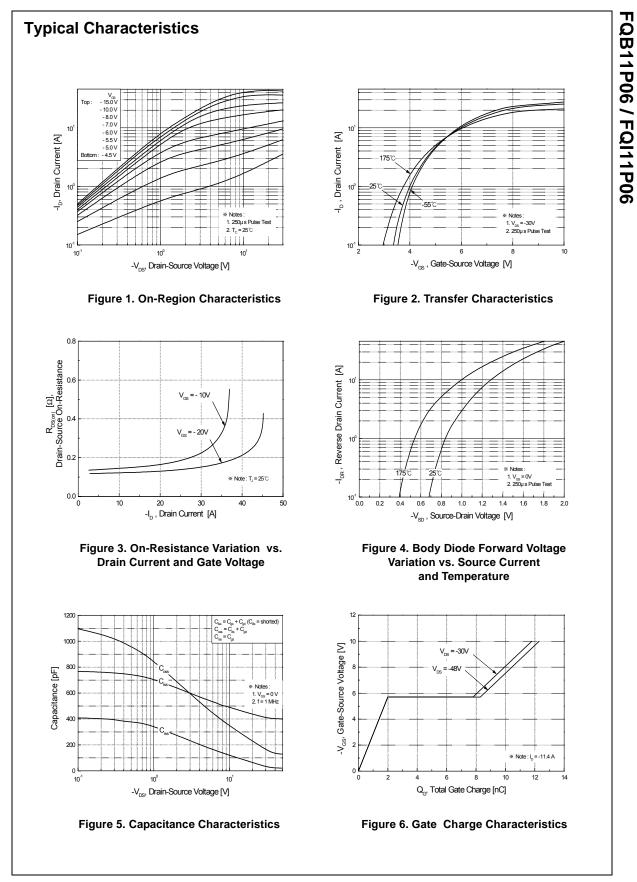
Thermal Characteristics

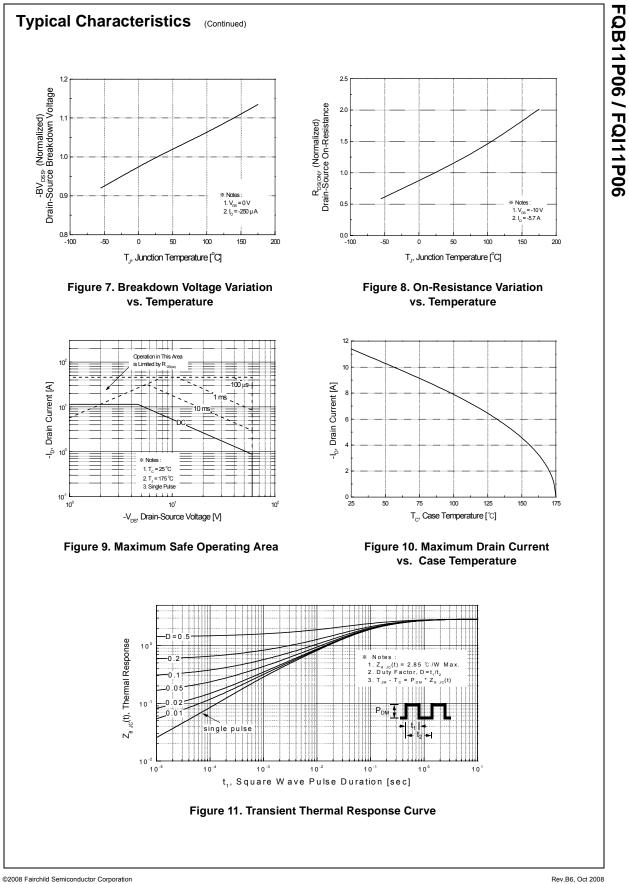
Symbol	Parameter	Тур	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		2.85	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient *		40	°C/W
R _{0JA}	Thermal Resistance, Junction-to-Ambient		62.5	°C/W

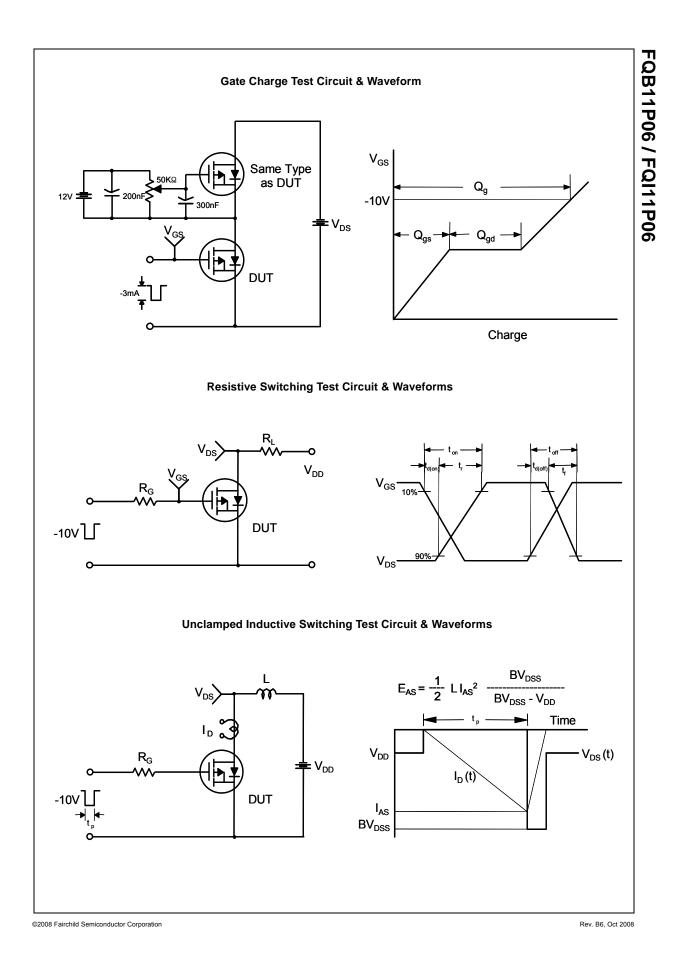
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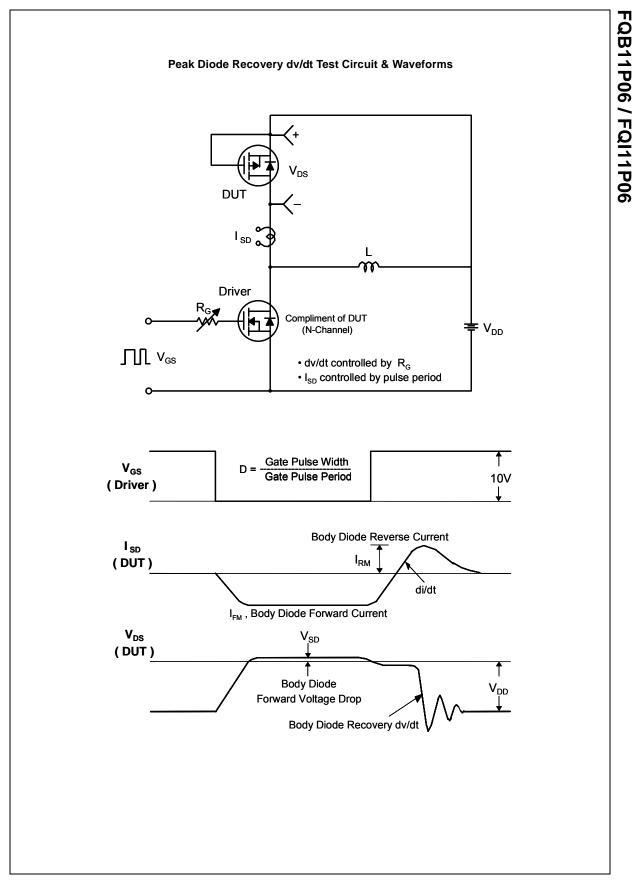
	Test Conditions	Min	Тур	Max	Units
ractoriation					
racteristics Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-60			V
Breakdown Voltage Temperature	$I_D = -250 \mu\text{A}$, Referenced to 25°C	-00	-0.07		V/°C
Coefficient					•
Zero Gate Voltage Drain Current	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$			-1	μA
Cata Dadu Laakaga Currant Farward					μA
					nA nA
Gale-Body Leakage Current, Reverse	V _{GS} = 23 V, V _{DS} = 0 V			100	nA
racteristics					
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250 μA	-2.0		-4.0	V
Static Drain-Source			0.14	0.175	0
On-Resistance			0.14	0.175	Ω
Forward Transconductance	$V_{DS} = -30 \text{ V}, \text{ I}_{D} = -5.7 \text{ A}$ (Note 4)		5.1		S
- Characteriation					
			420	550	~F
1 1					pF
	f = 1.0 MHz				рF
Reverse transfer Capacitance			40	60	pF
ng Characteristics					
Turn-On Delay Time			6.5	25	ns
	V _{DD} = -30 V, I _D = -5.7 A,	-			110
Turn-On Rise Time			40	90	ns
Turn-On Rise Time Turn-Off Delay Time	$R_{G} = 25 \Omega$		40 15	90 40	ns ns
Turn-Off Delay Time			15	40	ns
Turn-Off Delay Time Turn-Off Fall Time	R _G = 25 Ω (Note 4, 5)		15 45	40 100	ns ns
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge	$R_{G} = 25 $ Ω (Note 4, 5) $V_{DS} = -48 $ V, $I_{D} = -11.4 $ A,		15 45 13	40 100 17	ns ns nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge	$R_{G} = 25 \Omega$ (Note 4, 5) V _{DS} = -48 V, I _D = -11.4 A, V _{GS} = -10 V		15 45 13 2.0	40 100 17 	ns ns nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge	$R_{G} = 25 $ Ω (Note 4, 5) $V_{DS} = -48 $ V, $I_{D} = -11.4 $ A,	 	15 45 13	40 100 17	ns ns nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge	R_{G} = 25 Ω (Note 4, 5) V_{DS} = -48 V, I _D = -11.4 A, V_{GS} = -10 V (Note 4, 5)	 	15 45 13 2.0	40 100 17 	ns ns nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge	$R_{G} = 25 \Omega$ (Note 4, 5) V _{DS} = -48 V, I _D = -11.4 A, V _{GS} = -10 V (Note 4, 5) nd Maximum Ratings	 	15 45 13 2.0	40 100 17 	ns ns nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Durce Diode Characteristics ar Maximum Continuous Drain-Source Dio	$R_{G} = 25 \Omega$ (Note 4, 5) $V_{DS} = -48 V, I_{D} = -11.4 A,$ $V_{GS} = -10 V$ (Note 4, 5) (Note 4, 5	 	15 45 13 2.0 6.3	40 100 17 	ns ns nC nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Durce Diode Characteristics ar Maximum Continuous Drain-Source Diode Maximum Pulsed Drain-Source Diode F	$R_{G} = 25 \Omega$ (Note 4, 5) $V_{DS} = -48 V, I_{D} = -11.4 A,$ $V_{GS} = -10 V$ (Note 4, 5) (Note 4, 5	 	15 45 13 2.0 6.3	40 100 17 -11.4 -45.6	ns ns nC nC nC A A
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge Durce Diode Characteristics ar Maximum Continuous Drain-Source Dio	$R_{G} = 25 \Omega$ (Note 4, 5) $V_{DS} = -48 V, I_{D} = -11.4 A,$ $V_{GS} = -10 V$ (Note 4, 5) (Note 4, 5	 	15 45 13 2.0 6.3	40 100 17 	ns ns nC nC nC
	Gate-Body Leakage Current, Forward Gate-Body Leakage Current, Reverse Cacteristics Gate Threshold Voltage Static Drain-Source On-Resistance Forward Transconductance Characteristics Input Capacitance Output Capacitance Reverse Transfer Capacitance	Zero Gate Voltage Drain Current $V_{DS} = -48 \text{ V}, T_C = 150^{\circ}\text{C}$ Gate-Body Leakage Current, Forward $V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$ Gate-Body Leakage Current, Reverse $V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$ acteristics $V_{DS} = V_{GS}, I_D = -250 \mu \text{A}$ Gate Threshold Voltage $V_{DS} = V_{GS}, I_D = -250 \mu \text{A}$ Static Drain-Source $V_{DS} = -10 \text{ V}, I_D = -5.7 \text{ A}$ On-Resistance $V_{DS} = -30 \text{ V}, I_D = -5.7 \text{ A}$ Forward Transconductance $V_{DS} = -30 \text{ V}, I_D = -5.7 \text{ A}$ Input Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$ Reverse Transfer Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz}$	Zero Gate Voltage Drain Current $V_{DS} = -48 \text{ V}, T_C = 150^{\circ}\text{C}$ Gate-Body Leakage Current, Forward $V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$ Gate-Body Leakage Current, Reverse $V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$ acteristics $V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$ Gate Threshold Voltage $V_{DS} = V_{GS}, I_D = -250 \mu \text{ A}$ -2.0Static Drain-Source $V_{GS} = -10 \text{ V}, I_D = -5.7 \text{ A}$ On-Resistance $V_{DS} = -30 \text{ V}, I_D = -5.7 \text{ A}$ Forward Transconductance $V_{DS} = -30 \text{ V}, I_D = -5.7 \text{ A}$ (Note 4)Input Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Output Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Transfer Capacitance $$ or CharacteristicsInput Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Output Capacitance $$ OtheracteristicsInput CapacitanceInput Capaci	Zero Gate Voltage Drain Current $V_{DS} = -48 \text{ V}, T_C = 150^{\circ}\text{C}$ Gate-Body Leakage Current, Forward $V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$ Gate-Body Leakage Current, Reverse $V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$ Gate Threshold Voltage $V_{DS} = V_{GS}, I_D = -250 \mu \text{A}$ -2.0Static Drain-Source $V_{DS} = -10 \text{ V}, I_D = -5.7 \text{ A}$ 0.14On-Resistance $V_{DS} = -30 \text{ V}, I_D = -5.7 \text{ A}$ 5.1CharacteristicsInput Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Qutput Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Transfer Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Gate Transfer Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Transfer Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ Gate Transfer Capacitance $$ 45The Characteristics45	Zero Gate Voltage Drain Current $V_{DS} = -48 \text{ V}, T_C = 150^{\circ}\text{C}$ 10Gate-Body Leakage Current, Forward $V_{GS} = -25 \text{ V}, V_{DS} = 0 \text{ V}$ 100Gate-Body Leakage Current, Reverse $V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$ 100acteristicsGate Threshold Voltage $V_{DS} = V_{GS}, I_D = -250 \mu \text{ A}$ -2.04.0Static Drain-Source $V_{GS} = -10 \text{ V}, I_D = -5.7 \text{ A}$ 0.140.175On-Resistance $V_{DS} = -30 \text{ V}, I_D = -5.7 \text{ A}$ 5.1CharacteristicsInput Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ 420550Output Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ 195250Reverse Transfer Capacitance $V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ 4560ng Characteristics

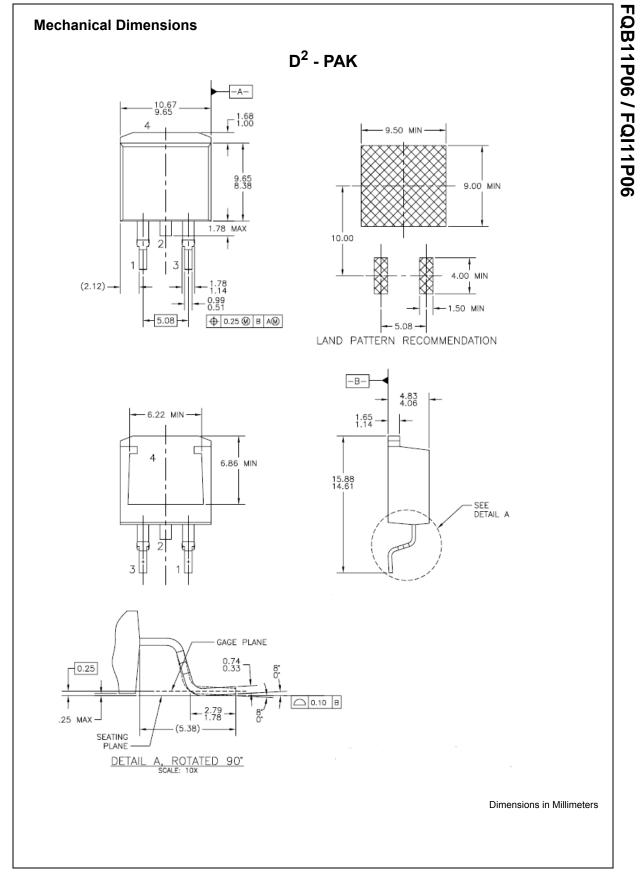


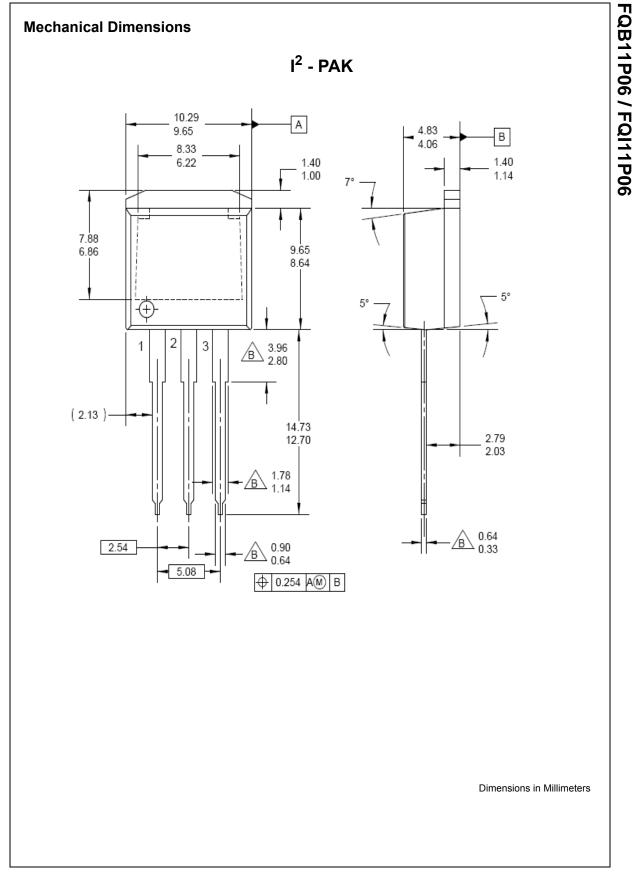




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