



## P-Channel 20 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A) <sup>a</sup> Q <sub>Q</sub>			
- 20	$0.112$ at $V_{GS} = -4.5 \text{ V}$	- 3.1	3.3 nC		
	0.142 at V <sub>GS</sub> = - 2.5 V	- 2.7	3.3 110		

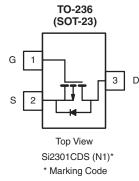
## **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



## **APPLICATIONS**

· Load Switch



Ordering Information: Si2301CDS-T1-E3 (Lead (Pb)-free)

Si2301CDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T	$_{A}$ = 25 °C, unless ot	herwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	$V_{DS}$	- 20	V		
Gate-Source Voltage		V <sub>GS</sub>	± 8	v	
	T <sub>C</sub> = 25 °C	I <sub>D</sub>	- 3.1		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 2.5		
Continuous Diain Current (1) = 150 C)	T <sub>A</sub> = 25 °C		- 2.3 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		- 1.8 <sup>b, c</sup>	Α	
Pulsed Drain Current		I <sub>DM</sub>	- 10		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	- 1.3		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	Is	- 0.72 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		1.6		
Maximum Bower Dissipation	T <sub>C</sub> = 70 °C	В	1.0	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.86 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		0.55 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	≤ 5 s	R <sub>thJA</sub>	120	145	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	62	78	- C/VV	

#### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 175 °C/W.

Document Number: 68741 S10-2430-Rev. C, 25-Oct-10

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					l		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 20			٧	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			- 18		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		2.2			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.4		- 1	V	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V			± 100	nA	
Zara Cata Valtana Duain Occurant		V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le$ - 5 V, $V_{GS}$ = - 4.5 V	- 6			Α	
Durin Course On Ot 1 D 11 2	П	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$		0.090	0.112	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 2.0 A		0.110	0.142		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 2.8 A		9.5		S	
Dynamic <sup>b</sup>						•	
Input Capacitance	C <sub>iss</sub>			405		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		75			
Reverse Transfer Capacitance	C <sub>rss</sub>			55			
Total Cata Charge	Qg	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3 \text{ A}$		5.5	10	nC	
Total Gate Charge				3.3	6		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V}, I_{D} = -3 \text{ A}$		0.7			
Gate-Drain Charge	Q <sub>gd</sub>			1.3			
Gate Resistance	$R_g$	f = 1 MHz		6.0		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			11	20		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_{I} = 10 \Omega$		35	60	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D = -1 A$ , $V_{GEN} = -4.5 V$ , $R_g = 1 \Omega$		30	50		
Fall Time	t <sub>f</sub>			10	20		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	Is	T <sub>C</sub> = 25 °C			- 1.3		
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 10	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 0.7 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			30	50	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			25	50	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -3.0 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		15			
Reverse Recovery Rise Time	t <sub>b</sub>	<b>-</b>		15		ns	

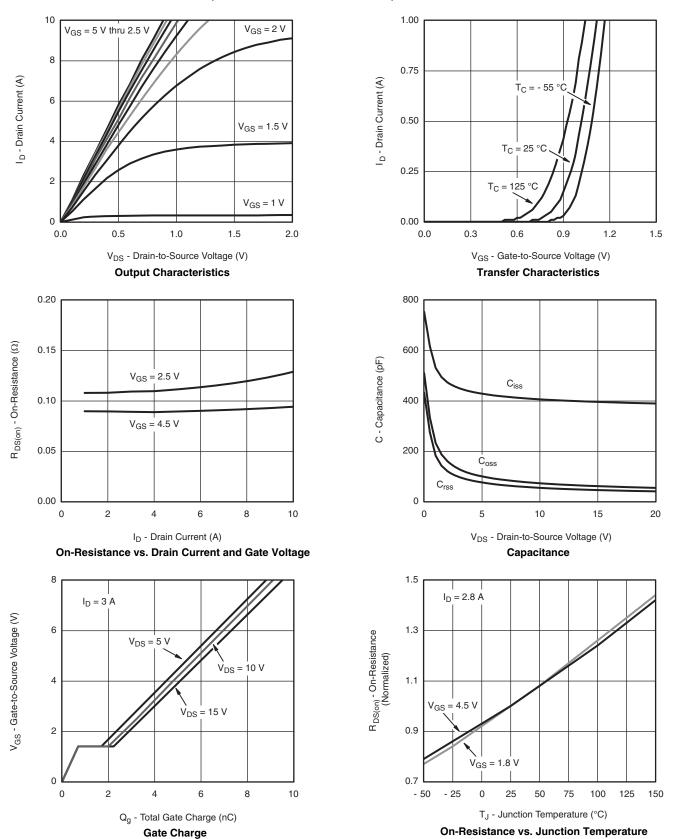
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



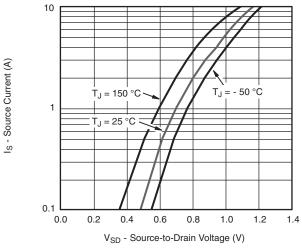
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



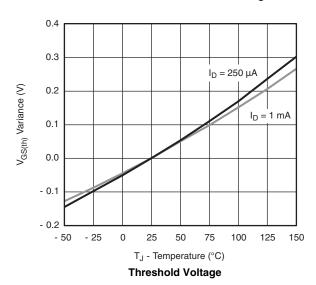
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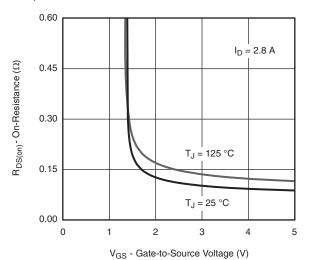
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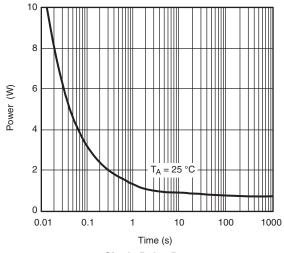


#### Source-Drain Diode Forward Voltage

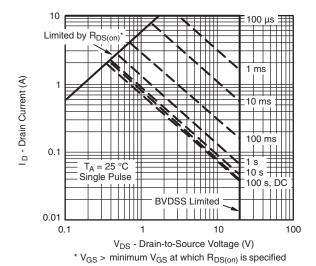




On-Resistance vs. Gate-to-Source Voltage

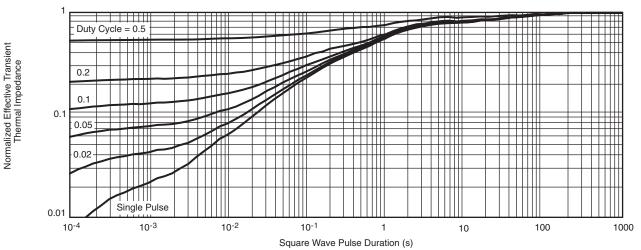


Single Pulse Power

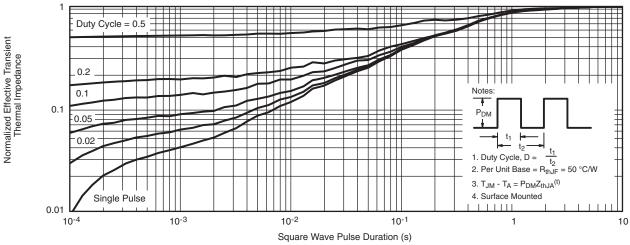




## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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Document Number: 91000 www.vishay.com
Revision: 11-Mar-11 1