

# ZXMN6A09G 60V SOT223 N-channel enhancement mode MOSFET

## **Summary**

V <sub>(BR)DSS</sub>	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)
60	0.040 @ V <sub>GS</sub> = 10V	7.5
	0.060 @ V <sub>GS</sub> = 4.5V	6.2



## **Description**

This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage power management applications.

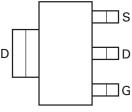
## **Features**

- · Low on-resistance
- · Fast switching speed
- · Low threshold
- · Low gate drive
- SOT223 package

# G

## **Applications**

- DC-DC converters
- · Power management functions
- · Disconnect switches
- · Motor control



Pinout - top view

## **Ordering information**

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN6A09GTA	7	12	1000

# **Device marking**

ZXMN 6A09

# **Absolute maximum ratings**

Parameter	Symbol	Limit	Unit
Drain-source voltage	$V_{DSS}$	60	V
Gate-source voltage	$V_{GS}$	±20	V
Continuous drain current @ V <sub>GS</sub> =10V; T <sub>amb</sub> =25°C <sup>(b)</sup>	I <sub>D</sub>	7.5	Α
@ V <sub>GS</sub> =10V; T <sub>amb</sub> =70°C <sup>(b)</sup>		6	
@ V <sub>GS</sub> =10V; T <sub>amb</sub> =25°C <sup>(a)</sup>		5.4	
Pulsed drain current <sup>(c)</sup>	I <sub>DM</sub>	33	Α
Continuous source current (body diode)(b)	I <sub>S</sub>	3.5	Α
Pulsed source current (body diode)(c)	I <sub>SM</sub>	33	Α
Power dissipation at T <sub>amb</sub> =25°C <sup>(a)</sup>	P <sub>D</sub>	2	W
Linear derating factor		16	mW/°C
Power dissipation at T <sub>amb</sub> =25°C <sup>(b)</sup>	$P_{D}$	3.9	W
Linear derating factor		31	mW/°C
Operating and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	°C

# Thermal resistance

Parameter	Symbol	Limit	Unit
Junction to ambient <sup>(a)</sup>	$R_{\Theta JA}$	62.5	°C/W
Junction to ambient <sup>(b)</sup>	$R_{\Theta JA}$	32.2	°C/W

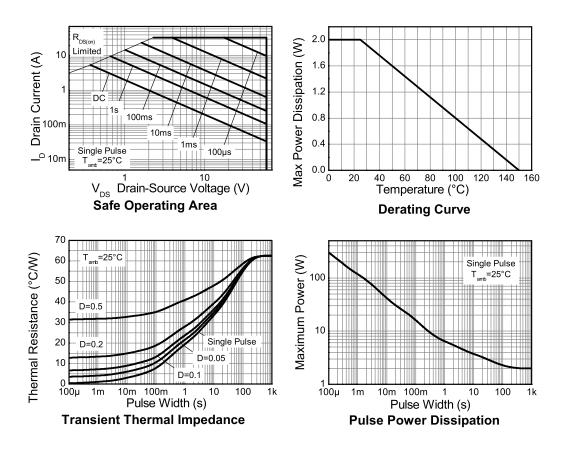
## NOTES:

<sup>(</sup>a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

<sup>(</sup>b) For a device surface mounted on FR4 PCB measured at t  $\leq$ 10 sec.

<sup>(</sup>c) Repetitive rating 25mm x 25mm FR4 PCB, D=0.02 pulse width=300μs - pulse width limited by maximum junction temperature.

# **Characteristics**



# Electrical characteristics (at $T_{amb} = 25$ °C unless otherwise stated)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Static	•	•				
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	60			V	I <sub>D</sub> = 250μA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>			1	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> =0V
Gate-body leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Gate-source threshold voltage	V <sub>GS(th)</sub>	1.0		3.0	V	I <sub>D</sub> = 250μA, V <sub>DS</sub> =V <sub>GS</sub>
Static drain-source on-state	R <sub>DS(on)</sub>			0.040	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 8.2A
resistance (*)				0.060	Ω	$V_{GS} = 4.5V, I_D = 7.4A$
Forward transconductance(*)(‡)	9 <sub>fs</sub>		15		S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 8.2A
Dynamic <sup>(‡)</sup>		II.	I.	I.		
Input capacitance	C <sub>iss</sub>		1407		рF	V <sub>DS</sub> = 40V, V <sub>GS</sub> =0V
Output capacitance	C <sub>oss</sub>		121		pF	f=1MHz
Reverse transfer capacitance	C <sub>rss</sub>		59		pF	
Switching (†) (‡)	l	I	I	I		
Turn-on-delay time	t <sub>d(on)</sub>		4.9		ns	V <sub>DD</sub> = 15V, I <sub>D</sub> = 3.5A
Rise time	t <sub>r</sub>		5.0		ns	R <sub>G</sub> ≅6.0Ω, V <sub>GS</sub> = 10V
Turn-off delay time	t <sub>d(off)</sub>		25.3		ns	
Fall time	t <sub>f</sub>		4.6		ns	
Total gate charge	Qg		12.4		nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 5V I <sub>D</sub> = 3.5A
Total gate charge	$O_g$		24.2		nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 5V
Gate-source charge	O <sub>gs</sub>		5.2		nC	I <sub>D</sub> = 3.5A
Gate drain charge	$Q_{gd}$		3.5		nC	
Source-drain diode		I	I	I		1
Diode forward voltage <sup>(*)</sup>	V <sub>SD</sub>		0.85	0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> = 6.6A, V <sub>GS</sub> =0V
Reverse recovery time(‡)	t <sub>rr</sub>		26.3		ns	T <sub>j</sub> =25°C, I <sub>S</sub> = 3.5A,
Reverse recovery charge <sup>(‡)</sup>	O <sub>rr</sub>		26.6		nC	di/dt=100A/μs

## NOTES:

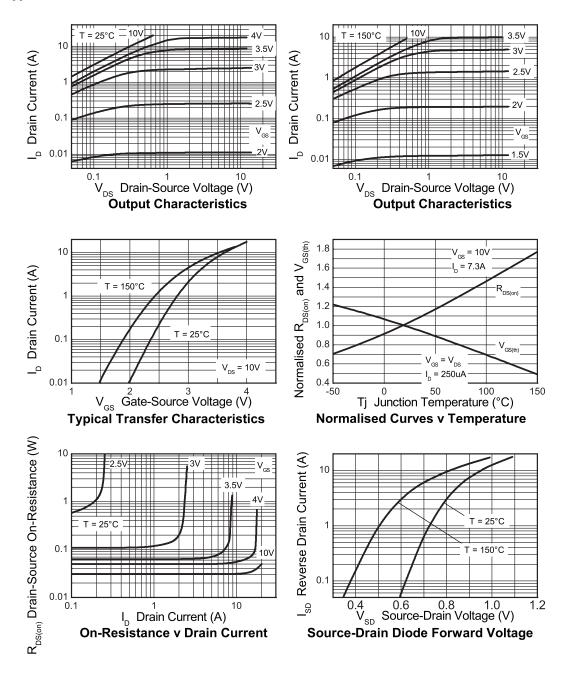
Downloaded from Elcodis.com electronic components distributor

<sup>(\*)</sup> Measured under pulsed conditions. Pulse width  $\leq$ 300 s; duty cycle  $\leq$ 2%.

<sup>(†)</sup> Switching characteristics are independent of operating junction temperature.

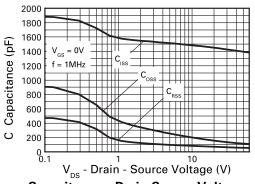
<sup>(‡)</sup> For design aid only, not subject to production testing.

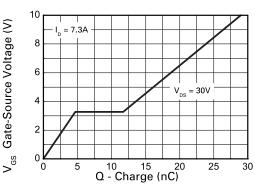
## **Typical characteristics**



# ZXMN6A09G

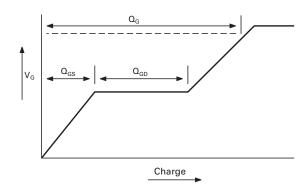
# **Typical characteristics**

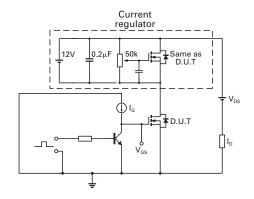




Capacitance v Drain-Source Voltage

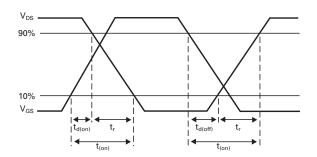
Gate-Source Voltage v Gate Charge

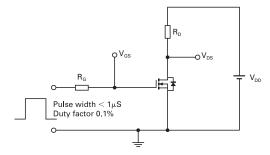




Basic gate charge waveform

Gate charge test circuit



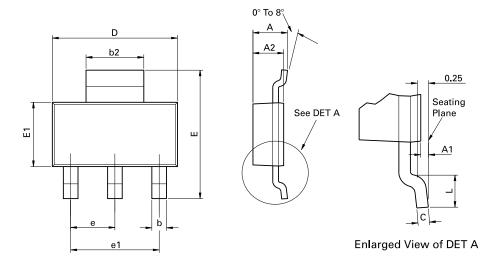


Switching time waveforms

Switching time test circuit

# ZXMN6A09G

# Package outline - SOT223



Conforms to JEDEC TO-261 AA Issue B

DIM	Millin	neters	Inc	hes	DIM	Millimeters		Inches	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	-	1.80	-	0.071	е	2.30	BSC	0.090	5 BSC
A1	0.02	0.10	0.0008	0.004	e1	4.60	BSC	0.181	BSC
b	0.66	0.84	0.026	0.033	Е	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
С	0.23	0.33	0.009	0.013	L	0.90	-	0.355	-
D	6.30	6.70	0.248	0.264	-	-	-	-	-

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

# ZXMN6A09G

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Issue 3 - June 2007

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