

Interface and switching (30V, 200mA)

2SK2731

Structure

Silicon N-channel MOSFET

Features

- 1) Low on-resistance.
- 2) High-speed switching.
- 3) Low-voltage drive(4V).

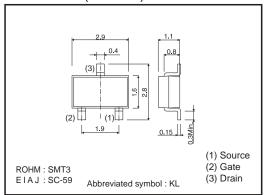
Application

Switching

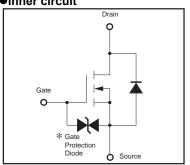
Packaging specifications

	<u> </u>	
Туре	Package	Taping
	Code	T146
	Basic ordering unit (pieces)	3000
2SK2731		0

●Dimensions (Unit: mm)



●Inner circuit



* A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use.Use a protection circuit when the fixed voltage are exceeded.

●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	30	V	
Gate-source voltage		Vgss	±20	V	
Drain current	Continuous	lo	200	mA	
	Pulsed	IDP*	800	mA	
Total power dissipation		Pp	200	mW	
Channel temperature		Tch	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

^{*} Pw≤10μs, Duty cycle≤1%

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●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
Drain-source breakdown voltage	V(BR) DSS	30	_	_	V	ID = 1mA, VGS = 0V
Zero gate voltage drain current	Ipss	_	_	10	μΑ	V _{DS} = 30V, V _{GS} = 0V
Gate threshold voltage	VGS (th)	1.0	_	2.5	V	V _{DS} = 10V, I _D = 1mA
Static drain-source on-state	Б	_	1.5	2.8		I _D = 0.1A, V _{GS} = 10V
resistance	RDS(on)	_	2.8	4.5	Ω	I _D = 0.1A, V _{GS} = 4V
Forward transfer admittance	Y _{fs} *	100	_	_	mS	I _D = 0.1A, V _{DS} = 10V
Input capacitance	Ciss	_	25	_	pF	V _{DS} = 10V
Output capacitance	Coss	_	15	_	pF	V _G S = 0V
Reverse transfer capacitance	Crss	_	10	_	pF	f = 1MHz
Turn-on delay time	td (on)	_	15	_	ns	I _D = 0.1A, V _{DD} ≒ 15V
Rise time	tr	-	20	_	ns	Vgs = 10V
Turn-off delay time	td (off)	_	90	_	ns	$R_L = 150\Omega$
Fall time	tf	-	100	_	ns	$R_G = 10\Omega$

 $^{* \} Pw \leq 300ms, \ \ Duty \ cycle \leq 1\%$

•Electrical characteristic curves

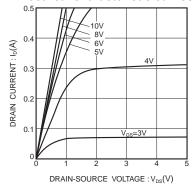


Fig.1 Typical Output Characteristics

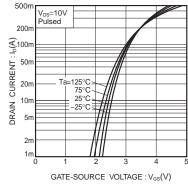


Fig.2 Typical Transfer Characteristics

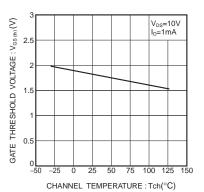


Fig.3 Gate Threshold Voltage vs. Channel Temperature

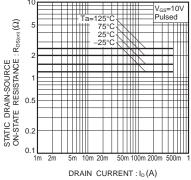


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current (I)

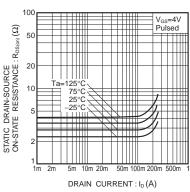


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current (II)

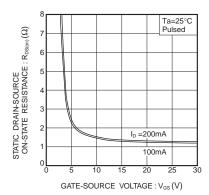


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

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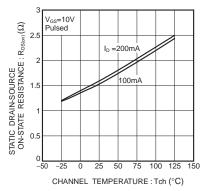


Fig.7 Static Drain-Source On-State Resistance vs. Channel Temperature

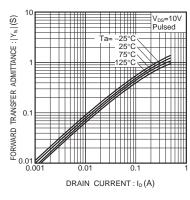


Fig.8 Forward Transfer Admittance vs. Drain Current

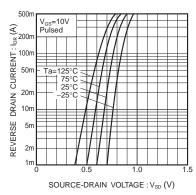


Fig.9 Reverse Drain Current vs. Source-Drain Voltage (I)

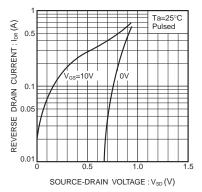


Fig.10 Reverse Drain Current vs. Source-Drain Voltage (II)

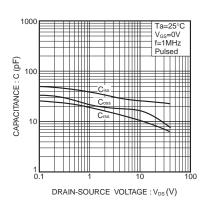


Fig.11 Typical Capacitance vs. Drain-Source Voltage

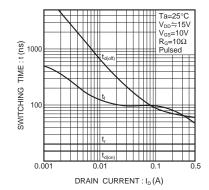


Fig.12 Switching Characteristics (See Figure. 13 and 14 for measurement circuit)

●Measurement circuit

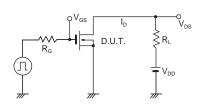


Fig.13 Switching Time Test Circuit

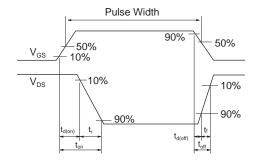


Fig.14 Switching Time Waveforms

Notes

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