

Vishay Siliconix

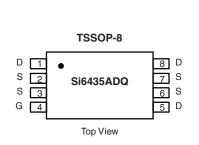
P-Channel 30-V (D-S) MOSFET

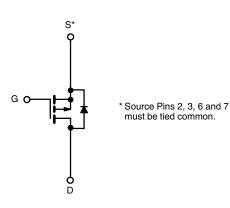
PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 30	0.030 at V _{GS} = - 10 V	± 5.5		
	0.055 at V _{GS} = - 4.5 V	± 4.1		

FEATURES

- Halogen-free
- TrenchFET[®] Power MOSFETs







Ordering Information: Si6435ADQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	$_{\rm A}$ = 25 °C, unle	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Drain Current /T 150 °C)	T _A = 25 °C	- I _D	± 5.5	± 4.7	•	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		± 4.5	± 3.7		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	± 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _s	- 1.35	- 0.95		
	T _A = 25 °C	– P _D	1.5	1.05	W	
Maximum Power Dissipation ^a	T _A = 70 °C		1.0	0.67		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R _{thJA}	65	83	
Maximum Junction-to-Ambient ^a	Steady State		100	120	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		43	52	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static	•						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1.0			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 24 V, V _{GS} = 0 V			- 1		
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$				μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -10 V$			٨		
		$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 7			A	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -5.5 \text{ A}$		0.024	0.030	0	
		V _{GS} = - 4.5 V, I _D = - 4.1 A		0.042	0.055		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -5.5 \text{ A}$		12		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S}$ = - 1.3 A, $V_{\rm GS}$ = 0 V		- 0.8	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Qg			15	20	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 5 V, I_{D} = - 5.5 A		5.7			
Gate-Drain Charge	Q _{gd}			5.0			
Turn-On Delay Time	t _{d(on)}			12	20		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		10	20	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 1 A, V_{GEN} = - 10 V, R_{G} = 6 Ω		42	60		
Fall Time	t _f			17	25		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.3 A, di/dt = 100 A/μs		40	80		

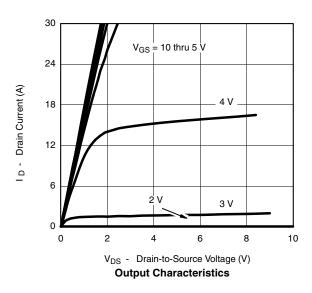
Notes:

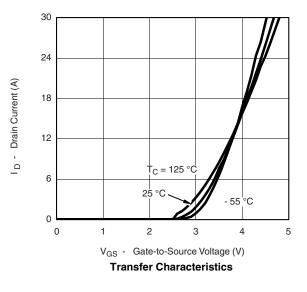
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





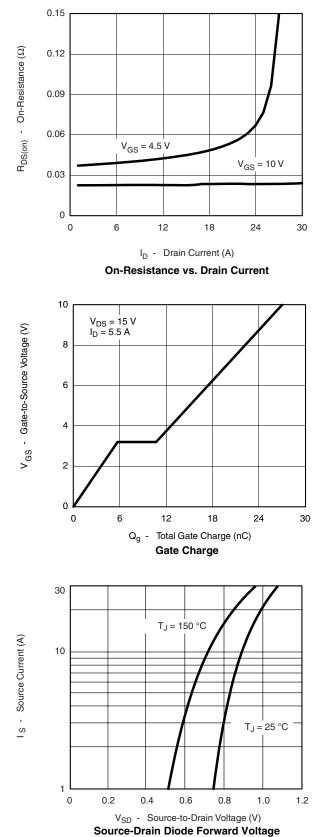
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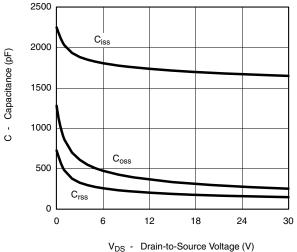


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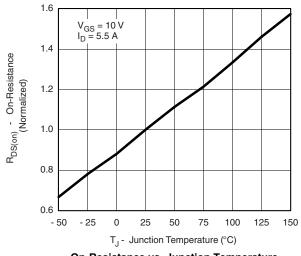
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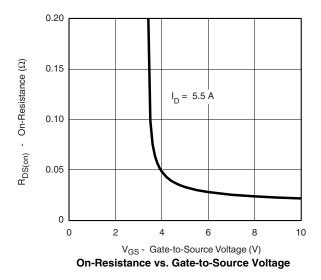




Capacitance



On-Resistance vs. Junction Temperature

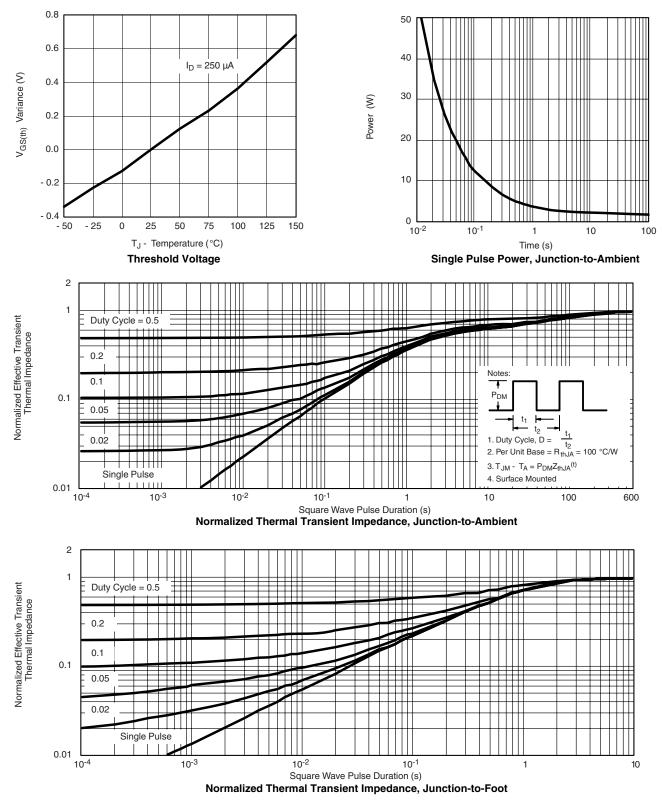


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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71104.

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