

March 2006

FDD8770/FDU8770 N-Channel PowerTrench® MOSFET

25V, 35A, 4.0m Ω

General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $r_{\text{DS}(\text{on})}$ and fast switching speed.

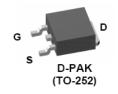


Features

- Max $r_{DS(on)} = 4.0 m\Omega$ at $V_{GS} = 10 V$, $I_D = 35 A$
- Max $r_{DS(on)} = 5.5 m\Omega$ at $V_{GS} = 4.5 V$, $I_D = 35 A$
- Low gate charge: Q_{q(10)} = 52nC(Typ), V_{GS} = 10V
- Low gate resistance
- RoHS Compliant

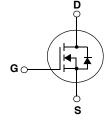
Application

- Vcore DC-DC for Desktop Computers and Servers
- VRM for Intermediate Bus Architecture









MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V_{DS}	Drain to Source Voltage		25	V
V_{GS}	Gate to Source Voltage		±20	V
	Drain Current -Continuous (Package Limited)		35	
I_D	-Continuous (Die Limited)		210	Α
	-Pulsed	(Note 1)	407	
E _{AS}	Single Pulse Avalanche Energy	(Note 2)	113	mJ
P_{D}	Power Dissipation		115	W
T _J , T _{STG}	Operating and Storage Temperature		-55 to 175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case TO-252,TO-251	1.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient TO-252,TO-251	100	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient TO-252,1in ² copper pad area	52	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD8770	FDD8770	TO-252AA	13"	12mm	2500 units
FDU8770	FDU8770	TO-251AA	N/A(Tube)	N/A	75 units
FDU8770	FDU8770_F071	TO-251AA	N/A(Tube)	N/A	75 units

Electrical Characteristics T_J = 25°C unless otherwise noted

Symbol	Parameter	rest Conditions	IVIIII	тур	IVIAX	Ullits
Off Char	acteristics					
B _{VDSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V$	25			V
$\frac{\Delta B_{VDSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, referenced to 25°C		13.6		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 20V, V _{GS} = 0V			1 250	μА
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±20V			±100	nA

On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250 \mu A$	1.2	1.6	2.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 250μA, referenced to 25°C		-5.9		mV/°C
		V _{GS} = 10V, I _D = 35A		3.3	4.0	
r	Drain to Source On Resistance	$V_{GS} = 4.5V, I_D = 35A$		4.0	5.5	mΩ
r _{DS(on)} Drain to Source On Resistance	$V_{GS} = 10V, I_D = 35A$ $T_J = 175^{\circ}C$		4.8	5.9	11122	

Dynamic Characteristics

C _{iss}	Input Capacitance	10/1/	2795	3720	pF
Coss	Output Capacitance	V _{DS} = 13V, V _{GS} = 0V, f = 1MHz	685	915	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1101112	450	675	pF
R _g	Gate Resistance	f = 1MHz	1.5		Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		10	20	ns
t _r	Rise Time	$V_{DD} = 13V, I_{D} = 35A$ $V_{GS} = 10V, R_{GS} = 5\Omega$	12	22	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10V, R_{GS} = 502$	49	78	ns
t _f	Fall Time		25	40	ns
Q_g	Total Gate Charge	V _{GS} = 0V to 10V	52	73	nC
Qg	Total Gate Charge	$V_{GS} = 0V \text{ to } 5V$ $V_{DD} = 13V$ $I_{D} = 35A$	29	41	nC
Q_{gs}	Gate to Source Gate Charge	$I_D = 35A$ $I_a = 1.0 \text{mA}$	8.1		nC
Q _{gd}	Gate to Drain "Miller" Charge	.g	11		nC

Drain-Source Diode Characteristics

V	Source to Drain Diode Forward Voltage	V _{GS} = 0V, I _S = 35A			\/
v_{SD}	Source to Drain Diode 1 of ward voltage	V _{GS} = 0V, I _S = 15A	0.79	1.0	v
t _{rr}	Reverse Recovery Time	I _F = 35A, di/dt = 100A/μs	32	48	ns
Q _{rr}	Reverse Recovery Charge	$I_F = 35A$, di/dt = 100A/ μ s	25	38	nC

Notes:
1: Pulse time < 300μ s, Duty cycle = 2%.
2: Starting $T_J = 25$ °C, L = 0.3mH, $I_{AS} = 27.5$ A , $V_{DD} = 23$ V, $V_{GS} = 10$ V.

Typical Characteristics T_J = 25°C unless otherwise noted

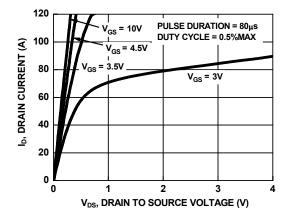


Figure 1. On Region Characteristics

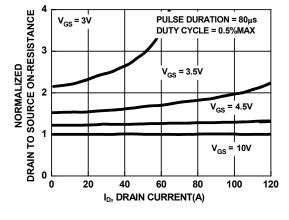


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

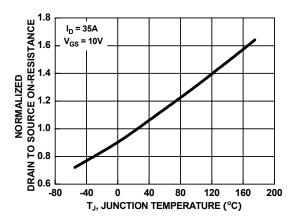


Figure 3. Normalized On Resistance vs Junction Temperature

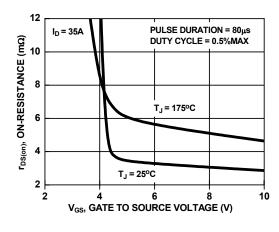


Figure 4. On-Resistance vs Gate to Source Voltage

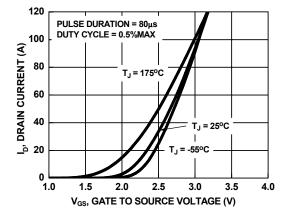


Figure 5. Transfer Characteristics

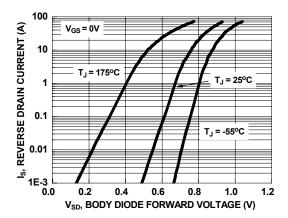
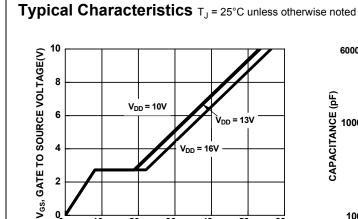
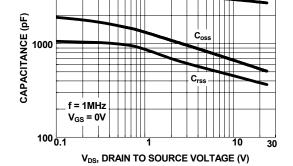


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

3 www.fairchildsemi.com



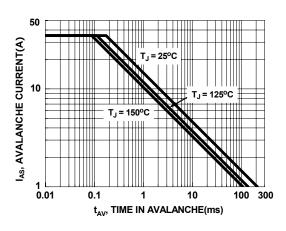


6000

Figure 7. Gate Charge Characteristics

Q_q, GATE CHARGE(nC)

Figure 8. Capacitance vs Drain to Source Voltage



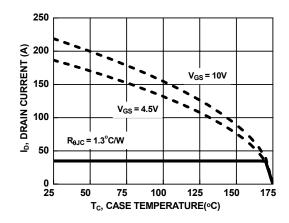
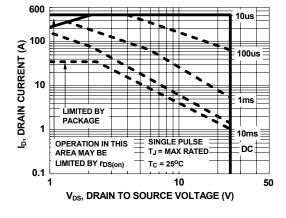


Figure 9. Unclamped Inductive Switching Capability

Figure 10. Maximum Continuous Drain Current vs **Case Temperature**



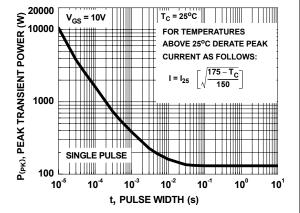


Figure 11. Forward Bias Safe Operating Area

Figure 12. Single Pulse Maximum Power Dissipation

www.fairchildsemi.com

Downloaded from Elcodis.com electronic components distributor

10¹

10°

10⁻¹

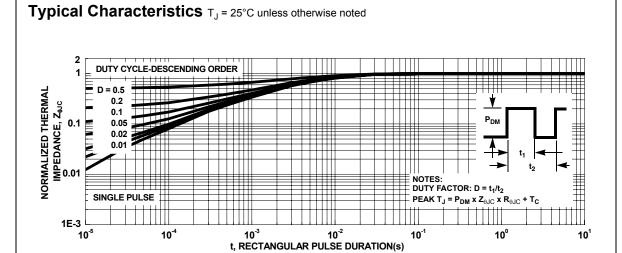


Figure 13. Transient Thermal Response Curve

10⁻⁵

10⁴

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST [®]	ISOPLANAR™	PowerEdge™	SuperFET™
ActiveArray™	FASTr™	LittleFET™	PowerSaver™	SuperSOT™-3
Bottomless™	FPS™	MICROCOUPLER™	PowerTrench [®]	SuperSOT™-6
Build it Now™	FRFET™	MicroFET™	QFET [®]	SuperSOT™-8
CoolFET™	GlobalOptoisolator™	MicroPak™	QS™	SyncFET™
CROSSVOLT™	GTO™	MICROWIRE™	QT Optoelectronics™	TCM™
DOME™	HiSeC™	MSX™	Quiet Series™	TinyLogic [®]
EcoSPARK™	I ² C™	MSXPro™	RapidConfigure™	TINYOPTO™
E ² CMOS™	i-Lo™	OCX™	RapidConnect™	TruTranslation™
EnSigna™	ImpliedDisconnect™	OCXPro™	μSerDes™	UHC™
FACT™	IntelliMAX™	OPTOLOGIC [®]	ScalarPump™	UniFET™
FACT Quiet Series	ТМ	OPTOPLANAR™	SILENT SWITCHER®	UltraFET [®]
Across the board. Around the world.™		PACMAN™	SMART START™	VCX™
The Power Franchise®		POP™	SPM™	Wire™
Programmable Acti	ve Droop™	Power247™	Stealth™	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I19