TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

TPCA8019-H

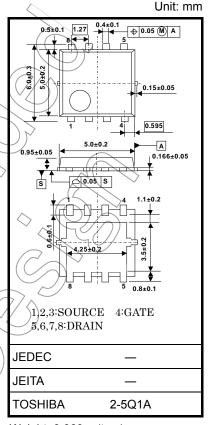
High-Efficiency DC/DC Converter Applications
Notebook PC Applications

Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 15.5 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = $2.3 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 130 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 30 \text{ V})$
- Enhancement mode: $V_{th} = 1.5$ to 2.5 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)/

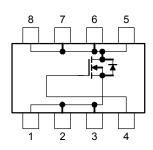
Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V
Gate-source voltage		V _{GSS}	±20	< <u>/</u>
Drain current	DC (Note 1)	ID((45	A
	Pulsed (Note 1)		135	,
Drain power dissipation	on (Tc=25°C)	(PD \	45	// w
Drain power dissipation	on (t = 10 s) (Note 2a)	PD	2.8	W
Drain power dissipation	on (t = 10 s) (Nøte 2b)	PD	(1.6/)	W
Single-pulse avalanch	ne energy (Note 3)	EAS	263	mJ
Avalanche current		I _{AR}	45	Α
Repetitive avalanche energy/ (Tc=25°C) (Note 4)		EAR	3.4	mJ
Channel temperature		Tch	150	°C
Storage temperature	range	Tstg	–55 to 150	°C



Weight: 0.069 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high

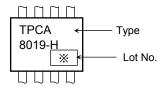
temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient $(t=10 \; s) \eqno(Note \; 2a)$	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

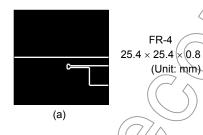
Marking (Note 5)

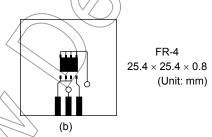


Note 1: The channel temperature should not exceed 150 C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = 24 \text{ V}$, $T_{Ch} = 25^{\circ}\text{C}$ (initial), $L = 100 \mu\text{H}$, $R_{G} \neq 25^{\circ}\Omega$, $I_{AR} = 45 \text{ A}$

Note 4: Repetitive rating; pulse width limited by max, channel temperature

Note 5: * Weekly code: (Three digits)

Week of manufacture

(01) for first week of year, continuing up to 52 or 53)

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Year of manufacture

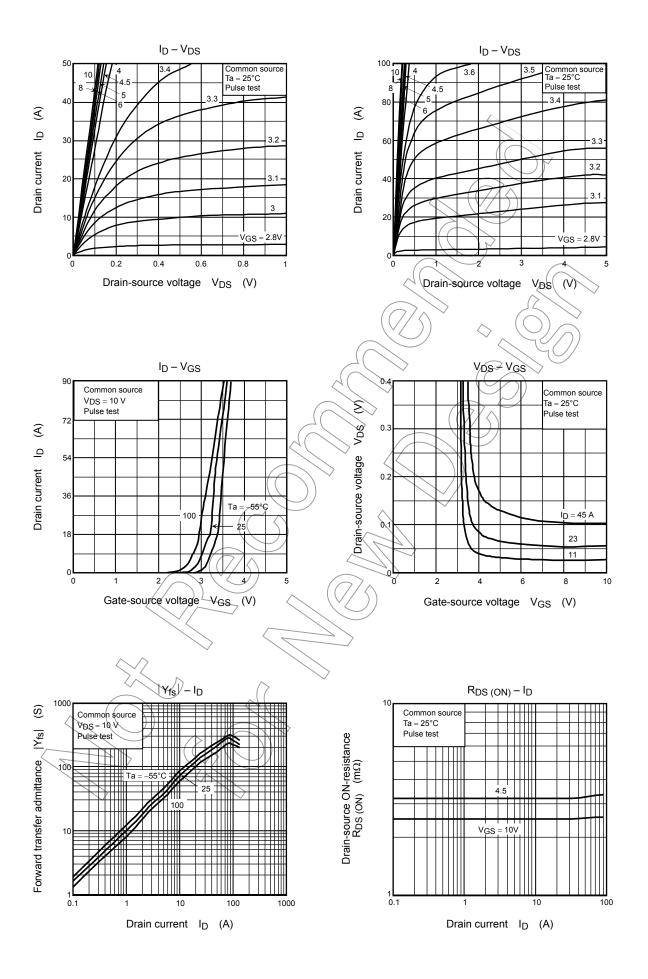
(The last digit of the calendar year)

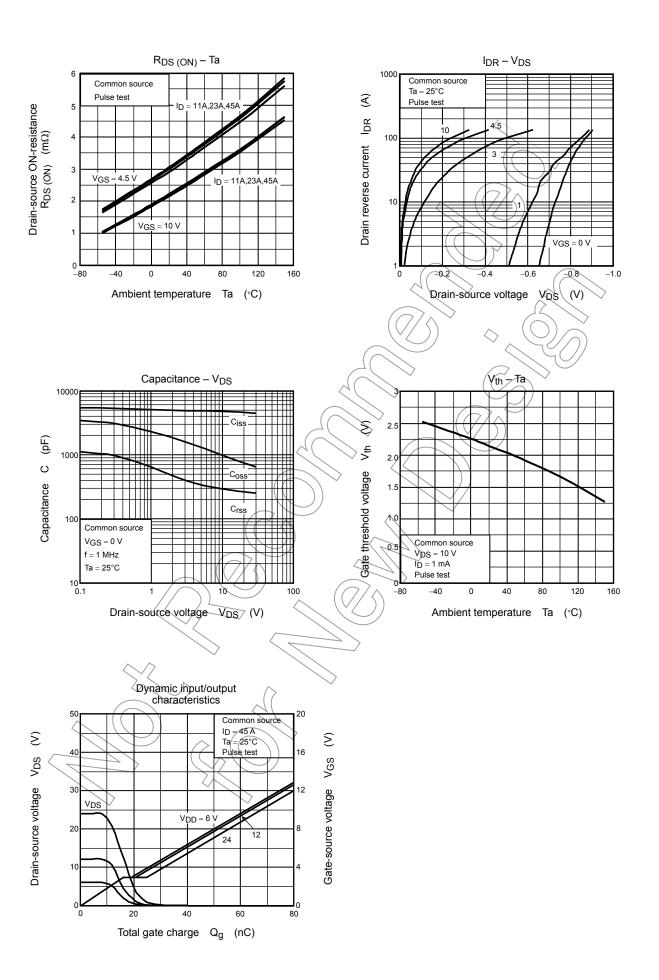
Electrical Characteristics (Ta = 25°C)

Cha	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	
Gate threshold vo	ltage	V_{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.5) >	2.5	٧
Drain-source ON-resistance		D	V _{GS} = 4.5 V, I _D = 23 A) 	3.1	4.1	- mΩ
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 23 A	\rightarrow	2.3	3.1	
Forward transfer a	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 23 A	65	130	_	S
Input capacitance		C _{iss}		_	4614	6150	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	284	426	pF
Output capacitance		Coss			<1100	7	
Gate resistance		rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	-	1.0) 1.5	Ω
Switching time	Rise time	t _r	V _{GS} 0 V I _D = 23A V _{OUT} G _G	7	5.8	_	ns
	Turn-on time	t _{on}		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	18	_	
	Fall time	t _f	G \$ 0.0		9.0	_	
	Turn-off time	t _{off}	Duty ≦ 1%, t _W =10 μs		56	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$	_	66	_	
			$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} = 45 \text{ A}$		34		
Gate-source char	ge 1 /	Qgs ₁		_	16	_	nC
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \simeq 24 V_{V} V_{GS} = 10 V, I_{D} = 45 A$		9.0		
Gate switch charg	Gate switch charge			_	15.5	_	

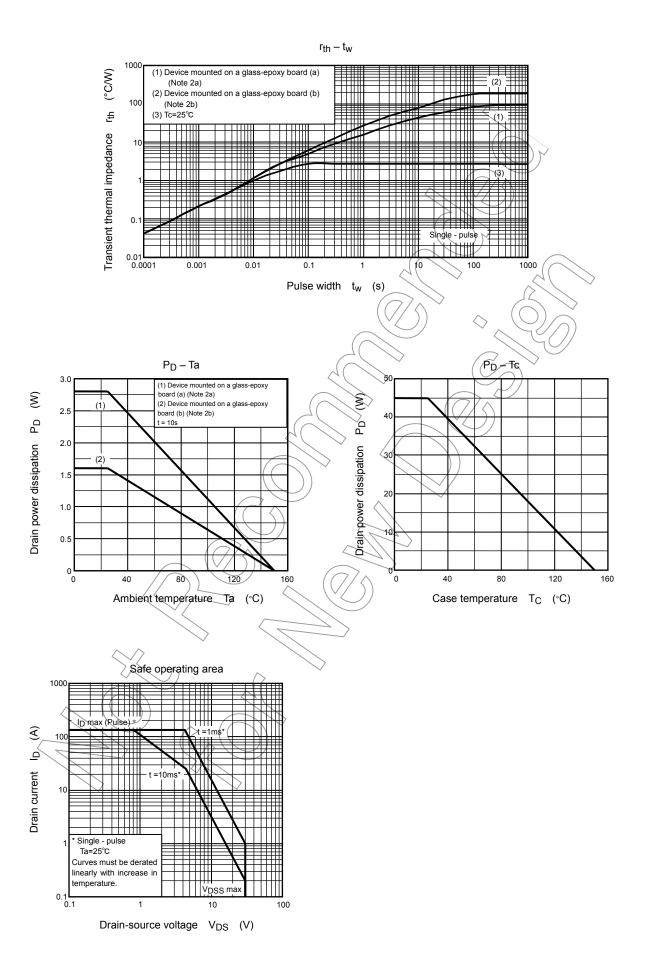
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP}	> -	_	_	135	Α
Forward voltage (diode)	VDSF	$I_{DR} = 45 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.2	V





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