# Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# RENESAS

# RJK0353DSP

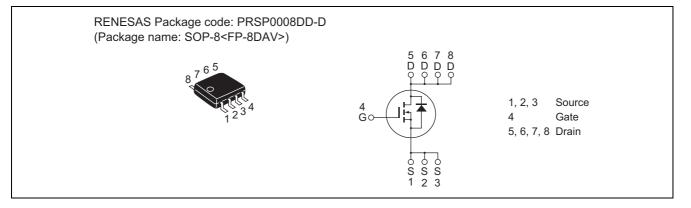
Silicon N Channel Power MOS FET Power Switching

REJ03G1648-0401 Rev.4.01 Apr 24, 2008

### Features

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- $R_{DS(on)} = 4.5 \text{ m}\Omega \text{ typ.}$  (at  $V_{GS} = 10 \text{ V}$ )
- Pb-free

### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	18	A
Drain peak current	Note1 I <sub>D(pulse)</sub>	144	A
Body-drain diode reverse drain current	I <sub>DR</sub>	18	A
Avalanche current	I <sub>AP</sub> Note 2	16	A
Avalanche energy	E <sub>AR</sub> Note 2	25.6	mJ
Channel dissipation	Pch Note3	2.0	W
Channel to ambient thermal impedance	θch-a <sup>Note3</sup>	62.5	°C/W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	۵°

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

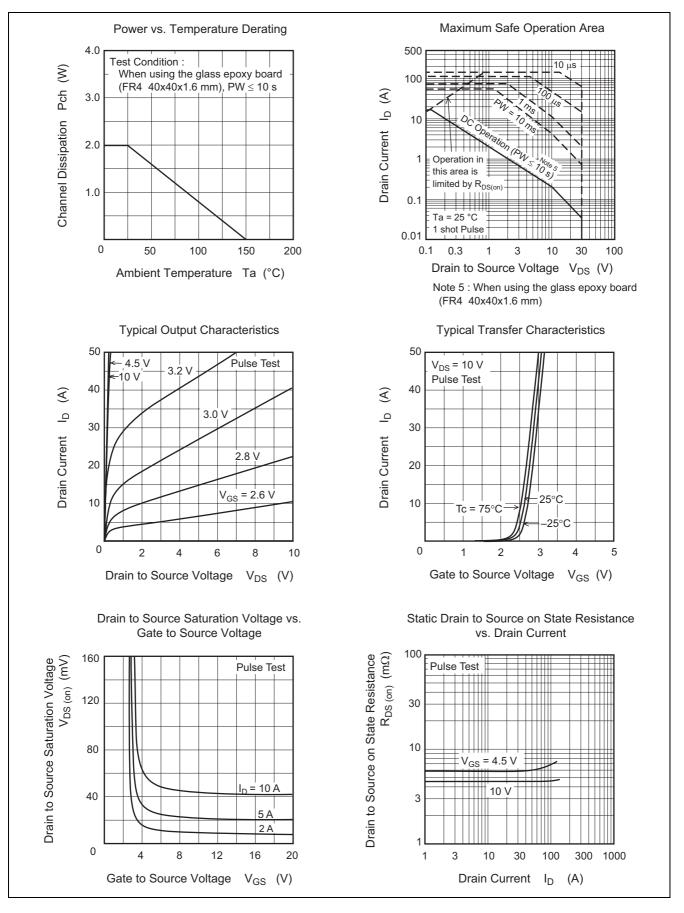
- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10s

# **Electrical Characteristics**

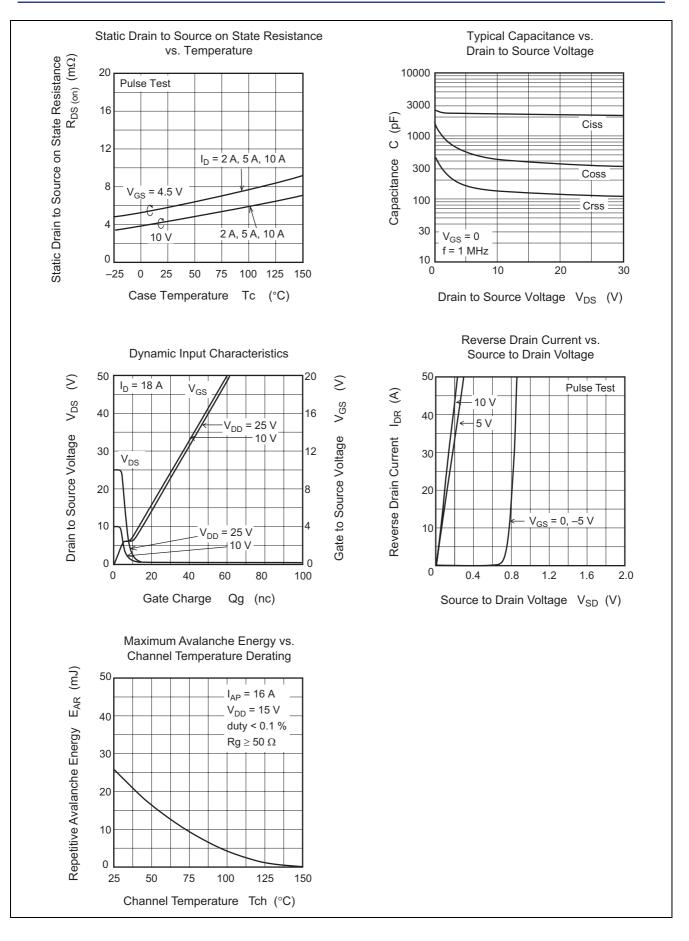
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	—	± 0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	—	1	μΑ	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	4.5	5.9	mΩ	$I_D = 9 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	5.9	8.3	mΩ	$I_D = 9 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$
Forward transfer admittance	y <sub>fs</sub>		41	_	S	$I_D = 9 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss		2180	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss		420	_	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss		135		pF	
Gate Resistance	Rg		2.0	_	Ω	
Total gate charge	Qg		15	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs		5.4	_	nC	V <sub>GS</sub> = 4.5 V I <sub>D</sub> = 9 A
Gate to drain charge	Qgd		3.0	_	nC	
Turn-on delay time	t <sub>d(on)</sub>		8.5		ns	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$
Rise time	tr		4.0	_	ns	$V_{DD} \cong 10 \text{ V}$ $R_{L} = 1.11 \Omega$ $Rg = 4.7 \Omega$
Turn-off delay time	t <sub>d(off)</sub>		46.4	_	ns	
Fall time	t <sub>f</sub>		6.0		ns	
Body–drain diode forward voltage	$V_{DF}$	_	0.8	1.04	V	$I_F = 18 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery	t <sub>rr</sub>		20	_	ns	$I_F = 18 \text{ A}, V_{GS} = 0$
time						$di_F/dt = 100 \text{ A}/\mu \text{s}$

Notes: 4. Pulse test

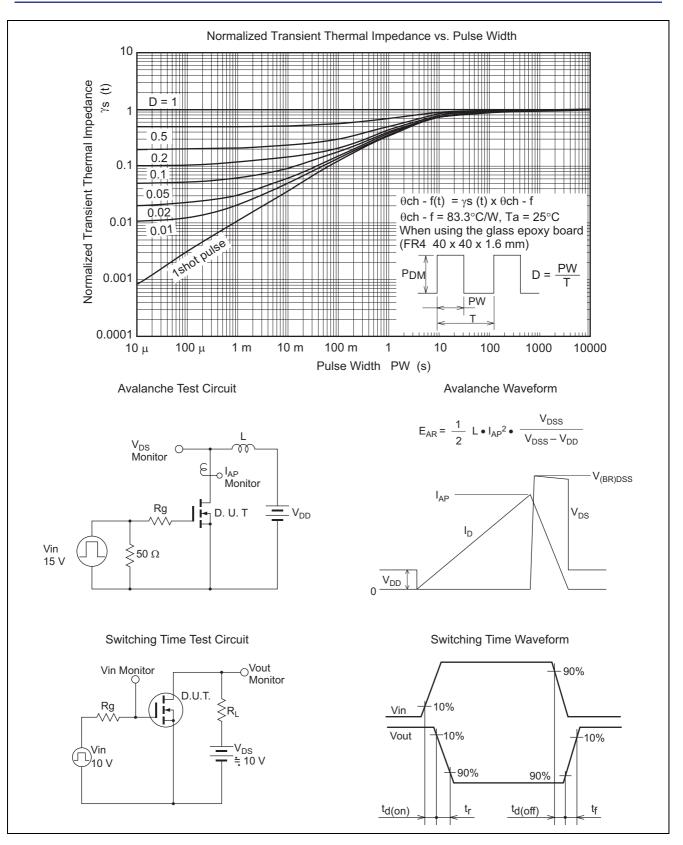
### **Main Characteristics**



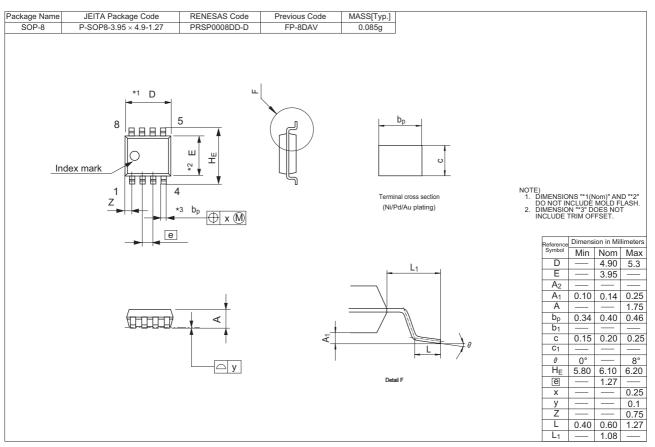
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### **Package Dimensions**



## **Ordering Information**

Part No.	Quantity	Shipping Container
RJK0353DSP-00-J0	2500 pcs	Taping

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