

## STV200N55F3

# N-channel 55 V, 1.8 mΩ, 200 A, PowerSO-10 STripFET™ Power MOSFET

#### **Features**

Туре	V <sub>DSS</sub>	R <sub>DS(on)</sub> max	I <sub>D</sub> <sup>(1)</sup>
STV200N55F3	55 V	$<$ 2.5 m $\Omega$	200 A

- 1. Current limited by package
- Conduction losses reduced
- Low profile, very low parasitic inductance

## **Application**

■ Switching applications

#### **Description**

This n-channel enhancement mode Power MOSFET is the latest refinement of ST's STripFET™ process. The resulting transistor shows extremely high packing density for low on resistance, rugged avalanche characteristics and low gate charge.

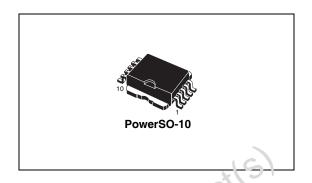


Figure 1. Internal schematic diagram and connection diagram (top view)

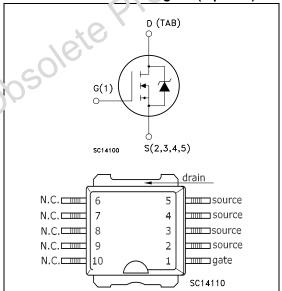


Table 1. Device summary

Order code	Marking	Package	Packaging
STV200N55F3	200N55F3	PowerSO-10	Tape and reel

March 2009 Rev 3 1/12

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STV200N55F3 **Electrical ratings** 

#### **Electrical ratings** 1

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-source voltage (v <sub>gs</sub> = 0)	55	V
V <sub>GS</sub>	Gate-source voltage	± 20	٧
I <sub>D</sub> <sup>(1)</sup>	Drain current (continuous) at T <sub>C</sub> = 25 °C	200	Α
I <sub>D</sub>	Drain current (continuous) at T <sub>C</sub> = 100 °C	170	Α
I <sub>DM</sub> <sup>(2)</sup>	Drain current (pulsed)	800	Α
P <sub>TOT</sub> (3)	Total dissipation at T <sub>C</sub> = 25 °C	300	W
	Derating factor	2.0	W/°C
E <sub>AS</sub> (4)	Single pulse avalanche energy	1.0	J
T <sub>stg</sub>	Storage temperature	-55 to 175	°C
T <sub>j</sub>	Operating junction temperature	-33 to 173	

- 1. Current limited by package
- 2. Pulse width limited by safe operating area
- 3. This value is rated according to Rthj-c
- 4. Starting Tj = 25 °C,  $I_D$  = 60 A,  $V_{DD}$  = 35 V

Table 3. Thermal data

Tj	Operating junction temperature						
1. Current lir	nited by package	900					
2. Pulse wid	2. Pulse width limited by safe operating area						
3. This value	is rated according to Rthj-c						
4. Starting 7	j = 25 °C, I <sub>D</sub> = 60 A, V <sub>DD</sub> = 35 V	0,					
	10						
	Table 3. Thermal data						
Table 3.	Thermal data						
Symbol	Thermal data  Parameter	Value	Unit				
	100	<b>Value</b> 0.5	Unit °C/W				

on 1 inc 1. When mounted on 1 inch<sup>2</sup> FR-4 2 oz Cu

Electrical characteristics STV200N55F3

## 2 Electrical characteristics

(T<sub>case</sub> = 25 °C unless otherwise specified)

Table 4. On /off states

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source breakdown voltage	I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0	55			٧
I <sub>DSS</sub>	Zero gate voltage drain current (V <sub>GS</sub> = 0)	$V_{DS}$ = Max rating, $V_{DS}$ = Max rating, $T_c$ = 125 °C			1 10	μ <b>Α</b> μ <b>Α</b>
I <sub>GSS</sub>	Gate body leakage current (V <sub>DS</sub> = 0)	V <sub>DS</sub> = ± 20 V			±100	nA
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4	V
R <sub>DS(on)</sub>	Static drain-source on resistance	$V_{GS} = 10 \text{ V}, I_D = 75 \text{ A}$		1.8	2.5	mΩ

Table 5. Dynamic

_		- y manne					
	Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
	$C_{iss}$ $C_{oss}$ $C_{rss}$	Input capacitance Output capacitance Reverse transfer capacitance	$V_{DS} = 25 \text{ V, f} = 1 \text{ MHz, } V_{GS} = 0$		6800 1450 15		pF pF pF
	Qg	Total gate charge	V <sub>DD</sub> = 44 V, I <sub>D</sub> = 120 A,		100		nC
	$oldsymbol{Q_gs} oldsymbol{Q_gd}$	Gate-source charge Gate-drain charge	V <sub>GS</sub> = 10 V Figure 14		30 26		nC nC
Obsole	ie P	roduct(s)					

Table 6. **Switching times** 

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub> t <sub>r</sub>	Turn-on delay time Rise time	$V_{DD}$ = 27.5 V, $I_{D}$ = 60 A $R_{G}$ = 4.7 $\Omega$ , $V_{GS}$ = 10 V, Figure 13		25 150		ns ns
t <sub>d(off)</sub>	Turn-off delay time Fall time	$V_{DD} = 27.5 \text{ V}, I_D = 60 \text{ A}$ $R_G = 4.7 \Omega, V_{GS} = 10 \text{ V},$ Figure 13		110 50		ns ns

Table 7. Source drain diode

Table 7.	- Jource drain diode	T .				
Symbo	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>SD</sub>	Source-drain current				200	Α
I <sub>SD</sub> <sup>(1)</sup>	Source-drain current (pulsed)				800	Α
V <sub>SD</sub> (2)	Forward on voltage	$I_{SD} = 120 \text{ A}, V_{GS} = 0$			1.5	V
t <sub>rr</sub>	Reverse recovery time	I <sub>SD</sub> = 120 A,di/dt = 100 A/μs		60		ns
Q <sub>rr</sub>	Reverse recovery charge	$V_{DD} = 35 \text{ V}, T_j = 150 \text{ °C}$	4	110	,	nC
I <sub>RRM</sub>	Reverse recovery current	Figure 18	0	3.5		Α
1. Pulse	width limited by safe operating area	1.5%				
2. Pulse	d: Pulse duration = 300 μs, duty cycle	1.5%				
		. 60'				
		703				
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# 2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

Figure 3. Thermal impedance

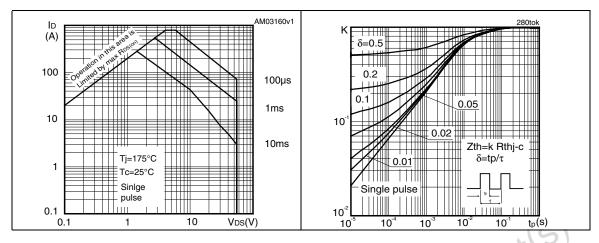


Figure 4. Output characteristics

Figure 5. Transfer characteristics

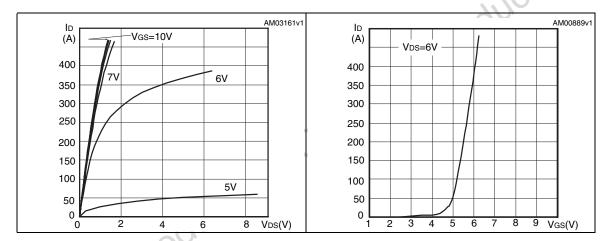


Figure 6. Normalized B<sub>VDSS</sub> vs temperature

Figure 7. Static drain-source on resistance

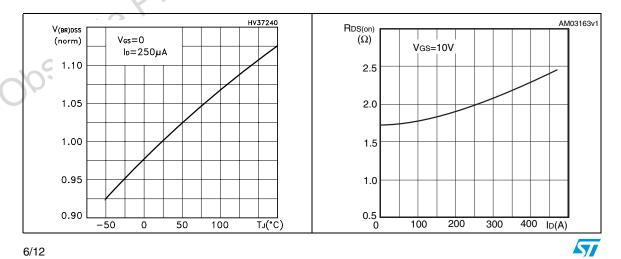


Figure 8. Gate charge vs gate-source voltage Figure 9. Capacitance variations

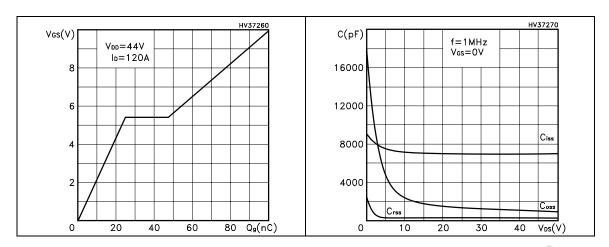


Figure 10. Normalized gate threshold voltage Figure 11. Normalized on resistance vs vs temperature temperature

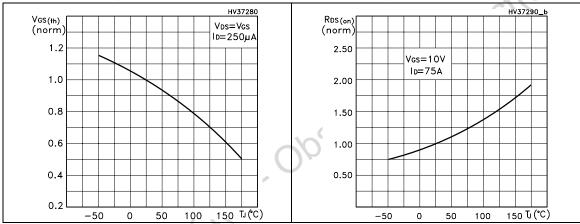
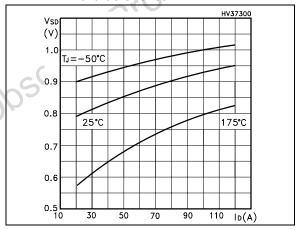


Figure 12. Source-drain diode forward characteristics



Test circuits STV200N55F3

## 3 Test circuits

Figure 13. Switching times test circuit for resistive load

Figure 14. Gate charge test circuit

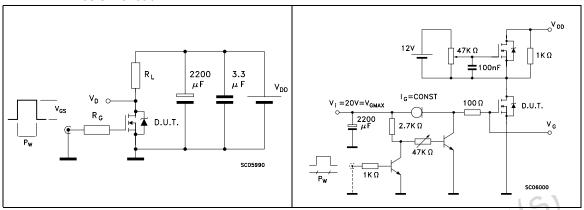


Figure 15. Test circuit for inductive load switching and diode recovery times

Figure 16. Unclamped inductive load test circuit

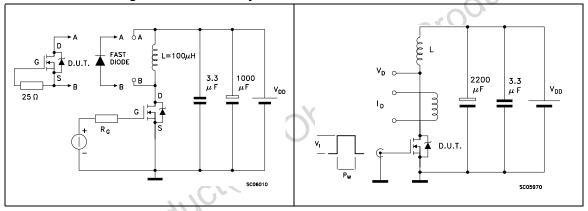
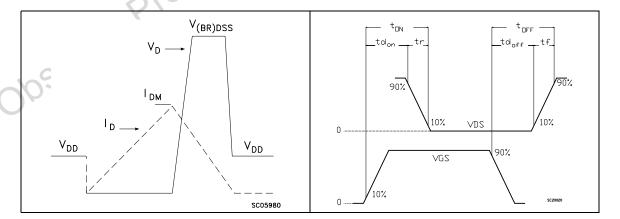


Figure 17. Unclamped inductive waveform

Figure 18. Switching time waveform



## 4 Package mechanical data

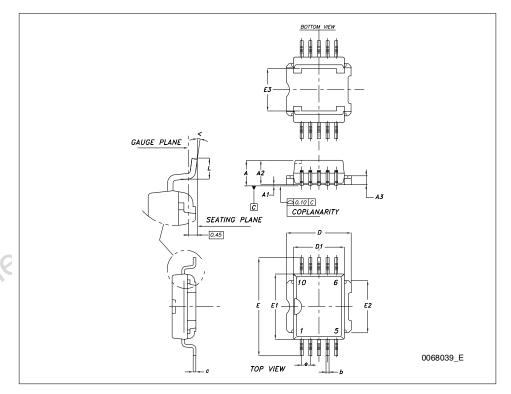
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Obsolete Product(s). Obsolete Product(s)

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#### PowerSO-10 mechanical data

Dim		mm	
Dilli	Min	Тур	Max
A			3.70
A1	0.00		0.10
A2	3.40		3.60
A3	1.25		1.35
b	0.40		0.53
С	0.35		0.55
D	9.40		9.60
D1	7.40		7.60
E	13.80		14.40
E1	9.30		9.50
E2	7.20		7.60
E3	5.90		6.10
е		1.27	
L	0.95		1.65
<	0°		8°



STV200N55F3 Revision history

# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
05-Mar-2008	1	First release.
10-Nov-2008	2	Document status promoted from preliminary to datasheet.
02-Mar-2009	3	Figure 2 has been updated.



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