DC-DC Converter (–20V, –1.5A) RTF015P02

●Features

- 1) Low on-resistance. ($80m\Omega$ at 2.5V)
- 2) High power package.
- 3) High speed switching.
- 4) Low voltage drive. (2.5V)

Applications

DC-DC converter

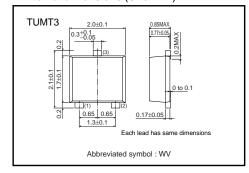
●Structure

Silicon P-channel MOS FET

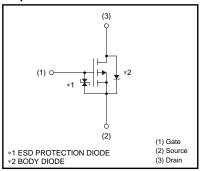
Packaging specifications

	Package	Taping		
Type	Code	TL		
	Basic ordering unit (pieces)	3000		
RTF015P02	0			

●External dimensions (Unit: mm)



●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	-20	V	
Gate-source voltage		V_{GSS}	±12	V	
Drain current	Continuous	I _D	±1.5	Α	
	Pulsed	IDP *1	±6	А	
Source current	Continuous	Is *1	-0.6	A	
(Body diode)	Pulsed	Isp	-6	A	
Total power dissipation		P _D *2	0.8	W	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	_	_	±10	μΑ	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	-20	-	_	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	_	-1	μΑ	Vps= -20V, Vgs=0V
Gate threshold voltage	V _{GS (th)}	-0.7	-	-2.0	V	V _{DS} = -10V, I _D = -1mA
Static drain-source on-state resistance	RDS (on)	_	100	135	mΩ	I _D = -1.5A, V _G S= -4.5V
		-	110	150	mΩ	Ip= -1.5A, Vgs= -4V
		-	180	250	mΩ	I _D = -1.5A, V _G S= -2.5V
Forward transfer admittance	Y _{fs} *	1.5	_	_	S	V _{DS} = -10V, I _D = -0.8A
Input capacitance	Ciss	-	560	-	pF	Vps= -10V
Output capacitance	Coss	-	90	-	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	_	55	_	pF	f=1MHz
Turn-on delay time	t d (on) *	-	12	-	ns	ID= -0.8A
Rise time	tr *	-	12	-	ns	VDD≒ -15V VGS= -4.5V
Turn-off delay time	t _{d (off)} *	-	38	-	ns	$R_1=9\Omega$
Fall time	tf *	_	12	_	ns	R _G s=10Ω
Total gate charge	Qg	-	5.2	-	nC	V _{DD} ≒−15V R _L ≒10Ω
Gate-source charge	Qgs	-	1.3	-	nC	$V_{GS} = -4.5V$ RGS=10 Ω
Gate-drain charge	Q _{gd}	-	1.4	-	nC	I _D = -1.5A

Body diode characteristics (source-drain characteristics)

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Forward voltage	VSD	-	_	-1.2	V	I _S = -0.6A, V _{GS} =0V



^{*1} Pw≤10μs, Duty cycle≤1% *2 Mounted on a ceramic board

Electrical characteristic curves

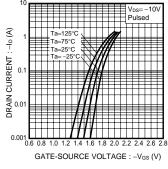


Fig.1 Typical Transfer Characteristics

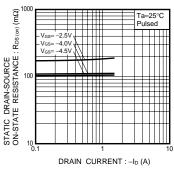


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

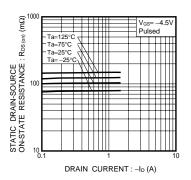


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

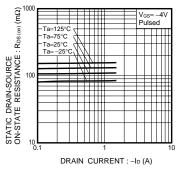


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

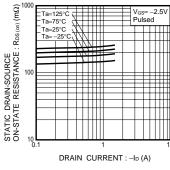


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

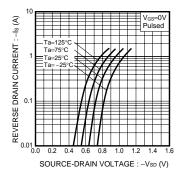


Fig.6 Reverse Drain Current vs. Source-Drain Voltage

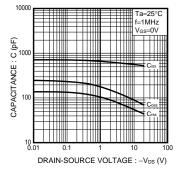


Fig.7 Typical Capacitance vs. Drain-Source Voltage

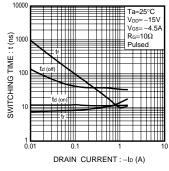


Fig.8 Switching Characteristics

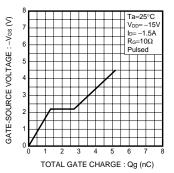


Fig.9 Dynamic Input Characteristics

●Measurement circuits

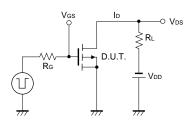


Fig.10 Switching Time Measurement Circuit

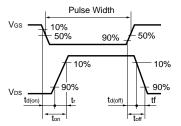


Fig.11 Switching Waveforms

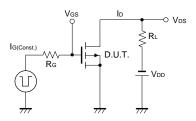


Fig.12 Gate Charge Measurement Circuit

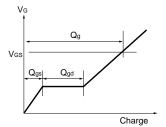


Fig.13 Gate Charge Waveforms

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