# 2.5V Drive Nch MOS FET

# 2SK3018

#### ●Structure

Silicon N-channel MOSFET

# Applications

Interfacing, switching (30V, 100mA)

#### ● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Low voltage drive (2.5V) makes this device ideal for portable equipment.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

# Packaging specifications

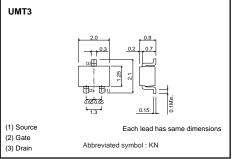
Туре	Package	Taping
	Code	T106
	Basic ordering unit (pieces)	3000
2SK3018		0

# ● Absolute maximum ratings (Ta=25°C)

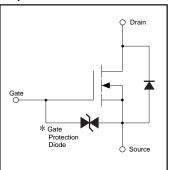
Parameter		Symbol	Limits	Unit
Drain-source voltage		Voss	30	V
Gate-source voltage		Vgss	±20	V
Drain current	Continuous	ΙD	±100	mA
	Pulsed	IDP*1	±400	mA
Total power dissipation		Pp*2	200	mW
Channel temperature		Tch	150	°C
Storage temperature		Tstg -55 to +150		°C

<sup>\*1</sup> Pw≤10μs, Duty cycle≤1%

# ●External dimensions (Unit : mm)



# ●Equivalent circuit



\*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use a protection circuit when the fixed voltages

# ●Thermal resistance

Parameter	Symbol	Limits	Unit	
Channel to ambient	Rth(ch-a) *	625	°C / W	

<sup>\*</sup> With each pin mounted on the recommended lands.

<sup>\*2</sup> With each pin mounted on the recommended lands.

#### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	±1	μΑ	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$
Drain-source breakdown voltage	V(BR)DSS	30	-	-	V	$I_D = 10\mu A$ , $V_{GS} = 0V$
Zero gate voltage drain current	IDSS	-	_	1	μΑ	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate threshold voltage	VGS(th)	0.8	-	1.5	V	V <sub>DS</sub> = 3V, I <sub>D</sub> = 100μA
Static drain-source on-state	RDS(on)	-	5	8	Ω	ID = 10mA, VGS = 4V
resistance	RDS(on)	-	7	13	Ω	ID = 1mA, VGS = 2.5V
Forward transfer admittance	Yfs	20	_	_	mS	V <sub>DS</sub> = 3V, I <sub>D</sub> = 10mA
Input capacitance	Ciss	-	13	-	pF	V <sub>DS</sub> = 5V
Output capacitance	Coss	_	9	_	pF	Vgs = 0V
Reverse transfer capacitance	Crss	_	4	_	pF	f = 1MHz
Turn-on delay time	td(on)	-	15	-	ns	I <sub>D</sub> = 10mA, V <sub>DD</sub> ≒ 5V
Rise time	tr	-	35	-	ns	Vgs = 5V
Turn-off delay time	td(off)	-	80	-	ns	R <sub>L</sub> = 500Ω
Fall time	tr	-	80	-	ns	R <sub>G</sub> = 10Ω

# •Electrical characteristic curves

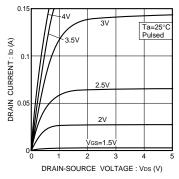


Fig.1 Typical output characteristics

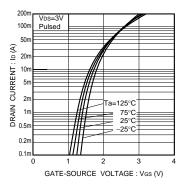


Fig.2 Typical transfer characteristics

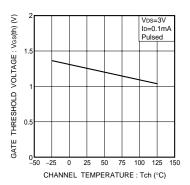


Fig.3 Gate threshold voltage vs. channel temperature

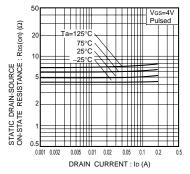


Fig.4 Static drain-source on-state resistance vs. drain current ( I )

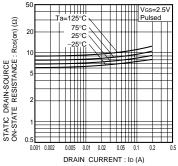


Fig.5 Static drain-source on-state resistance vs. drain current (II)

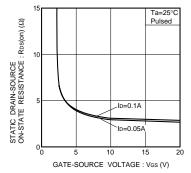


Fig.6 Static drain-source on-state resistance vs. gate-source voltage

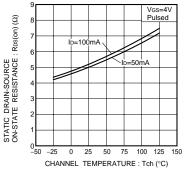


Fig.7 Static drain-source on-state resistance vs. channel temperature

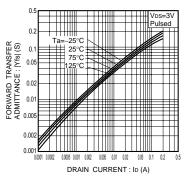


Fig.8 Forward transfer admittance vs. drain current

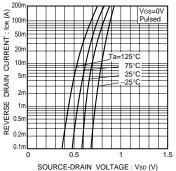


Fig.9 Reverse drain current vs. source-drain voltage ( I )

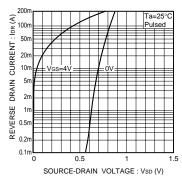


Fig.10 Reverse drain current vs. source-drain voltage (II)

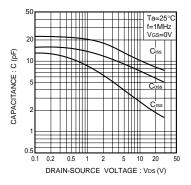


Fig.11 Typical capacitance vs. drain-source voltage

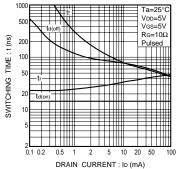


Fig.12 Switching characteristics (See Figures 13 and 14 for the measurement circuit and resultant waveforms)

# Switching characteristics measurement circuit

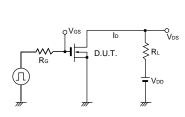


Fig.13 Switching time measurement circuit

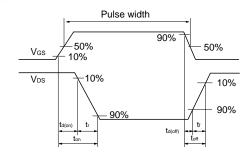


Fig.14 Switching time waveforms

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Appendix1-Rev1.1