

March 2008

# FDA8440 N-Channel PowerTrench<sup>®</sup> MOSFET 40V, 100A, 2.1m $\Omega$

### **Features**

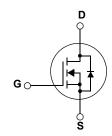
- $R_{DS(on)}$  = 1.46m $\Omega$  (Typ.)@  $V_{GS}$  = 10V,  $I_D$  = 80A
- $Q_{g(tot)} = 345nC (Typ.)@V_{GS} = 10V$
- · Low Miller Charge
- · Low QRR Body Diode
- UIS Capability (Single Pulse and Repetitive Pulse)
- 160A Guarantee for 2 sec
- RoHS Compliant



# **Application**

- · Automotive Engine Control
- · Powertrain Management
- · Motors, Solenoids
- · Electronic Steering
- · Integrated Starter/ Alternator
- · Distributed Power Architectures and VRMs
- · Primary Switch for 12V systems





## MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
V <sub>DSS</sub>	Drain to Source Voltage	40	V	
V <sub>GSS</sub>	Gate to Source Voltage	±20	V	
	Drain Current - Continuous (T <sub>C</sub> = 155°C)	100	А	
l <sub>D</sub>	- Continuous ( $T_A$ = 25°C, $V_{GS}$ = 10V, $R_{\theta JA}$ = 40°C/W )	30	Α	
	- Pulsed	500	Α	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 1)	1682	mJ	
D	Power dissipation	306	W	
$P_{D}$	Derate above 25°C	2.04	W/°C	
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Temperature	-55 to +175	°C	

## **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.49	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2)	40	°C/W

# **Package Marking and Ordering Information**

Device Marking Device		Package Reel Size		Tape Width	Quantity	
FDA8440	FDA8440	TO-3PN	N/A	N/A	30units	

# **Electrical Characteristics** $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Conditio	Min	Тур	Max	Units	
Off Charac	teristics	-					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250 \mu A$		40			V
I <sub>DSS</sub>	7 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	V <sub>DS</sub> = 32V				1	μΑ
	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0V	$T_{\rm C} = 150^{\rm o}{\rm C}$			250	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>GS</sub> = ±20V				±100	nA
On Charac	teristics			ı			
V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		1		3	V
` '		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 80A			1.56	2.2	. mΩ
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 80A			1.46	2.1	
		$V_{GS} = 10V, I_D = 80A,$ $T_C = 175^{\circ}C$			2.82	4.1	
Dynamic C	haracteristics			ı			
C <sub>iss</sub>	Input Capacitance				18600	24740	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz			1840	2450	pF
C <sub>rss</sub>	Reverse Transfer Capacitance				1400	2100	pF
R <sub>G</sub>	Gate Resistance	V <sub>GS</sub> = 0.5V, f = 1MHz			1.1		Ω
Q <sub>g(tot)</sub>	Total Gate Charge at 10V	V <sub>GS</sub> = 0V to 10V			345	450	nC
Q <sub>g(2)</sub>	Threshold Gate Charge	V <sub>GS</sub> = 0V to 2V	V <sub>DD</sub> = 20V		32.5		nC
Q <sub>gs</sub>	Gate to Source Gate Charge		I <sub>D</sub> = 80A		49		nC
Q <sub>gs2</sub>	Gate Charge Threshold to Plateau		$I_g = 1.0 \text{mA}$		16.5		nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge	7			74		nC
Switching	Characteristics (V <sub>GS</sub> = 10V)	•			•		
t <sub>ON</sub>	Turn-On Time				175	360	ns
t <sub>d(on)</sub>	Turn-On Delay Time	001/1 004			43	95	ns
t <sub>r</sub>	Rise Time	$V_{DD} = 20V, I_D = 80A$ $V_{CS} = 10V. R_{CEN} = 7\Omega$	$V_{DD} = 20V, I_D = 80A$ $V_{GS} = 10V, R_{GEN} = 7\Omega$		130	275	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	GS - / GEN			435	875	ns
t <sub>f</sub>	Fall Time				290	590	ns
t <sub>OFF</sub>	Turn-Off Time				730	1470	ns
Drain-Sour	rce Diode Characteristics and Maximu	ım Ratings		1	1		
V	Source to Drain Diode Voltage	I <sub>SD</sub> = 80A				1.25	V
$V_{SD}$		I <sub>SD</sub> = 40A				1.0	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> = 75A, dI <sub>SD</sub> /dt = 10	0A/μs		59		ns
Q <sub>RR</sub>	Reverse Recovery Charge	I <sub>SD</sub> = 75A, dI <sub>SD</sub> /dt = 10	0A/μs		77		nC

#### NOTES:

<sup>1:</sup> Starting T  $_{J}$  = 25°C, L = 1mH, I  $_{AS}$  = 58A, V  $_{DD}$  = 36V, V  $_{GS}$  = 10V.

<sup>2:</sup> Pulse width = 100s

# **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

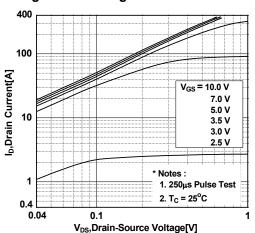
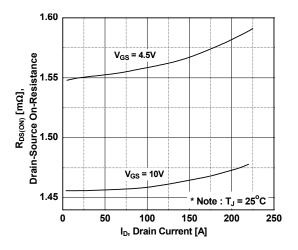
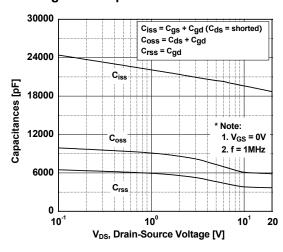


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage



**Figure 5. Capacitance Characteristics** 



**Figure 2. Transfer Characteristics** 

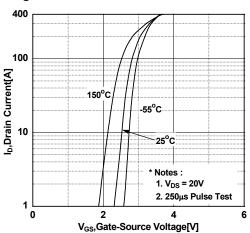


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

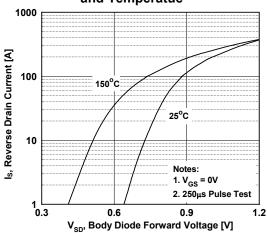
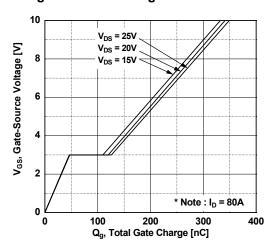


Figure 6. Gate Charge Characteristics



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# **Typical Performance Characteristics** (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

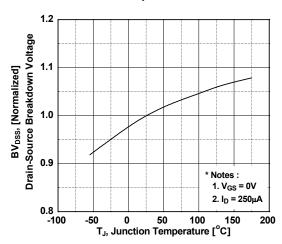


Figure 9. Unclamped Inductive Switching Capability

Figure 8. On-Resistance Variation vs. Temperature

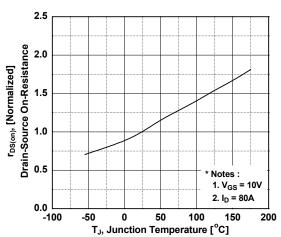
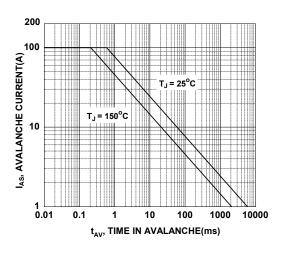


Figure 10. Safe Operating Area



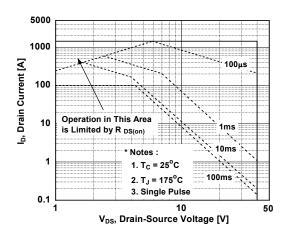
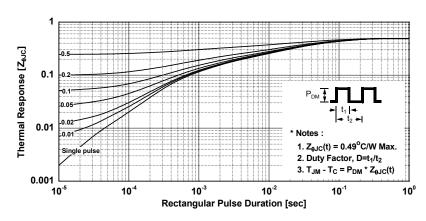
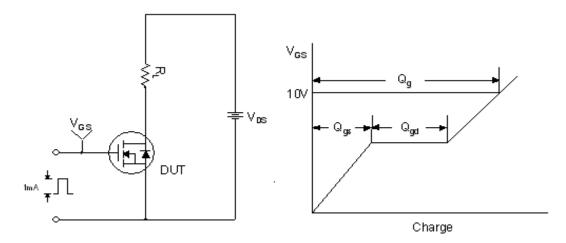


Figure 11. Transient Thermal Response Curve

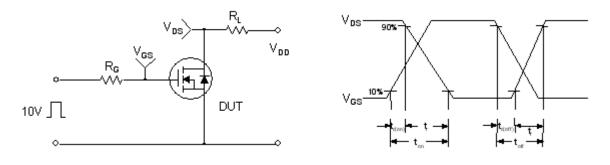


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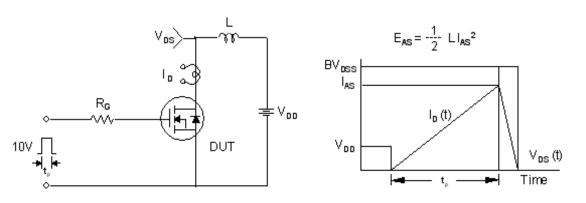
## **Gate Charge Test Circuit & Waveform**



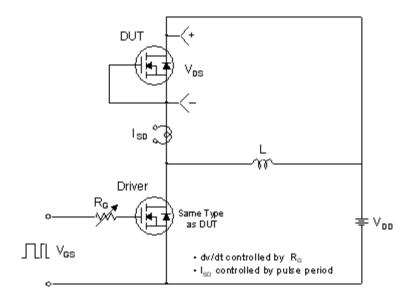
## **Resistive Switching Test Circuit & Waveforms**

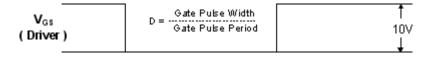


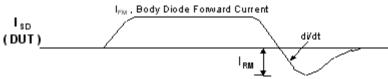
## **Unclamped Inductive Switching Test Circuit & Waveforms**



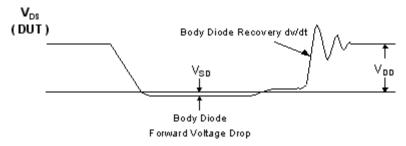
#### Peak Diode Recovery dv/dt Test Circuit & Waveforms

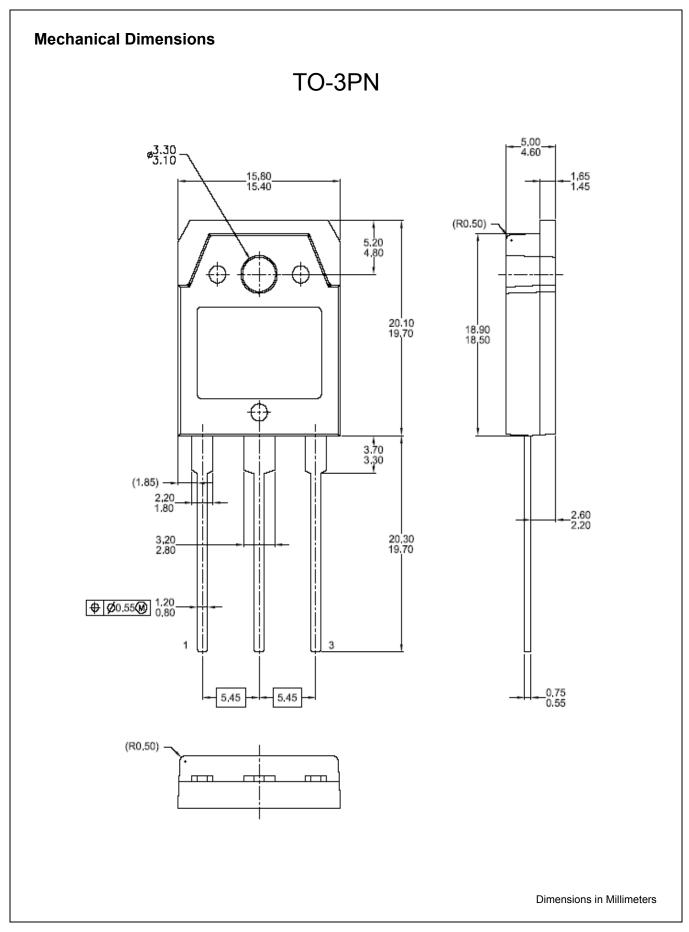






Body Diode Reverse Current









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