

100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V(BR)DSS	R _{DS(on)}	I _D T _A = 25°C		
100V	85mΩ @ Vgs = 10V	7.7A		
	100mΩ @ Vgs = 6V	7.1A		

Description and Applications

This MOSFET features low on-resistance, fast switching and a high avalanche withstand capability, making it ideal for high efficiency power management applications.

- **DC-DC Converters**
- Power management functions
- **Disconnect switches**
- Motor control
- Uninterrupted power supply

Features and Benefits

- Low input capacitance
- Low on-resistance
- Fast switching speed
- "Green" Component and RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

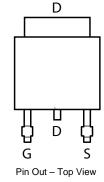
Mechanical Data

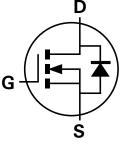
- Case: TO252-3L •
- Case Material: Molded Plastic "Green" Molding Compound, UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram •
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.33 grams (approximate)



TO252-3L

Top View





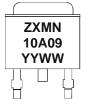
Equivalent Circuit

Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMN10A09KTC	ZXMN10A09	13	16	2,500

1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information Notes: about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

Marking Information



ZXMN = Product Type Marking Code, Line 1 10A09 = Product Type Marking Code, Line 2 YYWW = Date Code Marking YY = Year (ex: 09 = 2009) WW = Week (01-52)



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit	
Drain-Source voltage			V _{DSS}	100	V	
Gate-Source voltage			V _{GS}	±20	V	
Continuous Drain current	V _{GS} = 10V	(Note 3) T _A = 70°C (Note 3) (Note 2)	ID	7.7 6.2 5.0	А	
Pulsed Drain current V _{GS} = 10V (No		(Note 4)	IDM	27	А	
Continuous Source current (Body diode) (Note 3)		(Note 3)	Is	11	А	
Pulsed Source current (Body diode) (Note 4)		I _{SM}	27	А		

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
	(Note 2)		4.31 34.4	W mW/°C	
Power dissipation Linear derating factor	(Note 3)	PD	10.1 80.8		
·	(Note 6)		2.15 17.2		
	(Note 2)		29	°C/W	
Thermal Resistance, Junction to Ambient	(Note 3)	R _θ JA	12.3		
	(Note 6)		58		
Thermal Resistance, Junction to Lead	(Note 5)	R _{θJL}	1.14	°C/W	
Dperating and storage temperature range	•	TJ, TSTG	-55 to 150	°C	

Notes: 2. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

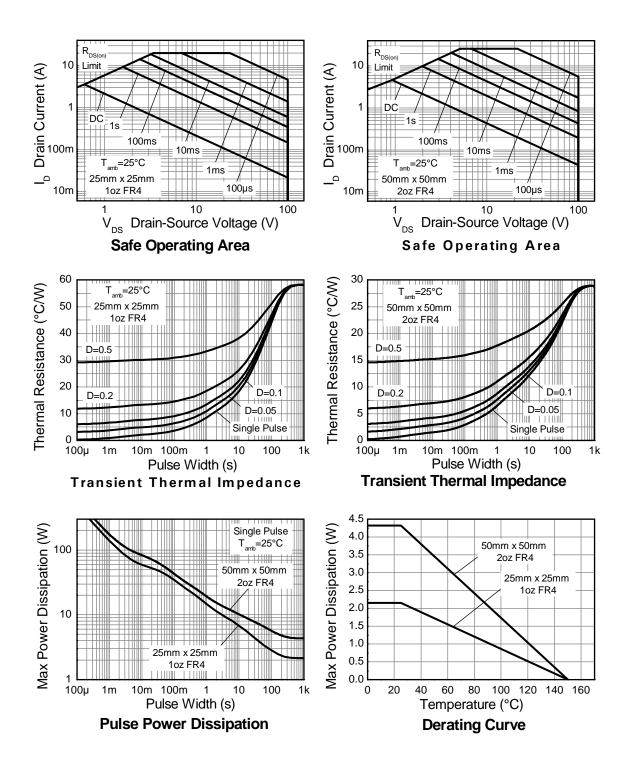
3. Same as note 2, except the device is measured at t \leq 10 sec.

Same as note 2, except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.
Thermal resistance from junction to solder-point (at the end of the drain lead).

6. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with the high coverage single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.



Thermal Characteristics





Electrical Characteristics @T_A = 25°C unless otherwise specified Characteristic Symbol Min Max Unit **Test Condition** Тур **OFF CHARACTERISTICS** Drain-Source Breakdown Voltage V **BV**_{DSS} 100 $I_D = 250 \mu A, V_{GS} = 0 V$ Zero Gate Voltage Drain Current $V_{DS} = 100V, V_{GS} = 0V$ 1 μΑ IDSS nA Gate-Source Leakage I_{GSS} ±100 $V_{GS} = \pm 20V, V_{DS} = 0V$ **ON CHARACTERISTICS** Gate Threshold Voltage 2 4 V $I_D = 250 \mu A$, $V_{DS} = V_{GS}$ VGS(th) ____ 0.085 $V_{GS} = 10V, I_D = 4.6A$ Static Drain-Source On-Resistance (Note 7) Ω RDS (ON) ____ 0.100 $V_{GS} = 6V, I_D = 4.2A$ Forward Transconductance (Notes 7 & 8) 10.7 S $V_{DS} = 15V, I_D = 4.6A$ g_{fs} V Diode Forward Voltage (Note 7) 0.850 0.950 $I_{S} = 4.7A, V_{GS} = 0V$ VSD ____ Reverse recovery time (Note 8) 40 t_{rr} ns $I_{S} = 3.0A$, di/dt = 100A/µs nC Reverse recovery charge (Note 8) Qrr ____ 62 DYNAMIC CHARACTERISTICS (Note 8) Input Capacitance Ciss 1313 pF ____ $V_{DS} = 50V, V_{GS} = 0V$ Output Capacitance C_{oss} ____ 83 ____ pF f = 1MHzpF Reverse Transfer Capacitance 56 Crss ____ ____ 17.2 Total Gate Charge (Note 9) Qg nC $V_{GS} = 6V$ Total Gate Charge (Note 9) 26.0 nC Qg $V_{DS} = 50V$, ____ ____ Gate-Source Charge (Note 9) Qgs 5.6 nC $V_{GS} = 10V$ $I_{D} = 4.6A$ Gate-Drain Charge (Note 9) Q_{gd} 7.6 nC ____ Turn-On Delay Time (Note 9) 6.8 ns t_{D(on)} Turn-On Rise Time (Note 9) tr 5.3 ns $V_{DD} = 50V, V_{GS} = 10V$ Turn-Off Delay Time (Note 9) 27.5 ns $I_D = 1.0A, R_G \cong 25\Omega$ t_{D(off)} Turn-Off Fall Time (Note 9) 12.3 ns tf

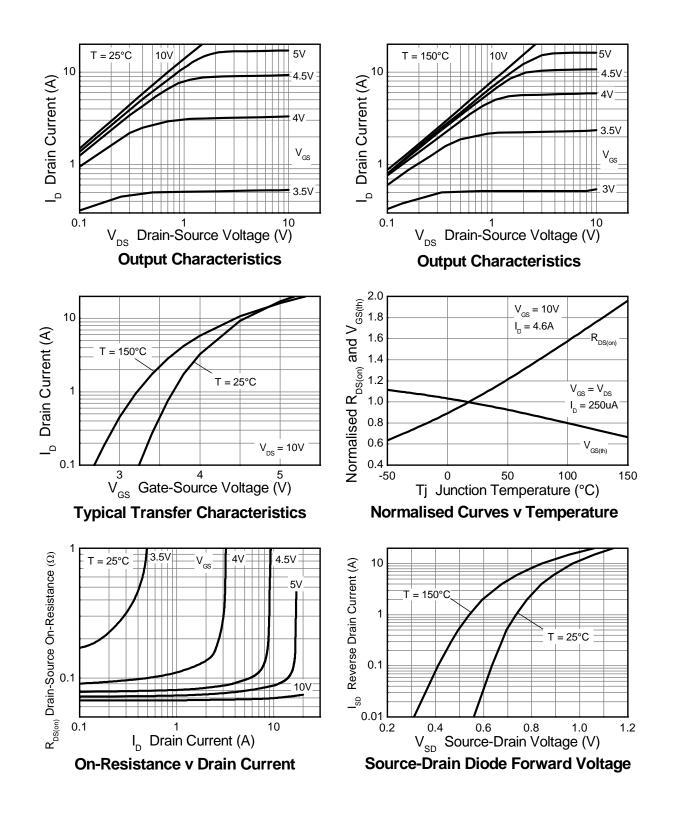
Notes: 7. Measured under pulsed conditions. Pulse width \leq 300µs; duty cycle \leq 2%

8. For design aid only, not subject to production testing.

9. Switching characteristics are independent of operating junction temperatures.



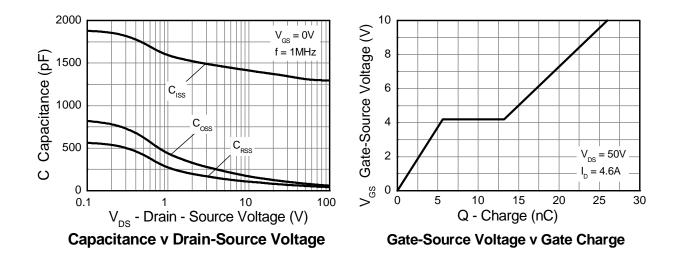
Typical Characteristics



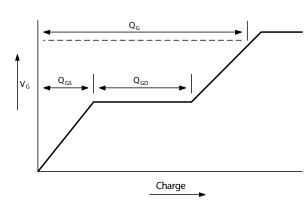
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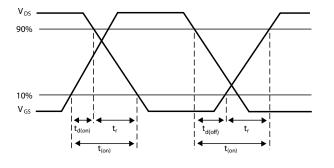
Typical Characteristics - continued



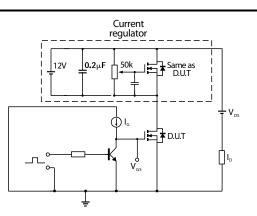
Test Circuits



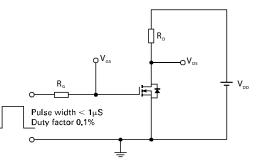
Basic gate charge waveform



Switching time waveforms



Gate charge test circuit

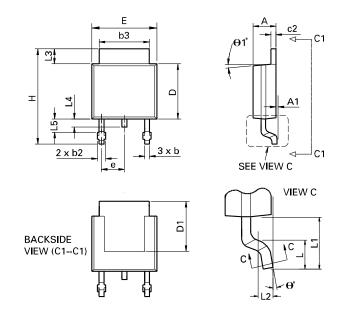


Switching time test circuit

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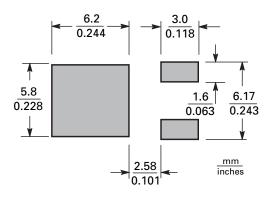


Package Outline Dimensions



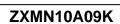
DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	0.086	0.094	2.18	2.39	е	0.090 BSC		2.29 BSC	
A1	-	0.005	-	0.127	н	0.370	0.410	9.40	10.41
b	0.020	0.035	0.508	0.89	L	0.055	0.070	1.40	1.78
b2	0.030	0.045	0.762	1.14	L1	0.108 REF		2.74 REF	
b3	0.205	0.215	5.21	5.46	L2	0.020 BSC		0.508 BSC	
с	0.018	0.024	0.457	0.61	L3	0.035	0.065	0.89	1.65
c2	0.018	0.023	0.457	0.584	L4	0.025	0.040	0.635	1.016
D	0.213	0.245	5.41	6.22	L5	0.045	0.060	1.14	1.52
D1	0.205	-	5.21	-	θ1°	0°	10°	0°	10°
Е	0.250	0.265	6.35	6.73	θ°	0°	15°	0°	15°
E1	0.170	-	4.32	-	-	-	-	-	-

Suggested Pad Layout



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