

RoHS

COMPLIANT

HALOGEN

Available

Vishay Siliconix

# N-Channel Reduced Q<sub>g</sub>, Fast Switching MOSFET

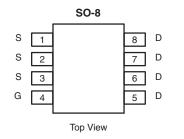
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)		
30	0.007 at V <sub>GS</sub> = 10 V	16	11		
	0.0095 at $V_{GS}$ = 4.5 V	13.5			

#### FEATURES

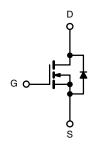
- Halogen-free According to IEC 61249-2-21
   Available
- TrenchFET<sup>®</sup> Gen II Power MOSFETs
- PWM Optimized
- 100 % Rg Tested

#### **APPLICATIONS**

DC/DC Conversion for PC



Ordering Information: Si4386DY-T1-E3 (Lead (Pb)-free) Si4386DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b>	<b>S</b> T <sub>A</sub> = 25 °C, unles	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		v
Continuous Drain Current /T 150 °C)	T <sub>A</sub> = 25 °C	1-	16	11	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	I <sub>D</sub>	13	9	
Pulsed Drain Current		I <sub>DM</sub>	± 50		А
Continuous Source Current (Diode Conduction) <sup>4</sup>	a	ا <sub>S</sub>	2.8	1.3	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	20		
Avalanche Energy		E <sub>AS</sub>	20		mJ
Mauinum Dauran Disairatiana	T <sub>A</sub> = 25 °C	PD	3.1	1.47	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	١D	2	0.95	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55	5 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R <sub>thJA</sub>	34	40	
Maximum Junction-to-Ambient (MOSFET) <sup>a</sup>	Steady State		71	85	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	18	22	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.5	2.0	2.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ	
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	40			Α	
Drain-Source On-State Resistance <sup>a</sup>	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 16 A		0.0058	0.007	Ω	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 13.5 A		0.0078	0.0095		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 16 A		51		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 2.8 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.1	V	
Dynamic <sup>b</sup>			1				
Total Gate Charge	Qg			11	18		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 16 \text{ A}$		5.8		nC	
Gate-Drain Charge	Q <sub>gd</sub>			3.0			
Gate Resistance	R <sub>g</sub>		0.8	1.7	2.5	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			12	18		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		9	14		
Turn-Off Delay Time	t <sub>d(off)</sub>	${ m I}_{ m D}\cong$ 1 A, ${ m V}_{ m GEN}$ = 10 V, ${ m R}_{ m g}$ = 6 $\Omega$		35	53	ns	
Fall Time	t <sub>f</sub>	-		10	15	1	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.8 A, dl/dt = 100 A/μs		25	50		

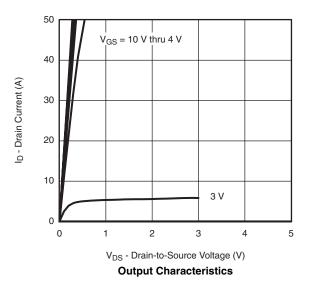
Notes:

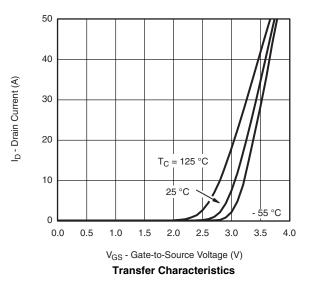
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

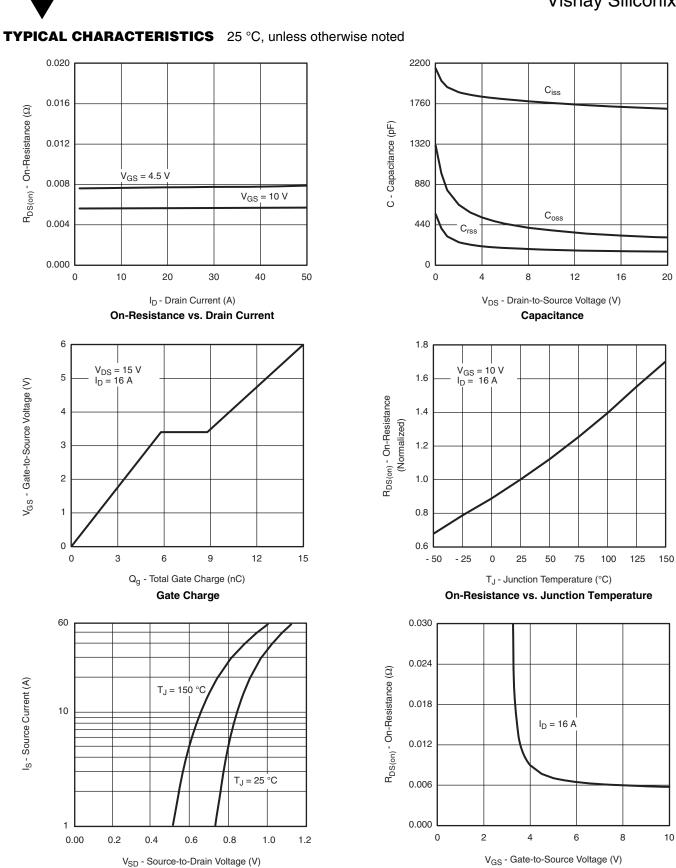
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







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Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage

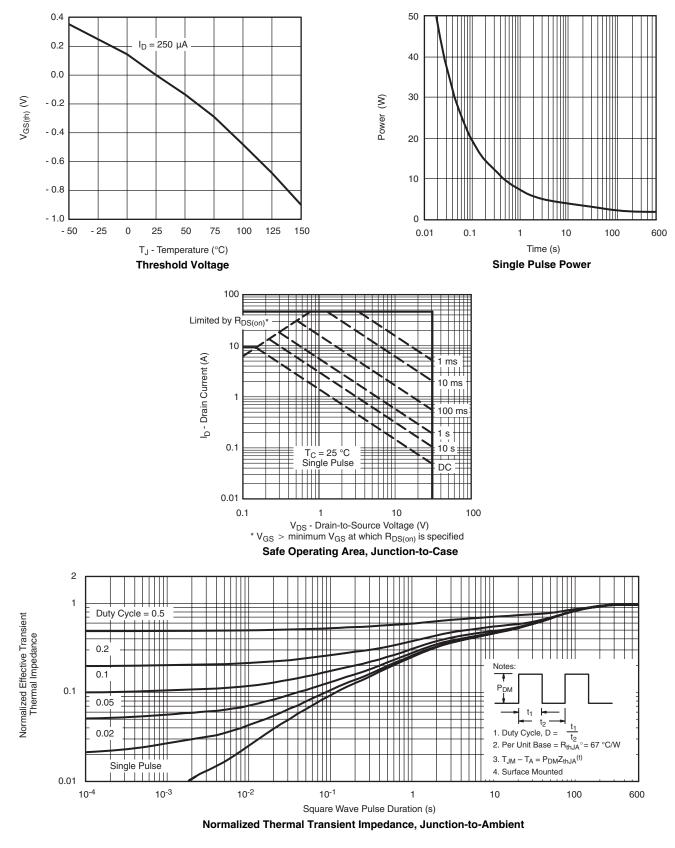
Si4386DY

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## Si4386DY

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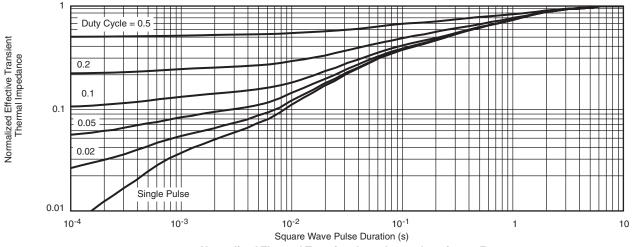
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## Si4386DY

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?73109</u>.

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