

## P-Channel 12 V (D-S) MOSFET

### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
- 12	0.130 at $V_{GS} = - 4.5$ V	1.18	6.7
	0.158 at $V_{GS} = - 2.5$ V	1.07	
	0.205 at $V_{GS} = - 1.8$ V	0.49	

### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 %  $R_g$  Tested
- Compliant to RoHS Directive 2002/95/EC

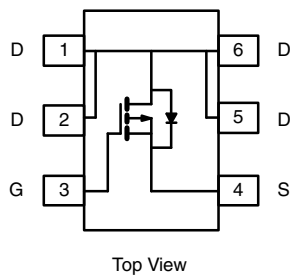


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

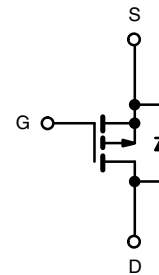
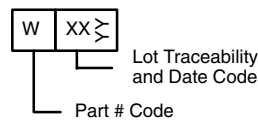
### APPLICATIONS

- Load Switch for Portable Devices

#### SC-89 (6-LEADS)



#### Marking Code



Ordering Information: Si1065X-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	- 12	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current ( $T_J = 150$ °C)	$I_D$	$T_A = 25$ °C	- 1.18 <sup>b, c</sup>
		$T_A = 70$ °C	- 0.94 <sup>b, c</sup>
Pulsed Drain Current	$I_{DM}$	- 8	A
Continuous Source-Drain Diode Current	$I_S$	- 0.2 <sup>b, c</sup>	
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25$ °C	0.236 <sup>b, c</sup>
		$T_A = 70$ °C	0.151 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, b</sup>	$R_{thJA}$	$t \leq 5$ s	440	530
		Steady State	540	650

#### Notes:

- Maximum under steady state conditions is 650 °C/W.
- Surface mounted on 1" x 1" FR4 board.
- $t = 5$  s.

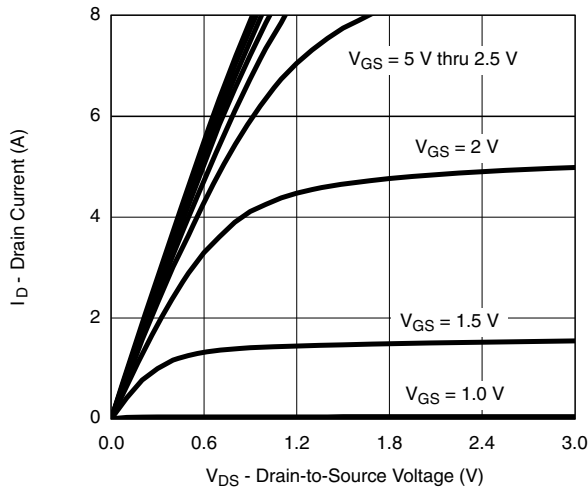
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	- 12			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		- 8.47		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		2.33			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 0.45		- 0.95	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -12\text{ V}, V_{GS} = 0\text{ V}$			- 1	nA
		$V_{DS} = -12\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$			- 10	$\mu\text{A}$
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = -4.5\text{ V}$	- 8			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}, I_D = -1.18\text{ A}$		0.108	0.130	$\Omega$
		$V_{GS} = -2.5\text{ V}, I_D = -1.07\text{ A}$		0.131	0.158	
		$V_{GS} = -1.8\text{ V}, I_D = -0.49\text{ A}$		0.158	0.204	
Forward Transconductance	$g_{fs}$	$V_{DS} = -6\text{ V}, I_D = -1.18\text{ A}$		5.18		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = -6\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		480		pF
Output Capacitance	$C_{oss}$		190			
Reverse Transfer Capacitance	$C_{rss}$		145			
Total Gate Charge	$Q_g$	$V_{DS} = -6\text{ V}, V_{GS} = -5\text{ V}, I_D = -1.18\text{ A}$		7.2	10.8	nC
		$V_{DS} = -6\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -1.18$		6.7	10.1	
Gate-Source Charge	$Q_{gs}$			0.84		
Gate-Drain Charge	$Q_{gd}$		2.7			
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		10	15	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -6\text{ V}, R_L = 6.32\text{ }\Omega$ $I_D \cong -0.95\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\text{ }\Omega$		13	19.5	ns
Rise Time	$t_r$		27	40.5		
Turn-Off Delay Time	$t_{d(off)}$		45	67.5		
Fall Time	$t_f$		27	40.5		
<b>Drain-Source Body Diode Characteristics</b>						
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				8	A
Body Diode Voltage	$V_{SD}$	$I_S = -0.63\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = -0.7\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		29.2	44	nC
Body Diode Reverse Recovery Charge	$Q_{rr}$		10.22	15.3	ns	
Reverse Recovery Fall Time	$t_a$		13.7			
Reverse Recovery Rise Time	$t_b$		15.5			

## Notes:

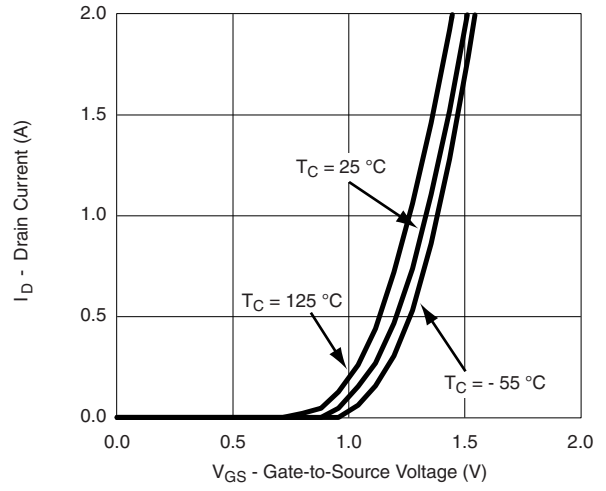
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

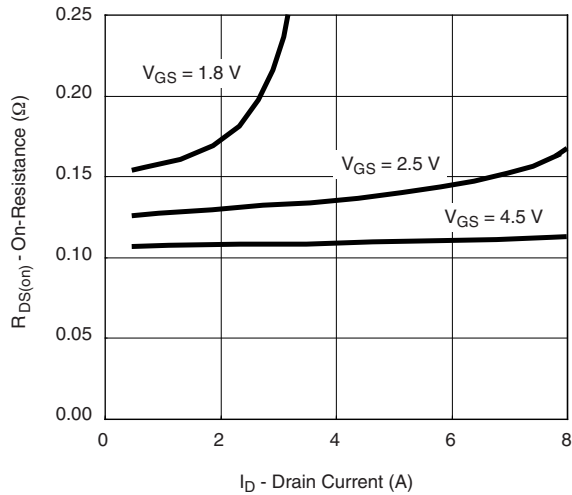
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



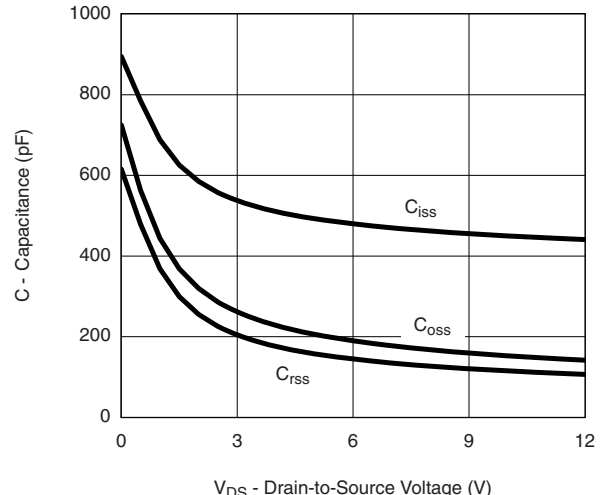
**Output Characteristics**



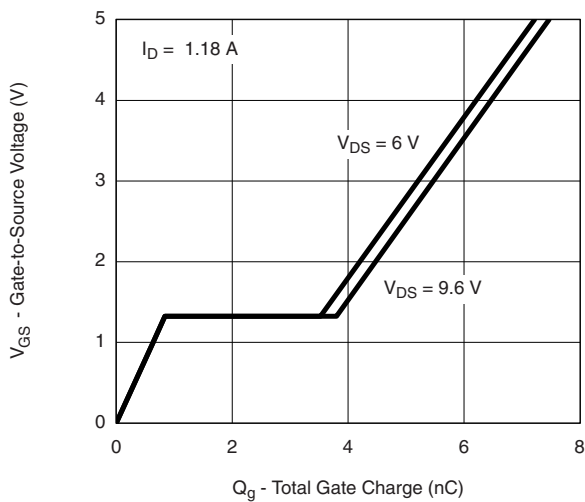
**Transfer Characteristics Curves vs. Temp.**



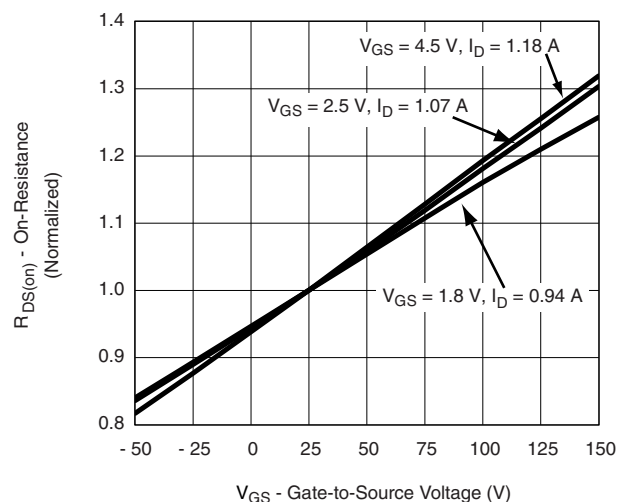
**On-Resistance vs. Drain Current**



**Capacitance**

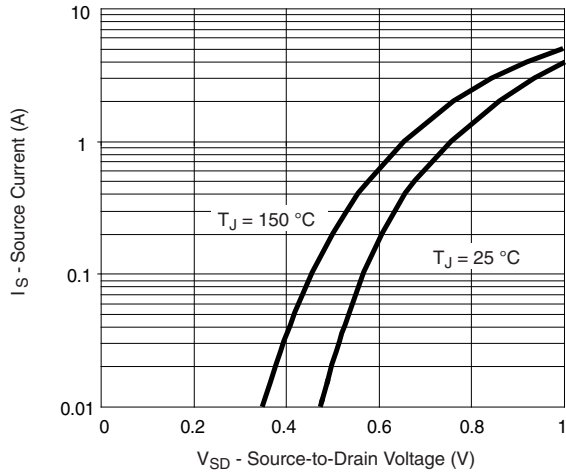


**Gate Charge**

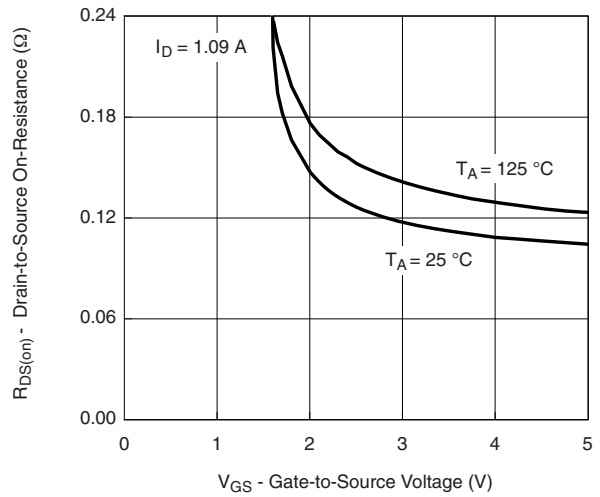


**On-Resistance vs. Junction Temperature**

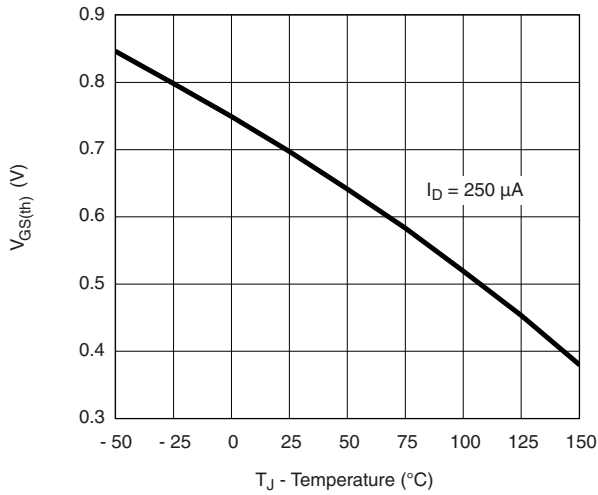
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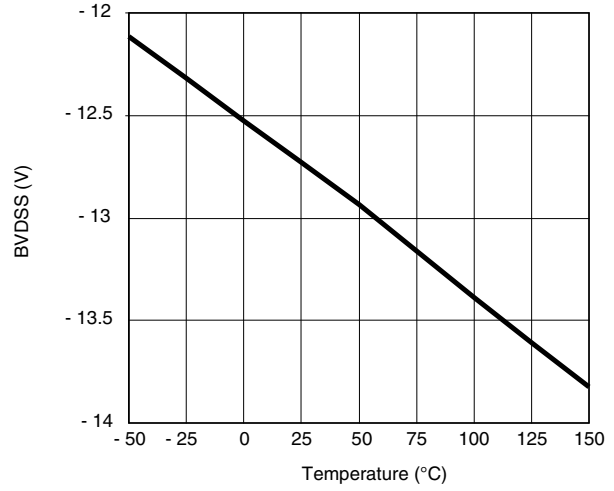
Source-Drain Diode Forward Voltage



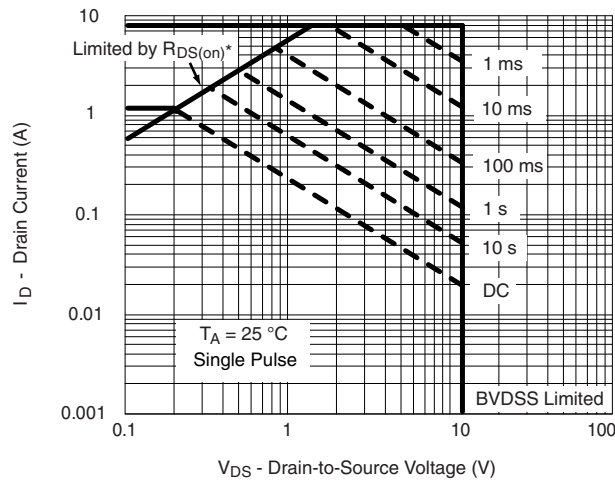
$R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



Threshold Voltage



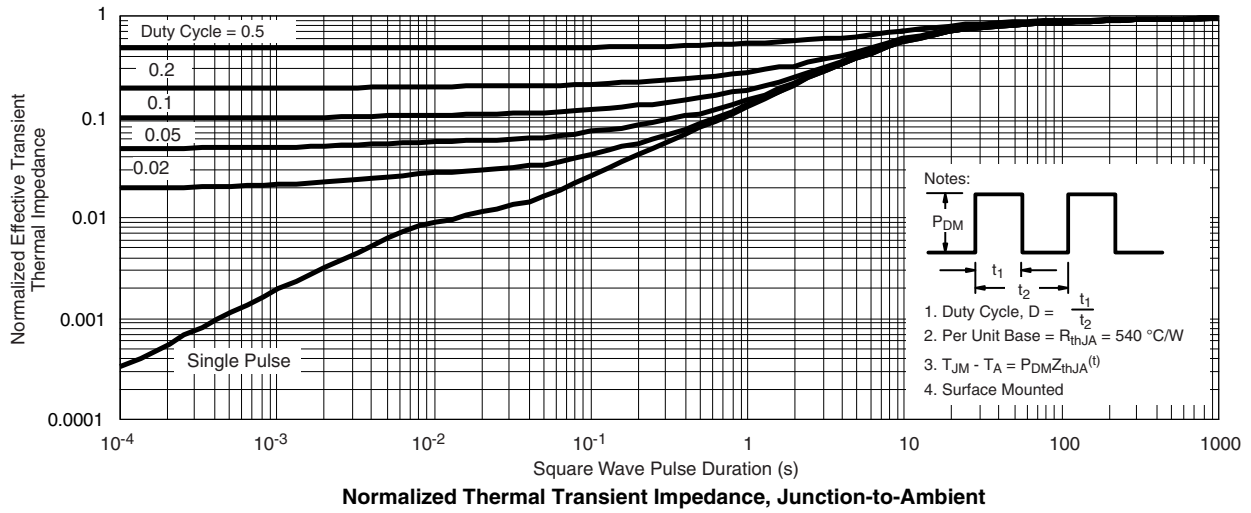
BVDSS vs. Temperature



\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



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