





### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Features**

- Low On-Resistance
  - $42.5m\Omega$  @  $V_{GS} = -4.5V$
  - $53m\Omega$  @  $V_{GS} = -2.5V$
  - $71m\Omega$  @  $V_{GS} = -1.8V$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- ESD Protected Up To 3kV
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

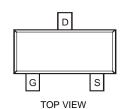
- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.008 grams (approximate)





TOP VIEW





Internal Schematic

## Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characte	eristic		Symbol	Value	Units
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage		V <sub>GSS</sub>	±8	V	
Continuous Drain Current (Note 3)	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-4.0 -3.5	А
Pulsed Drain Current (Note 4)			I <sub>DM</sub>	-30	Α

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P <sub>D</sub>	0.9	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = 25°C	R <sub>0JA</sub>	139	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C

Notes:

- 1. No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
  Device mounted on FR-4 PCB with 2oz. Copper and test pulse width t ≤ 10s.
- 4. Repetitive rating, pulse width limited by junction temperature.

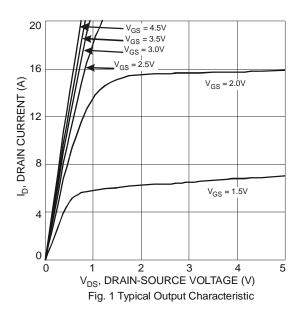


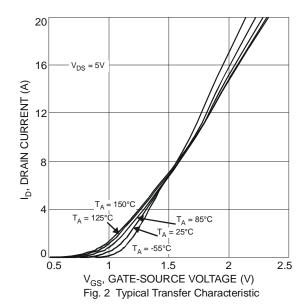
# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	-20	_		>	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	$T_J = 25^{\circ}C$	I <sub>DSS</sub>	_	_	-1.0	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage		$I_{GSS}$	_	_	±10	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage		V <sub>GS(th)</sub>	-0.3	-0.55	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
				31	42.5	mΩ	$V_{GS} = -4.5V$ , $I_{D} = -4.0A$
Static Drain-Source On-Resistance		R <sub>DS</sub> (ON)	_	40	53		$V_{GS} = -2.5V$ , $I_{D} = -3.5A$
				51	71		$V_{GS} = -1.8V$ , $I_D = -2.0A$
Forward Transfer Admittance		Y <sub>fs</sub>	_	3	_	S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS				<u>.</u>	<u>.</u>		
Input Capacitance		C <sub>iss</sub>	_	294	_	рF	101/11/ 01/
Output Capacitance		Coss	_	104	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ -f = 1.0MHz
Reverse Transfer Capacitance	ransfer Capacitance			25	_	pF	1 = 1.0WH IZ
Gate Resistnace		$R_g$	_	250	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
SWITCHING CHARACTERISTICS							
Total Gate Charge		$Q_{g}$	_	9.1	_	nC	
Gate-Source Charge		$Q_{gs}$	_	1.5	_	nC	$V_{GS} = -4.5V$ , $V_{DS} = -10V$ , $I_{D} = -4A$
Gate-Drain Charge		$Q_{gd}$	_	1.7	_	nC	
Turn-On Delay Time		t <sub>D(on)</sub>	_	71	_	ns	
Turn-On Rise Time		t <sub>r</sub>	_	117	_	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t <sub>D(off)</sub>	_	795	_	ns	$R_D = 2.5\Omega, R_G = 3.0\Omega, I_D = -1A$	
Turn-Off Fall Time	t <sub>f</sub>		393	_	ns		

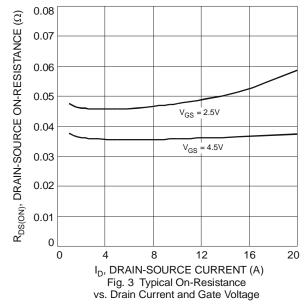
Notes:

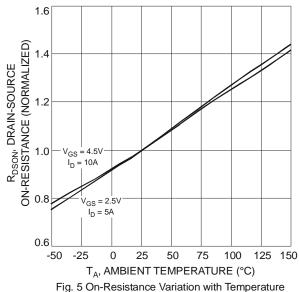
5. Short duration pulse test used to minimize self-heating effect.











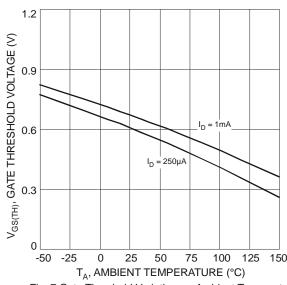


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

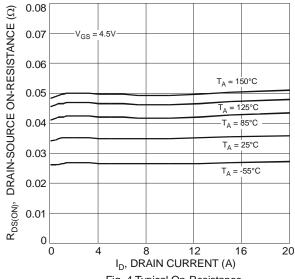


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

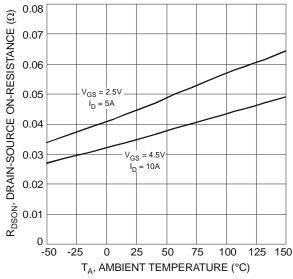


Fig. 6 On-Resistance Variation with Temperature

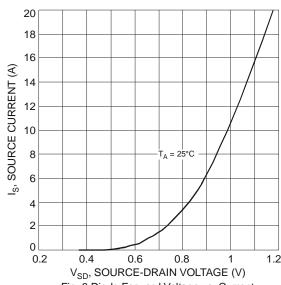


Fig. 8 Diode Forward Voltage vs. Current



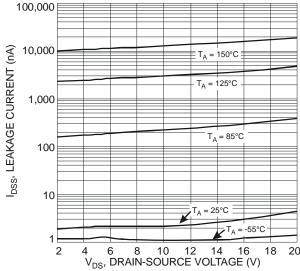
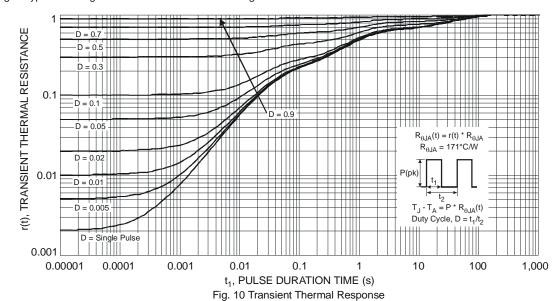


Fig. 9 Typical Leakage Current vs. Drain-Source Voltage

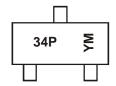


## Ordering Information (Note 6)

Part Number	Case	Packaging
DMG3415U-7	SOT-23	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# Marking Information



34P = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: W = 2009)

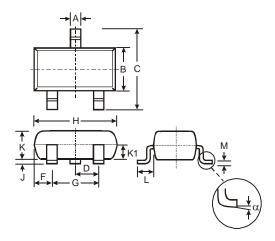
M = Month (ex: 9 = September)

Date Code Key

Year	2009	9	2010		2011	20	12	2013		2014		2015
Code	W		X		Υ		Z	Α		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

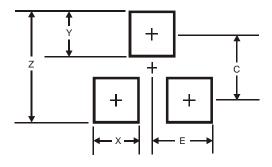


## **Package Outline Dimensions**



	SOT-23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.903	1.10	1.00					
K1	-	1	0.400					
L	0.45	0.61	0.55					
M	0.085	0.18	0.11					
α	0°	8°	-					
All	All Dimensions in mm							

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
Е	1.35



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