# Power MOSFET -3.05 Amps, -30 Volts

# **Dual P-Channel SOIC-8**

#### **Features**

- High Efficiency Components in a Dual SOIC-8 Package
- High Density Power MOSFET with Low R<sub>DS(on)</sub>
- Miniature SOIC-8 Surface Mount Package Saves Board Space
- Diode Exhibits High Speed with Soft Recovery
- I<sub>DSS</sub> Specified at Elevated Temperature
- Avalanche Energy Specified
- Mounting Information for the SOIC-8 Package is Provided
- Pb-Free Package is Available

#### **Applications**

- DC-DC Converters
- Low Voltage Motor Control
- Power Management in Portable and Battery-Powered Products, i.e.: Computers, Printers, PCMCIA Cards, Cellular & Cordless Telephones

# **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>	-30	V
Gate-to-Source Voltage - Continuous	$V_{GS}$	±20	V
Thermal Resistance – Junction-to-Ambient (Note 1) Total Power Dissipation @ T <sub>A</sub> = 25°C Continuous Drain Current @ 25°C Continuous Drain Current @ 70°C Pulsed Drain Current (Note 4)	R <sub>θJA</sub> P <sub>D</sub> I <sub>D</sub> I <sub>D</sub>	171 0.73 -2.34 -1.87 -8.0	°C/W W A A
Thermal Resistance – Junction-to-Ambient (Note 2) Total Power Dissipation @ T <sub>A</sub> = 25°C Continuous Drain Current @ 25°C Continuous Drain Current @ 70°C Pulsed Drain Current (Note 4)	R <sub>θJA</sub> P <sub>D</sub> I <sub>D</sub> I <sub>DM</sub>	100 1.25 -3.05 -2.44 -12	°C/W W A A
Thermal Resistance – Junction-to-Ambient (Note 3) Total Power Dissipation @ T <sub>A</sub> = 25°C Continuous Drain Current @ 25°C Continuous Drain Current @ 70°C Pulsed Drain Current (Note 4)	R <sub>0JA</sub> P <sub>D</sub> I <sub>D</sub> I <sub>D</sub>	62.5 2.0 -3.86 -3.1 -15	°C/W W A A
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Single Pulse Drain–to–Source Avalanche Energy – Starting $T_J$ = 25°C ( $V_{DD}$ = -30 Vdc, $V_{GS}$ = -4.5 Vdc, Peak $I_L$ = -7.5 Apk, $L$ = 5 mH, $R_G$ = 25 $\Omega$ )	E <sub>AS</sub>	140	mJ
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- 1. Minimum FR-4 or G-10 PCB, t = Steady State.
- 2. Mounted onto a 2" square FR-4 Board (1 in sq, 2 oz Cu 0.06" thick single sided), t = steady state.
- Mounted onto a 2" square FR-4 Board (1 in sq, 2 oz Cu 0.06" thick single sided), t ≤ 10 seconds.
- 4. Pulse Test: Pulse Width = 300 μs, Duty Cycle = 2%.

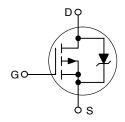


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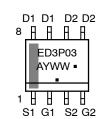
V <sub>DSS</sub>	R <sub>DS(ON)</sub> Typ	I <sub>D</sub> Max
-30 V	85 mΩ @ –10 V	-3.05 A

#### P-Channel





# MARKING DIAGRAM\* AND PIN ASSIGNMENT



ED3P03= Specific Device Code

A = Assembly Location

Y = Year WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

\*For additional marking information, refer to Application Note AND8002/D.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTMD3P03R2	SOIC-8	2500/Tape & Reel
NTMD3P03R2G	SOIC-8 (Pb-Free)	2500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted) (Note 5)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = -250 μAdc)		V <sub>(BR)DSS</sub>	-30 -	- -30	-	Vdc mV/°C
Temperature Coefficient (Positive)  Zero Gate Voltage Drain Current  (V <sub>DS</sub> = -24 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> = 25°C)  (V <sub>DS</sub> = -24 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> = 125°C)  (V <sub>DS</sub> = -30 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> = 25°C)		I <sub>DSS</sub>	- - -	- - - -	-1.0 -20 -2.0	μAdc
Gate-Body Leakage Current (V <sub>GS</sub> = -20 Vdc, V <sub>DS</sub> = 0 Vdc)			-	-	-100	nAdc
Gate-Body Leakage Current (V <sub>GS</sub> = +20 Vdc, V <sub>DS</sub> = 0 Vdc)			-	-	100	nAdc
ON CHARACTERISTICS		•		•	•	•
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = -250 \ \mu Adc)$ Temperature Coefficient (Negative)		V <sub>GS(th)</sub>	-1.0 -	-1.7 3.6	-2.5 -	Vdc
Static Drain-to-Source On-State Resistance ( $V_{GS} = -10$ Vdc, $I_D = -3.05$ Adc) ( $V_{GS} = -4.5$ Vdc, $I_D = -1.5$ Adc)		R <sub>DS(on)</sub>	-	0.063 0.090	0.085 0.125	Ω
Forward Transconductance (V <sub>DS</sub> =	-15 Vdc, I <sub>D</sub> = -3.05 Adc)	9FS	-	5.0	-	Mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C <sub>iss</sub>	-	520	750	pF
Output Capacitance	$(V_{DS} = -24 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, $ f = 1.0 MHz)	C <sub>oss</sub>	_	170	325	
Reverse Transfer Capacitance	- 110 1011127	C <sub>rss</sub>	-	70	135	1
SWITCHING CHARACTERISTICS (	Notes 6 and 7)	•		•	•	
Turn-On Delay Time		t <sub>d(on)</sub>	_	12	22	ns
Rise Time	$(V_{DD} = -24 \text{ Vdc}, I_D = -3.05 \text{ Adc},$	t <sub>r</sub>	-	16	30	
Turn-Off Delay Time	$V_{GS} = -10 \text{ Vdc},$ $R_{G} = 6.0 \Omega)$	t <sub>d(off)</sub>	-	45	80	1
Fall Time		t <sub>f</sub>	-	45	80	1
Turn-On Delay Time		t <sub>d(on)</sub>	-	16	-	ns
Rise Time	$(V_{DD} = -24 \text{ Vdc}, I_{D} = -1.5 \text{ Adc},$	t <sub>r</sub>	-	42	-	
Turn-Off Delay Time	$V_{GS}$ = -4.5 Vdc, $R_G$ = 6.0 Ω)	t <sub>d(off)</sub>	_	32	_	1
Fall Time	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	t <sub>f</sub>	_	35	_	1
Total Gate Charge	04.744	Q <sub>tot</sub>	-	16	25	nC
Gate-Source Charge	$V_{DS} = -24 \text{ Vdc},$ $V_{GS} = -10 \text{ Vdc},$	Q <sub>gs</sub>	_	2.0	-	1
Gate-Drain Charge	I <sub>D</sub> = -3.05 Adc)	Q <sub>gd</sub>	_	4.5	-	1
BODY-DRAIN DIODE RATINGS (N	ote 6)			l.	I	
Diode Forward On-Voltage	$(I_S = -3.05 \text{ Adc}, V_{GS} = 0 \text{ V})$ $(I_S = -3.05 \text{ Adc}, V_{GS} = 0 \text{ V}, T_J = 125^{\circ}\text{C})$	V <sub>SD</sub>	-	-0.96 -0.78	-1.25 -	Vdc
Reverse Recovery Time			-	34	-	ns
	$(I_S = -3.05 \text{ Adc}, V_{GS} = 0 \text{ Vdc},$ $dI_S/dt = 100 \text{ A}/\mu\text{s})$	t <sub>a</sub>	-	18	-	1
	2.5,21 100,4,40)	t <sub>b</sub>	-	16	-	1
Reverse Recovery Stored Charge		Q <sub>RR</sub>	-	0.03	-	μC

- 5. Handling precautions to protect against electrostatic discharge is mandatory.
- Indicates Pulse Test: Pulse Width = 300 μs max, Duty Cycle = 2%.
   Switching characteristics are independent of operating junction temperature.

#### TYPICAL ELECTRICAL CHARACTERISTICS

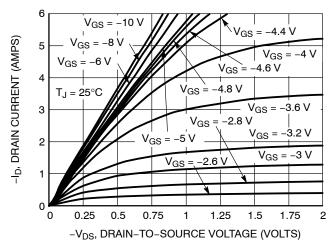


Figure 1. On-Region Characteristics

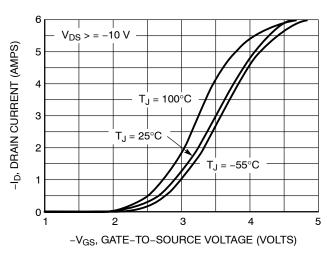


Figure 2. Transfer Characteristics

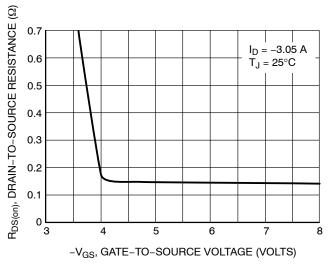


Figure 3. On-Resistance vs. Gate-to-Source Voltage

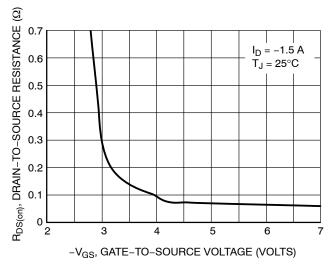


Figure 4. On-Resistance vs. Gate-to-Source Voltage

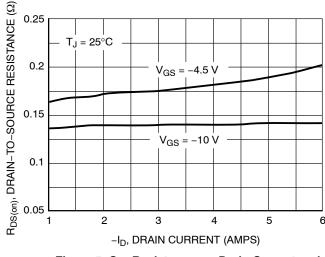


Figure 5. On-Resistance vs. Drain Current and Gate Voltage

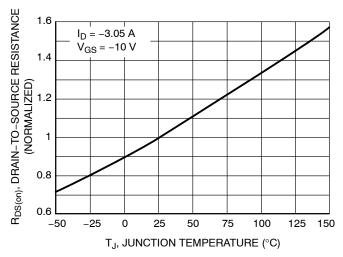
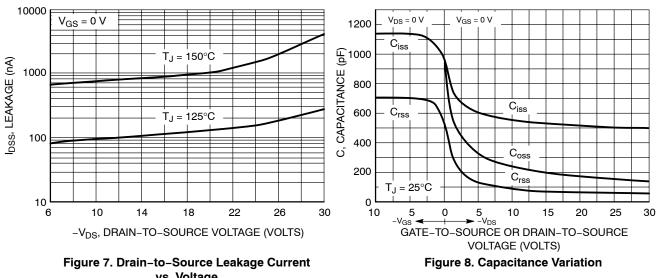


Figure 6. On Resistance Variation with Temperature



vs. Voltage

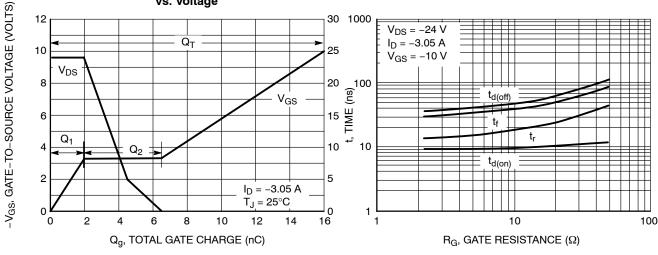


Figure 9. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

Figure 10. Resistive Switching Time Variation vs. Gate Resistance

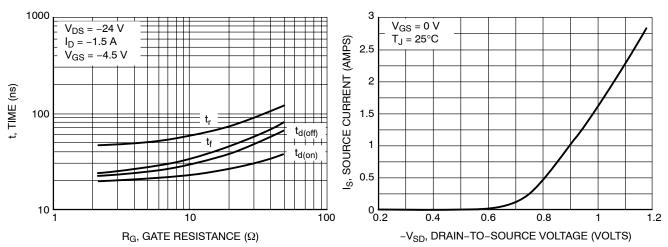
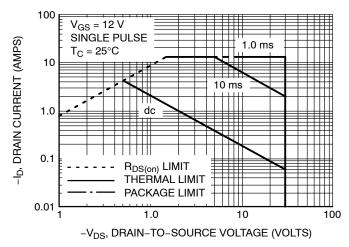


Figure 11. Resistive Switching Time Variation vs. Gate Resistance

Figure 12. Diode Forward Voltage vs. Current



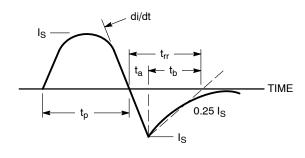


Figure 13. Maximum Rated Forward Biased Safe Operating Area

Figure 14. Diode Reverse Recovery Waveform

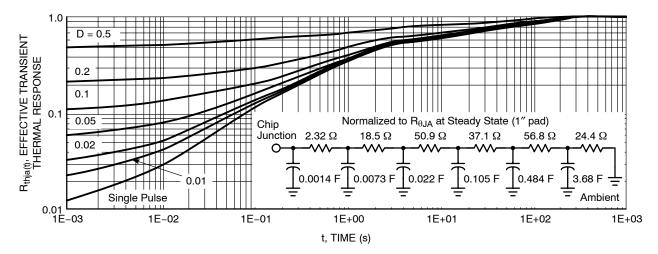
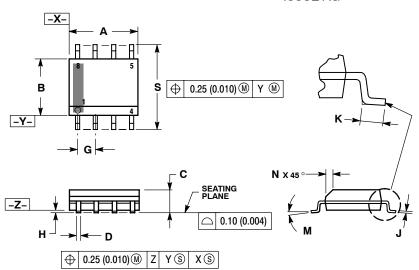


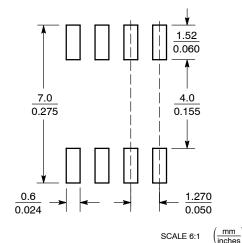
Figure 15. FET Thermal Response

#### PACKAGE DIMENSIONS

#### SOIC-8 NB CASE 751-07 **ISSUE AG**



#### **SOLDERING FOOTPRINT\***



details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
  DIMENSION A AND B DO NOT INCLUDE
- MOLD PROTRUSION.
  MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- DIMENSION DIDOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR
  PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT
- MAXIMUM MATERIAL CONDITION. 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
М	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

#### STYLE 11:

PIN 1. SOURCE 1

- GATE 1 SOURCE 2 2.
- 3.
- GATE 2 5.
- DRAIN 2 DRAIN 2 6.
- DRAIN 1 DRAIN 1

\*For additional information on our Pb-Free strategy and soldering

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