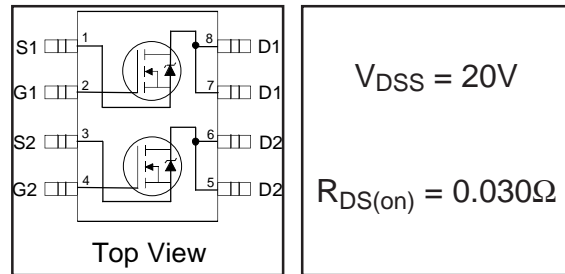


IRF7530

HEXFET® Power MOSFET

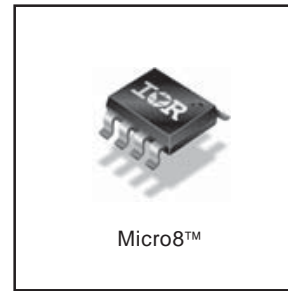
- Trench Technology
- Ultra Low On-Resistance
- Dual N-Channel MOSFET
- Very Small SOIC Package
- Low Profile (<1.1mm)
- Available in Tape & Reel



Description

New trench HEXFET® power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The new Micro8™ package has half the footprint area of the standard SO-8. This makes the Micro8 an ideal device for applications where printed circuit board space is at a premium. The low profile (<1.1 mm) of the Micro8 will allow it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards.



Absolute Maximum Ratings

	Parameter	Max.	Units
V_{DS}	Drain- Source Voltage	20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	5.4	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	4.3	
I_{DM}	Pulsed Drain Current ①	40	
$P_D @ T_A = 25^\circ C$	Power Dissipation	1.3	W
$P_D @ T_A = 70^\circ C$	Power Dissipation	0.80	
	Linear Derating Factor	10	mW/°C
E_{AS}	Single Pulse Avalanche Energy②	33	mJ
V_{GS}	Gate-to-Source Voltage	± 12	V
T_J, T_{STG}	Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

	Parameter	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient③	100	°C/W

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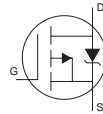
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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

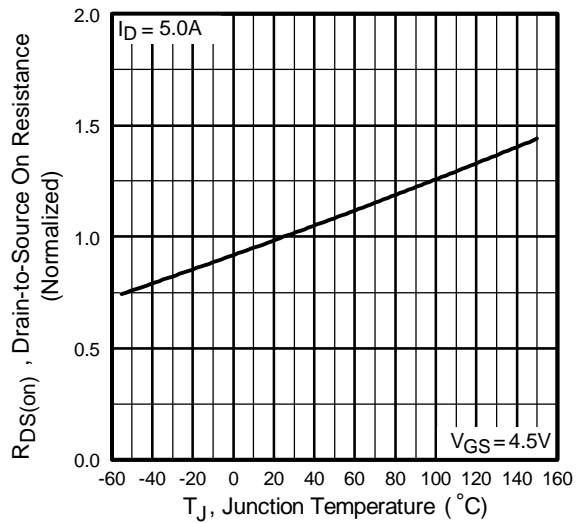
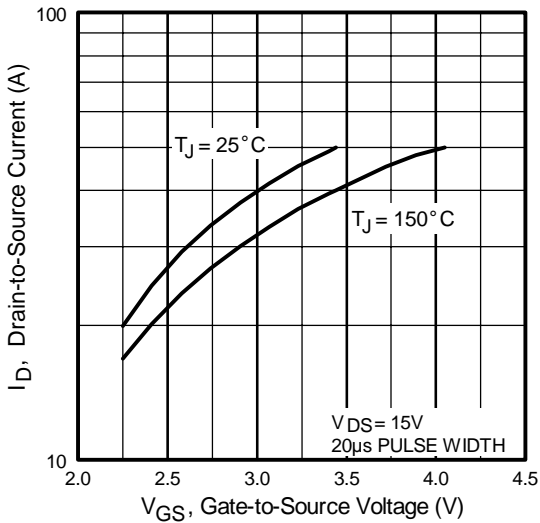
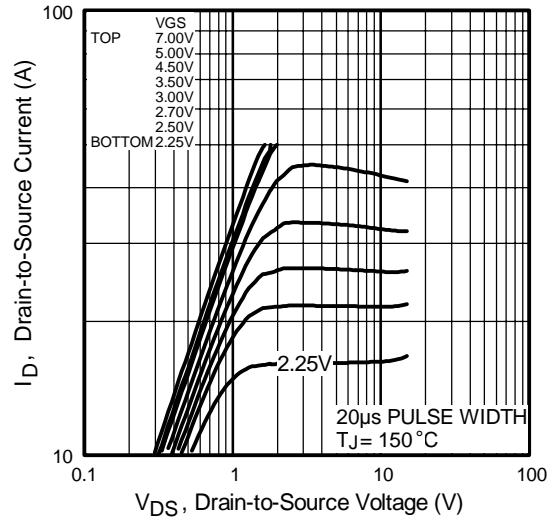
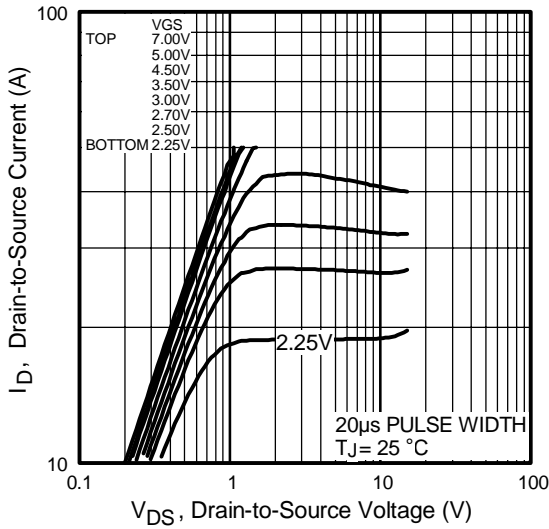
	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	20	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS/ΔT_J}	Breakdown Voltage Temp. Coefficient	—	0.01	—	V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	—	0.030	Ω	V _{GS} = 4.5V, I _D = 5.4A ②
		—	—	0.045		V _{GS} = 2.5V, I _D = 4.6A ②
V _{GS(th)}	Gate Threshold Voltage	0.60	—	1.2	V	V _{DS} = V _{GS} , I _D = 250μA
g _{fs}	Forward Transconductance	13	—	—	S	V _{DS} = 10V, I _D = 5.4A
I _{DSS}	Drain-to-Source Leakage Current	—	—	1.0	μA	V _{DS} = 16V, V _{GS} = 0V
		—	—	25		V _{DS} = 16V, V _{GS} = 0V, T _J = 70°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V _{GS} = 12V
	Gate-to-Source Reverse Leakage	—	—	-100		V _{GS} = -12V
Q _g	Total Gate Charge	—	18	26	nC	I _D = 5.4A
Q _{gs}	Gate-to-Source Charge	—	3.4	5.1		V _{DS} = 16V
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	3.4	5.1		V _{GS} = 4.5V ②
t _{d(on)}	Turn-On Delay Time	—	8.5	—	ns	V _{DD} = 10V
t _r	Rise Time	—	11	—		I _D = 1.0A
t _{d(off)}	Turn-Off Delay Time	—	36	—		R _G = 6.0Ω
t _f	Fall Time	—	16	—		R _D = 10Ω ②
C _{iss}	Input Capacitance	—	1310	—	pF	V _{GS} = 0V
C _{oss}	Output Capacitance	—	180	—		V _{DS} = 15V
C _{rss}	Reverse Transfer Capacitance	—	150	—		f = 1.0MHz

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	1.3	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	40		
V _{SD}	Diode Forward Voltage	—	—	1.2	V	T _J = 25°C, I _S = 1.3A, V _{GS} = 0V ②
t _{rr}	Reverse Recovery Time	—	19	29	ns	T _J = 25°C, I _F = 1.3A
Q _{rr}	Reverse Recovery Charge	—	13	20	nC	di/dt = 100A/μs ②

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width ≤ 400μs; duty cycle ≤ 2%.
- ③ When mounted on 1 inch square copper board, t < 10 sec
- ④ Starting T_J = 25°C, L = 2.6mH
R_G = 25Ω, I_{AS} = 5.0A. (See Figure 10)



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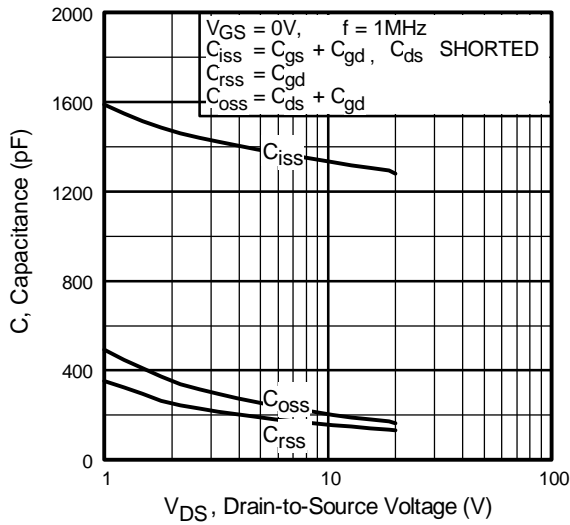


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

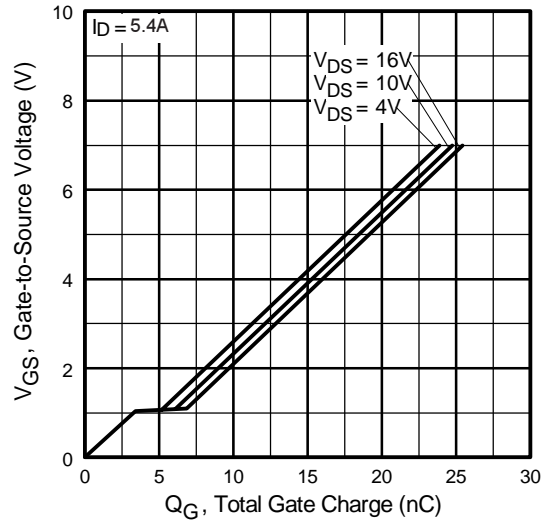


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

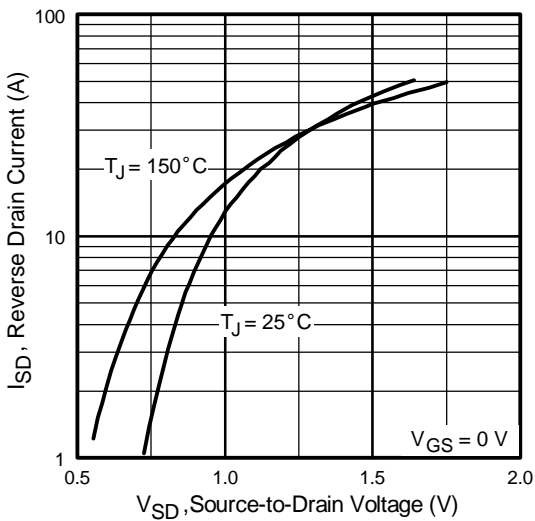


Fig 7. Typical Source-Drain Diode Forward Voltage

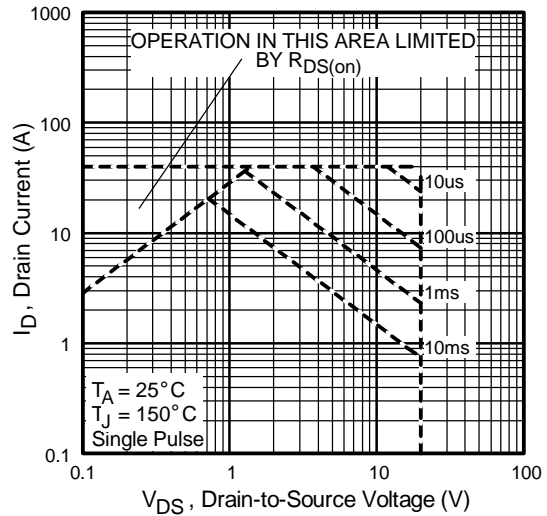


Fig 8. Maximum Safe Operating Area

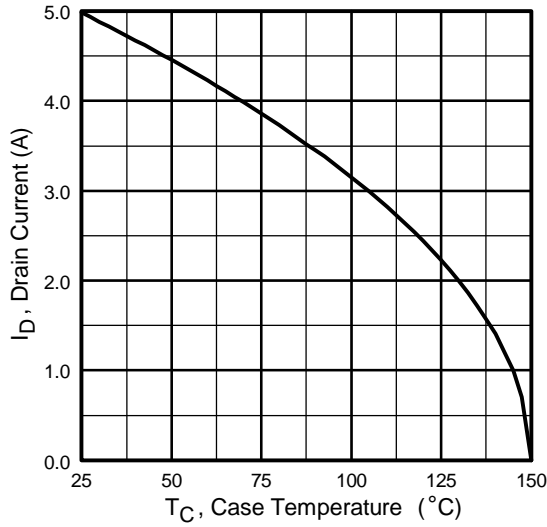


Fig 9. Maximum Drain Current Vs. Case Temperature

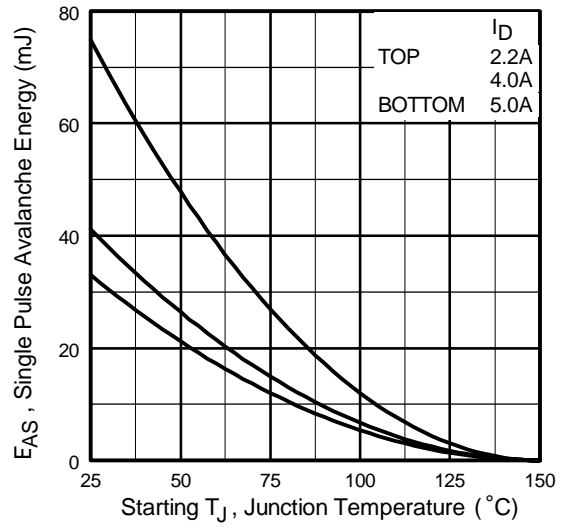


Fig 10. Maximum Avalanche Energy Vs. Drain Current

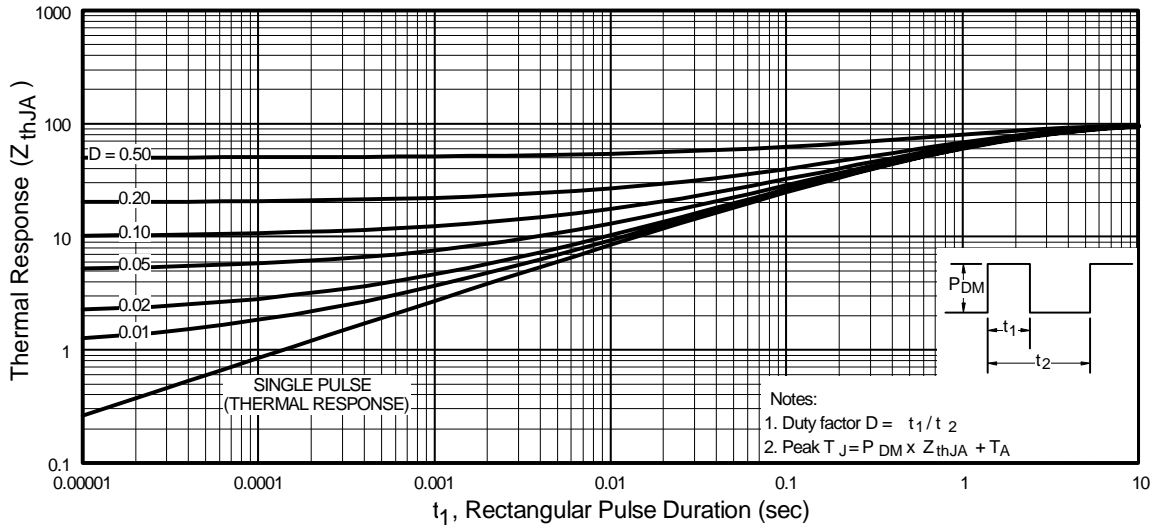


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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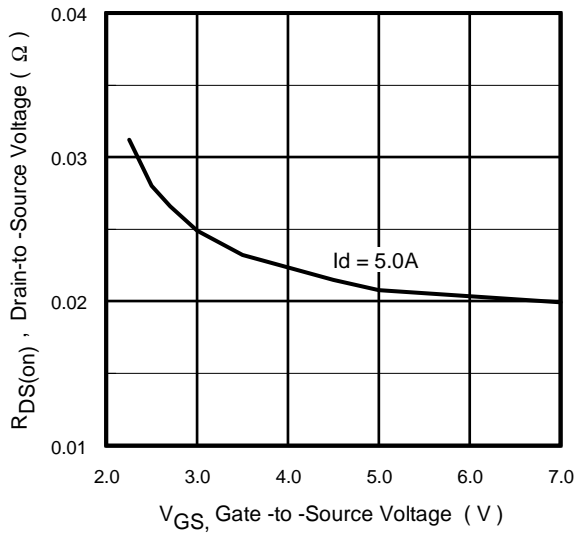


Fig 12. On-Resistance Vs. Gate Voltage

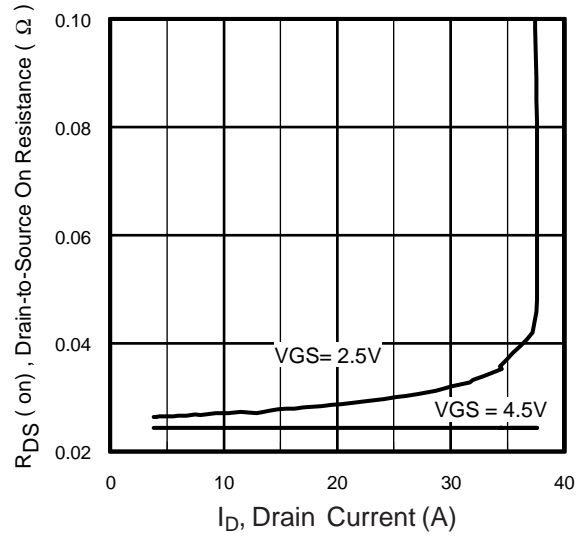
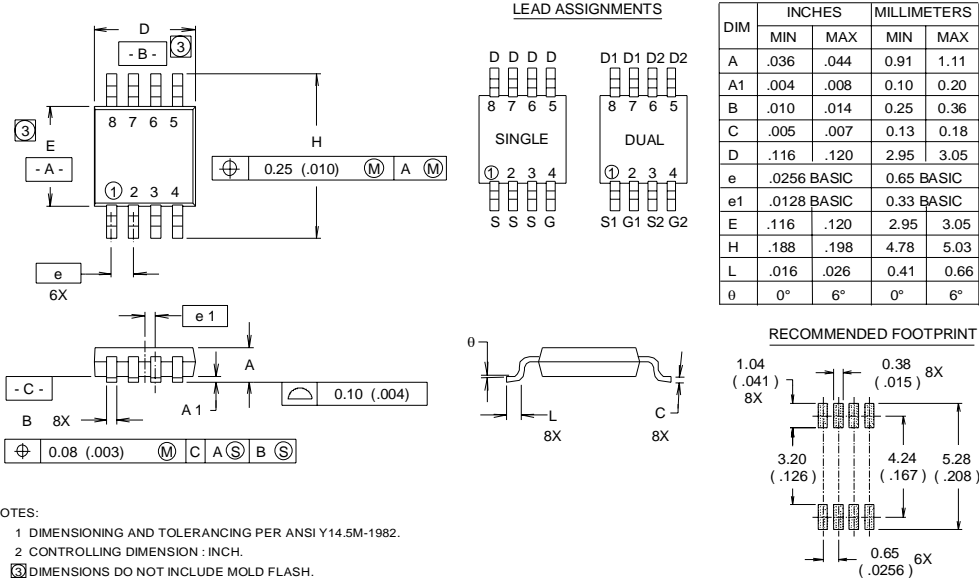


Fig 13. On-Resistance Vs. Drain Current

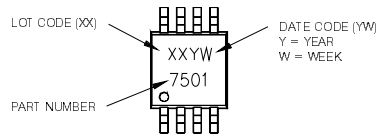
Micro8™ Package Outline

Dimensions are shown in millimeters (inches)



Micro8™ Part Marking Information

EXAMPLE: THIS IS AN IRF7501



DATE CODE EXAMPLES:
 YYWW = 9503 = 5C
 YYWW = 9532 = EF

WW = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR

YEAR	Y	WORK WEEK	W
2001	1	01	A
2002	2	02	B
2003	3	03	C
1994	4	04	D
1995	5		
1996	6		
1997	7		
1998	8		
1999	9		
2000	0	24	X
		25	Y
		26	Z

WW = (27-52) IF PRECEDED BY A LETTER

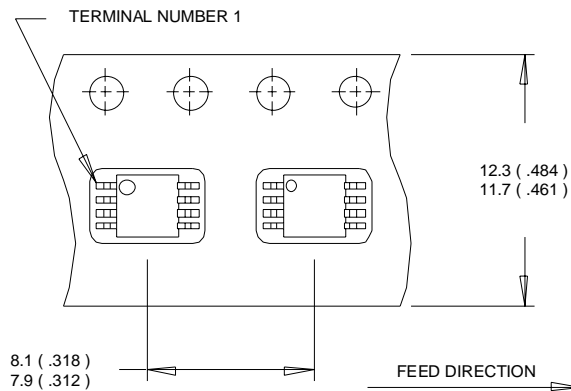
YEAR	Y	WORK WEEK	W
2001	A	27	A
2002	B	28	B
2003	C	29	C
1994	D	30	D
1995	E		
1996	F		
1997	G		
1998	H		
1999	J		
2000	K	50	X
		51	Y
		52	Z

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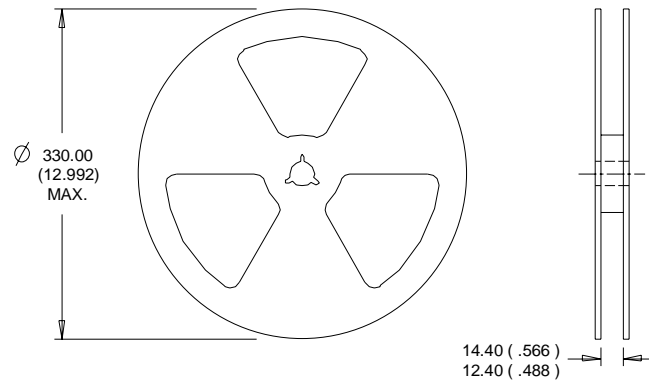
Micro8™ Tape & Reel Information

Dimensions are shown in millimeters (inches)



NOTES:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.
2. CONTROLLING DIMENSION : MILLIMETER.



NOTES:

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

This product has been designed and qualified for the consumer market.
Qualification Standards can be found on IR's Web site.

International
IR Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7903

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Data and specifications subject to change without notice. 04/04

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