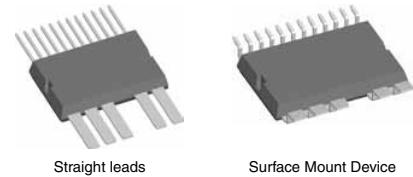
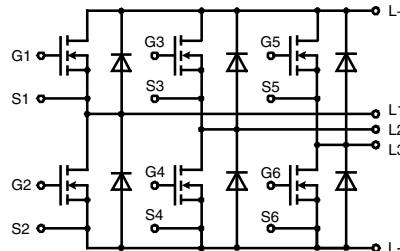


**Three phase full Bridge**  
with Trench MOSFETs  
in DCB isolated high current package

**V<sub>DSS</sub>** = 100 V  
**I<sub>D25</sub>** = 90 A  
**R<sub>DSON typ.</sub>** = 7.5 mΩ



Straight leads      Surface Mount Device

**MOSFETs**

| Symbol           | Conditions                     | Maximum Ratings |      |   |
|------------------|--------------------------------|-----------------|------|---|
| V <sub>DSS</sub> | T <sub>J</sub> = 25°C to 150°C |                 | 100  | V |
| V <sub>GS</sub>  |                                |                 | ± 20 | V |
| I <sub>D25</sub> | T <sub>C</sub> = 25°C          |                 | 90   | A |
| I <sub>D90</sub> | T <sub>C</sub> = 90°C          |                 | 68   | A |
| I <sub>F25</sub> | T <sub>C</sub> = 25°C (diode)  |                 | 90   | A |
| I <sub>F90</sub> | T <sub>C</sub> = 90°C (diode)  |                 | 68   | A |

**Symbol**    **Conditions**

|   |   | Characteristic Values                               |                        |                          |
|---|---|---|------------------------|--------------------------|
|   |   | (T <sub>J</sub> = 25°C, unless otherwise specified) |                        |                          |
|   |   |   | min.                   | typ.                     |
| R <sub>DSON</sub> <sup>1)</sup>   | on chip level at<br>V <sub>GS</sub> = 10 V; I <sub>D</sub> = 80 A   | T <sub>J</sub> = 25°C<br>T <sub>J</sub> = 125°C     |                        | 7.5<br>14                |
| V <sub>GS(th)</sub>   | V <sub>DS</sub> = 20 V; I <sub>D</sub> = 250 μA   |   | 2.5                    | 4.5                      |
| I <sub>DSS</sub>  | V <sub>DS</sub> = V <sub>DSS</sub> ; V <sub>GS</sub> = 0 V  | T <sub>J</sub> = 25°C<br>T <sub>J</sub> = 125°C     |                        | 1<br>0.1                 |
| I <sub>GSS</sub>  | V <sub>GS</sub> = ± 20 V; V <sub>DS</sub> = 0 V   |   |                        | 0.2                      |
| Q <sub>g</sub><br>Q <sub>gs</sub><br>Q <sub>gd</sub>                          | V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 65 V; I <sub>D</sub> = 90 A   |   | 90<br>30<br>30         | nC<br>nC<br>nC           |
| t <sub>d(on)</sub><br>t <sub>r</sub><br>t <sub>d(off)</sub><br>t <sub>f</sub> | inductive load<br>V <sub>GS</sub> = 10 V; V <sub>DS</sub> = 48 V<br>I <sub>D</sub> = 70 A; R <sub>G</sub> = 33 Ω;<br>T <sub>J</sub> = 125°C |   | 130<br>95<br>290<br>55 | ns<br>ns<br>ns<br>ns     |
| E <sub>on</sub><br>E <sub>off</sub><br>E <sub>recoff</sub>                    |   |   | 0.4<br>0.4<br>0.007    | mJ<br>mJ<br>mJ           |
| R <sub>thJC</sub><br>R <sub>thJH</sub>  | with heat transfer paste (IXYS test setup)  |   | 1.3                    | 1.0<br>1.6<br>K/W<br>K/W |

<sup>1)</sup> V<sub>DS</sub> = I<sub>D</sub> · (R<sub>DSON</sub> + 2R<sub>Pin to Chip</sub>)

**Applications**

- AC drives
- in automobiles
    - electric power steering
    - starter generator
  - in industrial vehicles
    - propulsion drives
    - fork lift drives
  - in battery supplied equipment

**Features**

- MOSFETs in trench technology:
  - low R<sub>DSON</sub>
  - optimized intrinsic reverse diode
- package:
  - high level of integration
  - high current capability 300 A max.
  - aux. terminals for MOSFET control
  - terminals for soldering or welding connections
  - isolated DCB ceramic base plate with optimized heat transfer
- Space and weight savings

**Package options**

- 2 lead frames available
  - straight leads (SL)
  - SMD lead version (SMD)

**Source-Drain Diode**

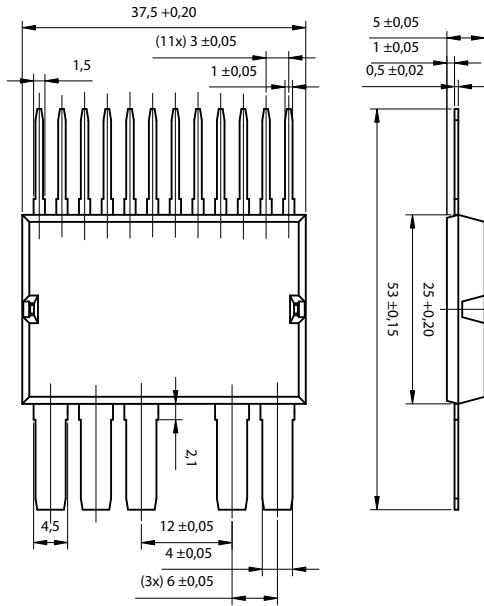
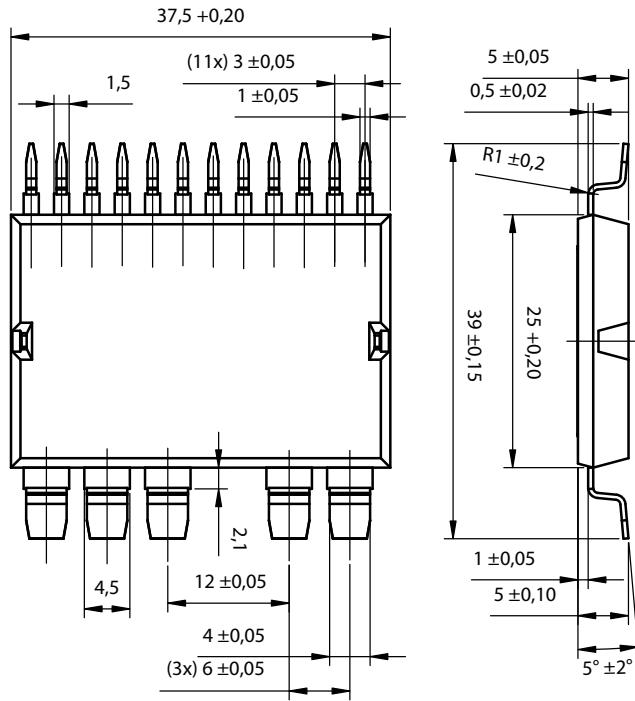
| Symbol                           | Conditions   | Characteristic Values |      |                          |
|----------------------------------|--|-----------------------|------|--------------------------|
|                                  |  | min.                  | typ. | max.                     |
| $V_{SD}$                         | (diode) $I_F = 70 \text{ A}; V_{GS} = 0 \text{ V}$                             | 0.9                   | 1.2  | V                        |
| $t_{rr}$<br>$Q_{RM}$<br>$I_{RM}$ | $I_F = 70 \text{ A}; -di_F/dt = 800 \text{ A}/\mu\text{s}; V_R = 48 \text{ V}$ | 55<br>0.95<br>33      |      | ns<br>$\mu\text{C}$<br>A |

**Component**

| Symbol             | Conditions  | Maximum Ratings          |  |  |
|--------------------|---|--------------------------|--|--|
| $I_{RMS}$          | per pin in main current paths (P+, N-, L1, L2, L3)<br>may be additionally limited by external connections | 300                      | A  |  |
| $T_J$<br>$T_{stg}$ |   | -55...+175<br>-55...+125 | $^{\circ}\text{C}$<br>$^{\circ}\text{C}$ |  |
| $V_{ISOL}$         | $I_{ISOL} \leq 1 \text{ mA}, 50/60 \text{ Hz}, f = 1 \text{ minute}$                                      | 1000                     | V~                                       |  |
| $F_c$              | mounting force with clip  | 50 - 250                 | N  |  |

| Symbol                          | Conditions  | Characteristic Values |      |                  |
|---------------------------------|---|-----------------------|------|------------------|
|                                 |   | min.                  | typ. | max.             |
| $R_{pin to chip}$ <sup>1)</sup> |   |                       | 0.6  | $\text{m}\Omega$ |
| $C_P$                           | coupling capacity between shorted pins and mounting tab in the case |                       | 160  | pF               |
| <b>Weight</b>                   |   |                       | 25   | g                |

<sup>1)</sup>  $V_{DS} = I_D \cdot (R_{DS(on)} + 2R_{Pin to Chip})$

**Straight Leads      GWM 100-01X1-SL****Surface Mount Device      GWM 100-01X1-SMD**

| Leads    | Ordering | Part Name & Packing Unit Marking | Part Marking | Delivering Mode | Base Qty. | Ordering Code |
|----------|----------|----------------------------------|--------------|-----------------|-----------|---------------|
| Straight | Standard | GWM 100-01X1 - SL                | GWM 100-01X1 | Blister         | 28        | 505 535       |
| SMD      | Standard | GWM 100-01X1 - SMD               | GWM 100-01X1 | Blister         | 28        | 505 542       |

IXYS reserves the right to change limits, test conditions and dimensions.

20110307e

© 2011 IXYS All rights reserved

3 - 6

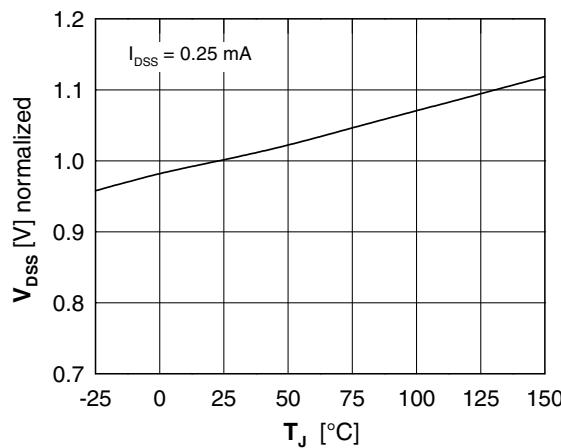


Fig. 1 Drain source breakdown voltage  $V_{DSS}$  vs. junction temperature  $T_J$

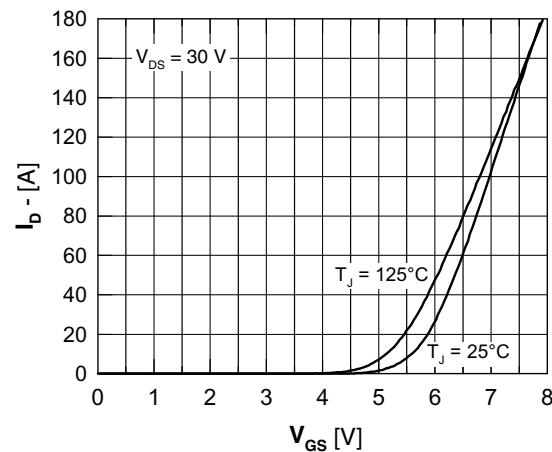


Fig. 2 Typical transfer characteristic

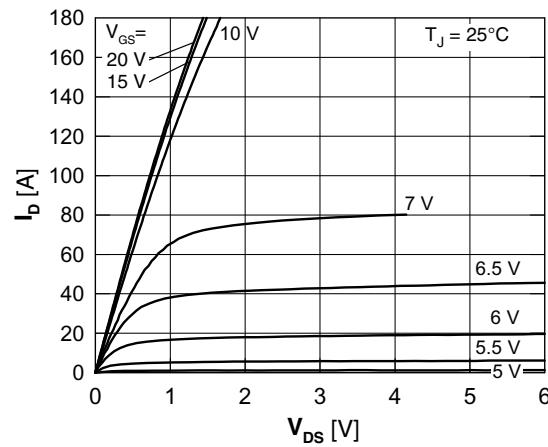


Fig. 3 Typical output characteristic

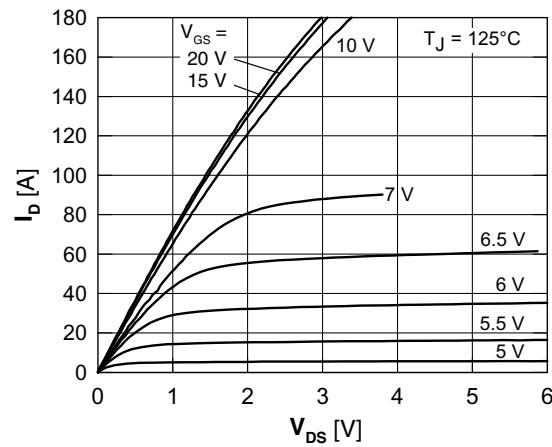


Fig. 4 Typical output characteristic

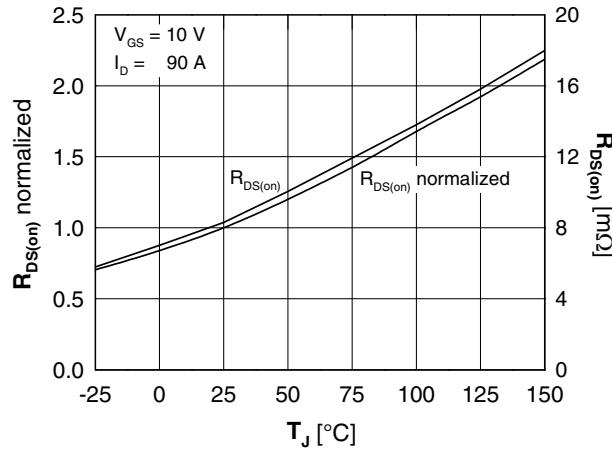


Fig. 5 Drain source on-state resistance  $R_{DS(on)}$  versus junction temperature  $T_J$

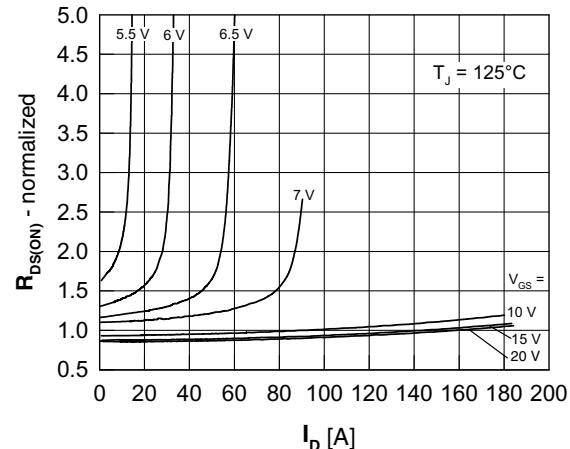


Fig. 6 Drain source on-state resistance  $R_{DS(on)}$  versus  $I_D$

IXYS reserves the right to change limits, test conditions and dimensions.

20110307e

© 2011 IXYS All rights reserved

4 - 6

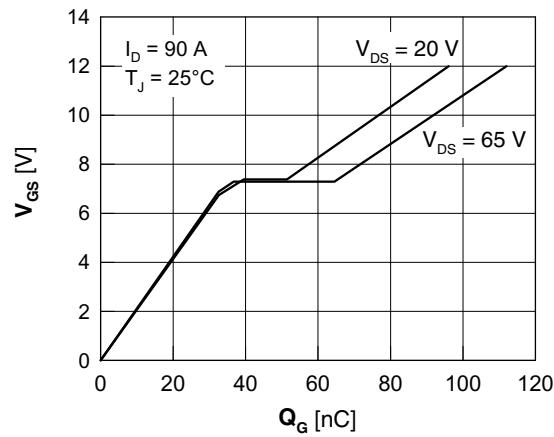


Fig. 7 Gate charge characteristic

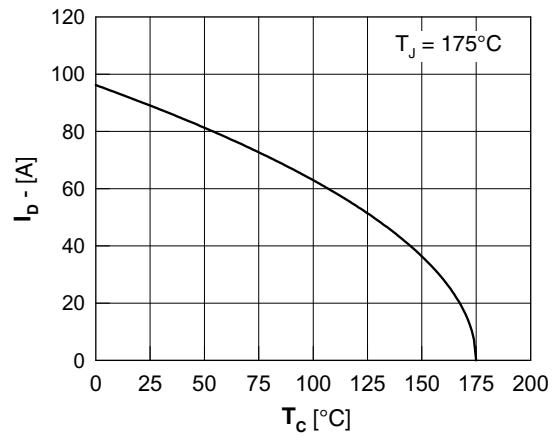
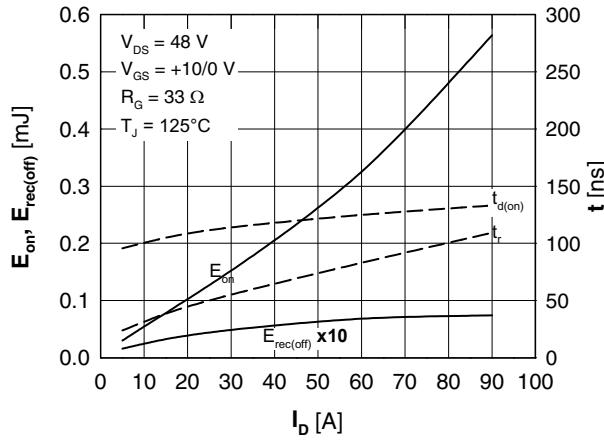
Fig. 8 Drain current  $I_D$  vs. case temperature  $T_c$ 

Fig. 9 Typ. turn-on energy &amp; switching times vs. collector current, inductive switching

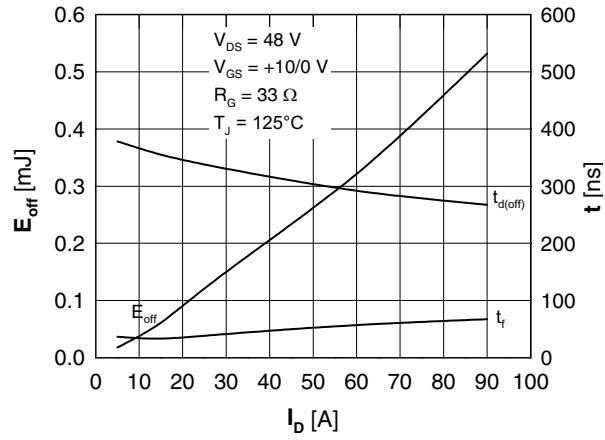


Fig. 10 Typ. turn-off energy &amp; switching times vs. collector current, inductive switching

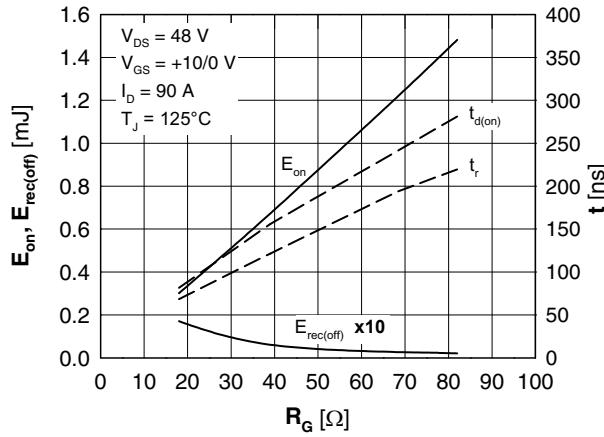


Fig. 11 Typ. turn-on energy &amp; switching times vs. gate resistor, inductive switching

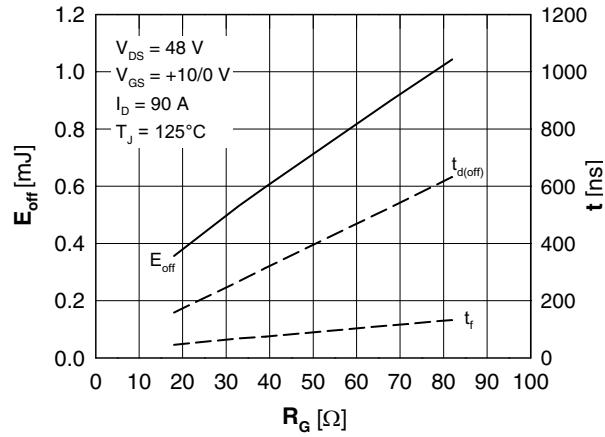


Fig. 12 Typ. turn-off energy &amp; switching times vs. gate resistor, inductive switching

IXYS reserves the right to change limits, test conditions and dimensions.

20110307e

© 2011 IXYS All rights reserved

5 - 6

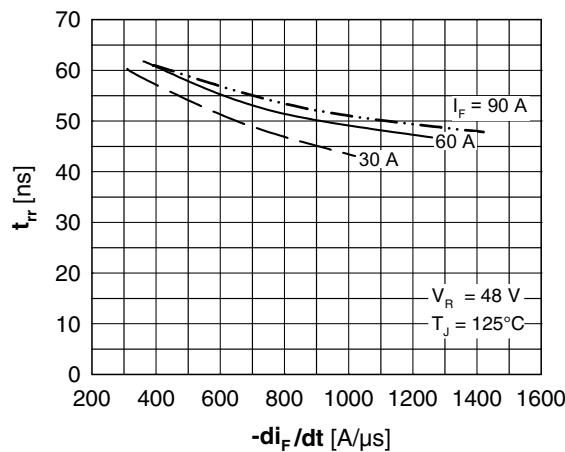


Fig. 13 Reverse recovery time  $t_{rr}$  of the body diode vs.  $di/dt$

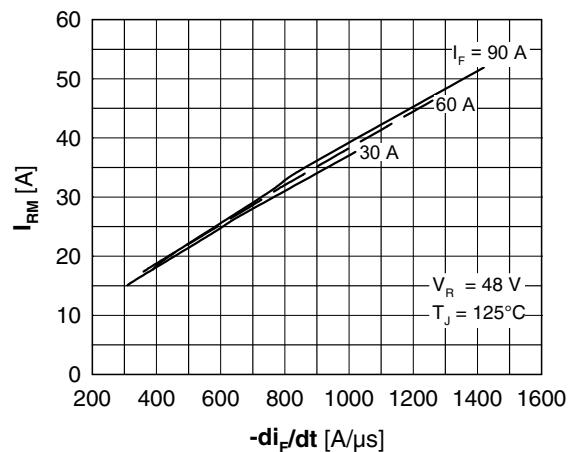


Fig. 14 Reverse recovery current  $I_{RM}$  of the body diode vs.  $di/dt$

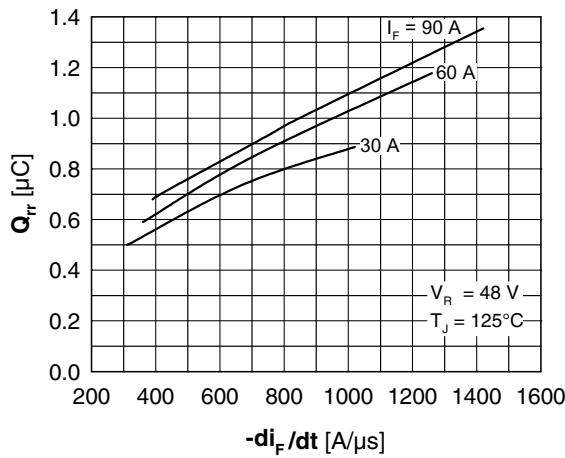


Fig. 15 Reverse recovery charge  $Q_{rr}$  of the body diode vs.  $di/dt$

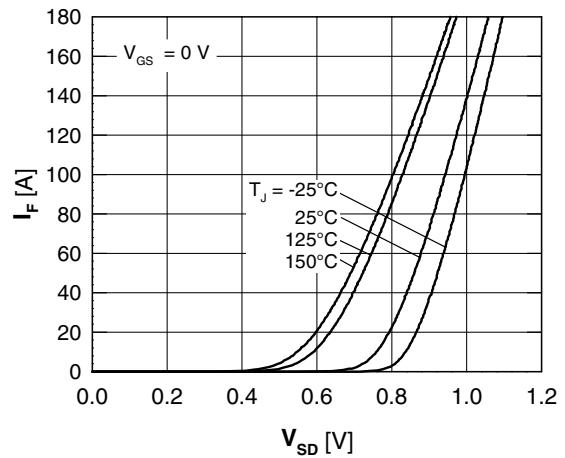


Fig. 16 Source drain diode current  $I_F$  vs. source drain voltage  $V_{SD}$  (body diode)

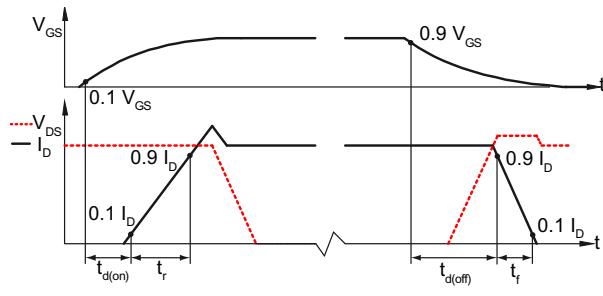


Fig. 17 Definition of switching times

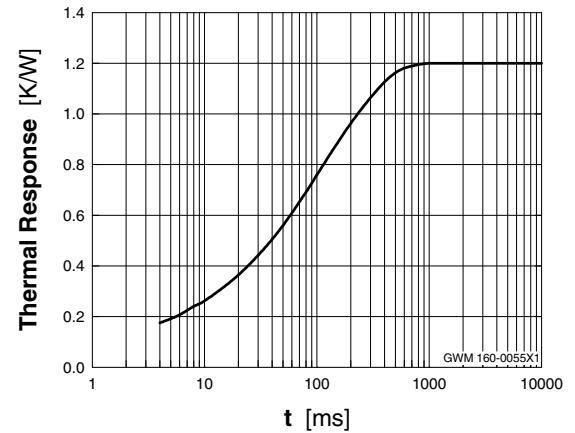


Fig. 18 Typ. thermal impedance junction to heatsink  $Z_{thJH}$  with heat transfer paste

IXYS reserves the right to change limits, test conditions and dimensions.

20110307e

© 2011 IXYS All rights reserved

6 - 6