4V Drive Nch+Pch MOSFET **SP8M2**

●Structure

Silicon N-channel MOSFET/ Silicon P-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) Built-in G-S protection diode.
- 3) Small surface mount package (SOP8).

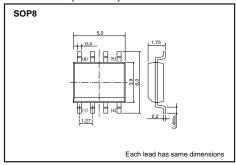
Applications

Switching

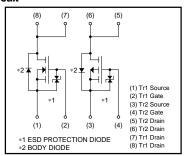
Package specifications

	Package	Taping		
Туре	Code	TB		
	Basic ordering unit (pieces)	2500		
SP8M2		0		

● Dimensions (Unit: mm)



●Inner circuit



● Absolute maximum ratings (Ta=25°C)

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Paramete	Cumbal	Lin	Lleit			
Parameter		Symbol	Tr1: N-ch	Tr2 : P-ch	Unit	
Drain-source voltage		Voss	30	-30	V	
Gate-source voltage		V _{GSS}	±20	±20	V	
Drain current	Continuous	ID	±3.5	±3.5	Α	
	Pulsed	I _{DP} *1	±14	±14	Α	
Source current	Continuous	Is	1.6	-1.6	Α	
(Body diode)	Pulsed	Isp*1	14	-14	Α	
Total power dissipation		P _D *2	2.0		W / TOTAL	
Channel temperature		Tch	150		°C	
Storage temperature		Tsta	-55 to +150		°C	

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^{*1} Pw≤10μs, Duty cycle≤1%

N-ch

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	ı	±10	μΑ	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	$V_{(BR)\;DSS}$	30	-	-	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	-	-	1	μΑ	V _{DS} = 30V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	-	2.5	V	V _{DS} = 10V, I _D = 1mA
0		-	59	83	mΩ	I _D = 3.5A, V _{GS} = 10V
Static drain-source on-state resistance	RDS (on)*	-	93	130	mΩ	ID= 3.5A, VGS= 4.5V
169191911CG		-	107	150	mΩ	I _D = 3.5A, V _{GS} = 4V
Forward transfer admittance	Y _{fs} *	2.0	-	-	S	V _{DS} = 10V, I _D = 3.5A
Input capacitance	Ciss	-	140	_	pF	V _{DS} = 10V
Output capacitance	Coss	_	45	-	pF	Vgs=0V
Reverse transfer capacitance	Crss	-	30	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	6	_	ns	Vdd≒ 15V
Rise time	tr *	_	6	_	ns	ID= 1.75A
Turn-off delay time	t _{d (off)} *	_	17	_	ns	V _{GS} = 10V R _L = 8.57Ω
Fall time	t _f *	_	4	_	ns	R _G =10Ω
Total gate charge	Qg *	_	2.5	3.5	nC	V _{DD} ≒15V, V _{GS} =5V
Gate-source charge	Q _{gs} *	_	0.8	-	nC	I _D = 3.5A
Gate-drain charge	Q _{gd} *	-	0.8	-	nC	$R_L = 4.29\Omega, R_G = 10\Omega$
*Pulsed						

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp*	_	_	1.2	V	Is= 6.4A, Vgs=0V

*Pulsed

P-ch

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	_	-	±10	μΑ	V _{GS} = ±20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	-30	-	-	V	I _D = -1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	-	-1	μΑ	Vps= -30V, Vgs=0V
Gate threshold voltage	V _{GS (th)}	-1.0	-	-2.5	V	V _{DS} = -10V, I _D = -1mA
Otatia daria assura a stata		-	65	90	mΩ	I _D = -3.5A, V _G S= -10V
Static drain-source on-state resistance	RDS (on)*	_	100	140	mΩ	In= -1.75A, Vgs= -4.5V
resistance		_	120	165	mΩ	I _D = -1.75A, V _G s= -4V
Forward transfer admittance	Y _{fs} *	1.8	-	-	S	V _{DS} = -10V, I _D = -1.75A
Input capacitance	Ciss	-	490	-	pF	V _{DS} = -10V
Output capacitance	Coss	_	110	_	pF	Vgs= 0V
Reverse transfer capacitance	Crss	-	75	-	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	-	10	-	ns	Vpp≒ –15V
Rise time	tr *	_	15	_	ns	ID= -1.75A
Turn-off delay time	t _{d (off)} *	-	35	-	ns	Vgs= -10V RL= 8.57Ω
Fall time	t _f *	-	10	-	ns	R _G = 10Ω
Total gate charge	Qg *	_	5.5	7.7	nC	V _{DD} ≒-15V, V _{GS} =-5V
Gate-source charge	Q _{gs} *	-	1.5	-	nC	I _D = -3.5A
Gate-drain charge	Q _{gd} *	-	2.0	-	nC	R _L = 4.29Ω, R _G = 10Ω

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp*	_	_	-1.2	V	Is= -1.6A, Vgs=0V

*Pulsed

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Appendix1-Rev2.0