### MMBFJ175LT1G

# **JFET Chopper**

## **P-Channel - Depletion**

#### **Features**

• These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Drain-Gate Voltage	$V_{DG}$	25	V
Reverse Gate-Source Voltage	V <sub>GS(r)</sub>	-25	V

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Gate – Source Breakdown Voltage ( $V_{DS} = 0$ , $I_D = 1.0 \mu A$ )	V <sub>(BR)GSS</sub>	30	_	V	
Gate Reverse Current (V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 20 V)	I <sub>GSS</sub>	-	1.0	nA	
Gate – Source Cutoff Voltage $(V_{DS} = 15, I_D = 10 \text{ nA})$	V <sub>GS(OFF)</sub>	3.0	6.0	V	

#### **ON CHARACTERISTICS**

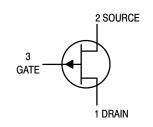
Zero Gate-Voltage D (V <sub>GS</sub> = 0, V <sub>DS</sub> =	I <sub>DSS</sub>	7.0	60	mA	
Drain Cutoff Current (V <sub>DS</sub> = 15 V, V <sub>GS</sub>	I <sub>D(off)</sub>	_	1.0	nA	
Drain Source On Resistance (I <sub>D</sub> = 500 μA)		r <sub>DS(on)</sub>	-	125	Ω
Input Capacitance	\\ 0 \\ 10\\	C <sub>iss</sub>	_	11	
Reverse Transfer Capacitance	V <sub>DS</sub> = 0, V <sub>GS</sub> = 10V f = 1.0 MHz	C <sub>rss</sub>	-	5.5	pF

2. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.



#### ON Semiconductor®

http://onsemi.com





SOT-23 (TO-236) CASE 318 STYLE 10

#### **MARKING DIAGRAM**



6W = Device Code
M = Date Code\*

Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### ORDERING INFORMATION

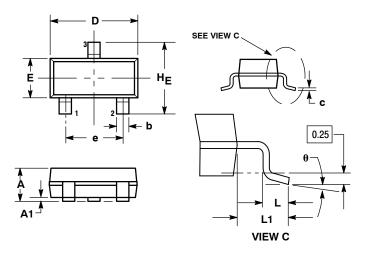
Device	Package	Shipping <sup>†</sup>
MMBFJ175LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### MMBFJ175LT1G

#### PACKAGE DIMENSIONS

**SOT-23 (TO-236)** CASE 318-08 ISSUE AN



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
   V14 5M 1982
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. 318–01 THRU –07 AND –09 OBSOLETE, NEW STANDARD 318–08.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104

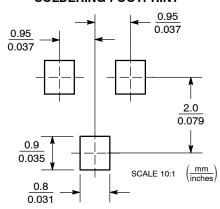
STYLE 10:

PIN 1. DRAIN

2. SOURCE

3. GATE

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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