

# SIDC19D60SIC3

## Silicon Carbide Schottky Diode

## **FEATURES:**

- Worlds first 600V Schottky diode
- Revolutionary semiconductor material -Silicon Carbide
- Switching behavior benchmark
- No reverse recovery
- No temperature influence on the switching behavior
- Ideal diode for Power Factor Correction
- No forward recovery

## Applications:

SMPS, PFC, snubber



Chip Type	$V_{BR}$	I <sub>F</sub>	Die Size	Package	Ordering Code
SIDC19D60SIC3	600V	6A	1.38 x 1.38 mm <sup>2</sup>	sawn on foil	Q67050-A4162- A104

#### **MECHANICAL PARAMETER:**

Raster size	1.38 x 1.38	mm			
Anode pad size	1.08 x 1.08				
Area total / active	1.904 / 1.191	mm <sup>2</sup>			
Thickness	355	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	2058 pcs				
Passivation frontside	Photoimide				
Anode metalization	3200 nm Al				
Cathode metalization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤ 250μm				
Reject Ink Dot Size	Ø ≥ 0.3 mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

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## **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$		600	V
Surge peak reverse voltage	V <sub>RSM</sub>		600	V
Continuous forward current limited by $T_{jmax}$	I <sub>F</sub>		6	
Single pulse forward current (depending on wire bond configuration)	I <sub>FSM</sub>	$T_C$ =25° C, $t_P$ =10 ms sinusoidal	21.5	А
Maximum repetitive forward current limited by T <sub>jmax</sub>	I <sub>FRM</sub>	$T_C = 100$ °C, $T_j = 150$ °C, D = 0.1	28	
Non repetitive peak forward current	I <sub>FMAX</sub>	$T_C = 25^{\circ}C$ , $tp = 10\mu$ s	60	
Operating junction and storage temperature	$T_{\rm j}$ , $T_{\rm stg}$		-55+175	°C

## Static Electrical Characteristics (tested on chip), $T_j$ =25 °C, unless otherwise specified

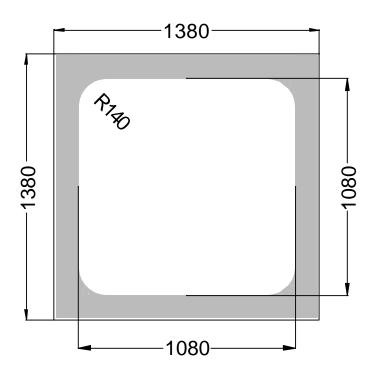
Parameter	Symbol	Condi	itions		Value		Unit	
i arameter	Cyllibol	Cond	itions	min.	Тур.	max.		
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =600V	<i>T<sub>j</sub></i> =25° <i>C</i>		20	200	μΑ	
Forward voltage drop	V <sub>F</sub>	<i>I<sub>F</sub></i> =6 <i>A</i>	T <sub>j</sub> =25°C		1.5	1.7	V	

## **Dynamic Electrical Characteristics**, at $T_j = 25$ °C, unless otherwise specified, tested at component

Parameter	Symbol	Conditions		Value			Unit
raiailletei	Symbol			min.	Тур.	max.	Jonit
Total capacitive charge	Q <sub>C</sub>	$I_F=6A$ di/dt=200A/ms $V_R=400V$	$T_j = 150  ^{\circ}\mathrm{C}$		21		nC
Switching time	t <sub>rr</sub>	I <sub>F</sub> =6A di/dt=200A/ <b>m</b> s V <sub>R</sub> = 400V	$T_j = 150  ^{\circ}\text{C}$		n.a.		ns
Total capacitance	С	I <sub>F</sub> =6A di/dt=200A/ <b>m</b> s	V <sub>R</sub> = 1 V		300		
		$T_j=25$ °C f=1MHz	V <sub>R</sub> =300V		20		pF
			V <sub>R</sub> =600V		15		









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#### **FURTHER ELECTRICAL CHARACTERISTICS:**

This chip data sheet refers to the device data sheet s

## **Description:**

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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