

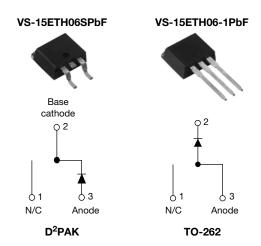


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HALOGEN

FREE

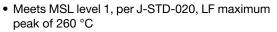
# Hyperfast Rectifier, 15 A FRED Pt®

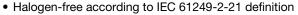


PRODUCT SUMMARY					
t <sub>rr</sub> (typical)	22 ns				
I <sub>F(AV)</sub>	15 A				
V <sub>R</sub>	600 V				

#### **FEATURES**

- Hyperfast recovery time
- · Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Single die center tap module





- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

#### **DESCRIPTION/APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the ac-to-dc section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Peak repetitive reverse voltage	$V_{RRM}$		600	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 140 °C	15				
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120	Α			
Peak repetitive forward current	I <sub>FM</sub>		30				
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	$V_{BR}$ , $V_{R}$	I <sub>R</sub> = 100 μA	600	-	-	.,,			
Forward voltage	VF	I <sub>F</sub> = 15 A	-	1.8	2.2	V			
Torward voitage	۷F	I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	1	1.3	1.6				
Deverge legicage augment		$V_R = V_R$ rated	-	0.2	50				
Reverse leakage current	I <sub>R</sub>	$T_J = 150 ^{\circ}\text{C},  V_R = V_R \text{ rated}$	-	30	500	μΑ			
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	20	-	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	-	nH			

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 10$	ı	22	30		
Reverse recovery time		$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A}$	$I_F = 15 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		28		35
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	29	-	ns
		T <sub>J</sub> = 125 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 390 \text{ V}$	-	75	-	
Peak recovery current	1	T <sub>J</sub> = 25 °C		-	3.5	-	A
reak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		ı	7	-	
Reverse recovery charge				-	57	-	μC
neverse recovery charge	$Q_{rr}$	T <sub>J</sub> = 125 °C		-	300	-	μΟ
Reverse recovery time	t <sub>rr</sub>		I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 800 A/µs	-	51	-	ns
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		ı	20	-	Α
Reverse recovery charge	Q <sub>rr</sub>		V <sub>R</sub> = 390 V	-	580	-	nC

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.0	1.3		
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Weight			-	2.0	-	g	
weignt			=	0.07	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking daviso		Case style D <sup>2</sup> PAK	15ETH06S				
Marking device		Case style TO-262		15ET	H06-1		

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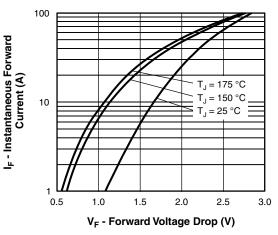


Fig. 1 - Typical Forward Voltage Drop Characteristics

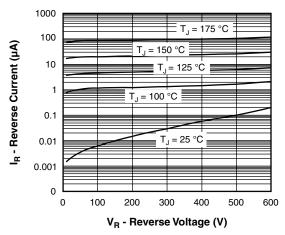


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

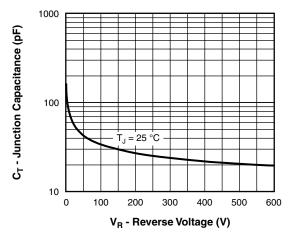


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

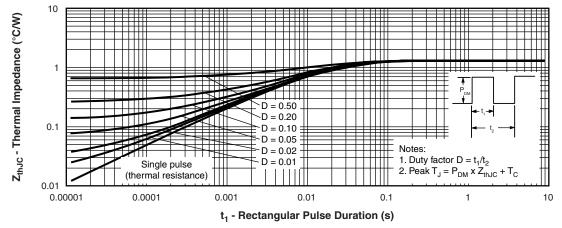


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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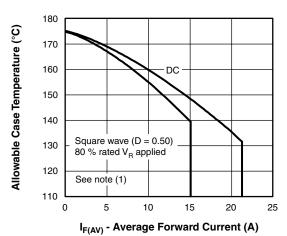


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

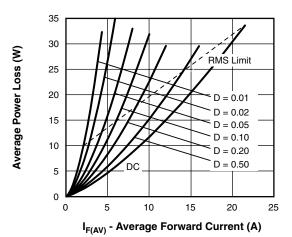


Fig. 6 - Forward Power Loss Characteristics

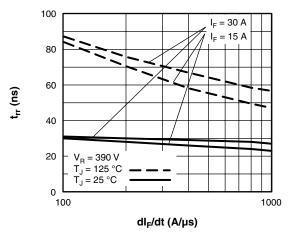


Fig. 7 - Typical Reverse Recovery vs. dI<sub>F</sub>/dt

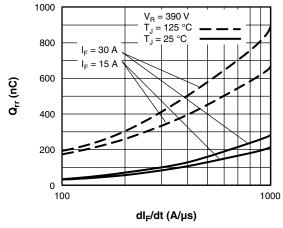


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$ 

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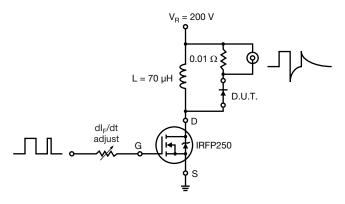
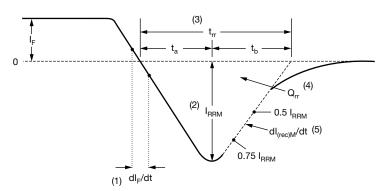


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $t_{\rm rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{\rm F}$  to point where a line passing through 0.75  $I_{\rm RBM}$  and 0.50  $I_{\rm RBM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{\text{RRM}}$

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

(5) dl<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 10 - Reverse Recovery Waveform and Definitions

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#### **ORDERING INFORMATION TABLE**

Device code

VS-	15	E	Т	Н	06	S	TRL	PbF
1	2	3	4	5	6	7	8	9

- HPP product suffix
- 2 Current rating (15 A)
- 3 E = Single diode
- T = TO-220, D<sup>2</sup>PAK
- H = Hyperfast rectifier
- Voltage rating (06 = 600 V)
- 7 • S = D<sup>2</sup>PAK
  - -1 = TO-262
- None = Tube (50 pieces)
  - TRL = Tape and reel (left oriented, for D<sup>2</sup>PAK package)
  - TRR = Tape and reel (right oriented, for D<sup>2</sup>PAK package)
- 9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95014</u>						
Part marking information	www.vishay.com/doc?95008					
Packaging information	www.vishay.com/doc?95032					

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