Vishay High Power Products

HEXFRED[®] Ultrafast Soft Recovery Diode, 4 A



- Ultrafast recovery
- Ultrasoft recovery
- Very low I_{RRM}
- Very low Q_{rr}
- Specified at operating conditions
- · Designed and qualified for industrial level

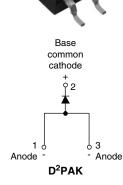
BENEFITS

- Reduced RFI and EMI
- · Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

DESCRIPTION

HFA04TB60S is a state of the art ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 V and 4 A continuous current, the HFA04TB60S is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I_{RBM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA04TB60S is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		600	V	
Maximum continuous forward current	١ _F	T _C = 100 °C	4		
Single pulse forward current	I _{FSM}		25	А	
Maximum repetitive forward current	I _{FRM}		16		
Maximum neuror discinction	P _D	T _C = 25 °C	25	W	
Maximum power dissipation		T _C = 100 °C	10		
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 150	°C	



PRODUCT SUMMARY				
V _R	600 V			
V _F at 4 A at 25 °C	1.8 V			
I _{F(AV)}	4 A			
t _{rr} (typical)	17 ns			
T _J (maximum)	150 °C			
Q _{rr} at 125 °C	40 nC			
dl _{(rec)M} /dt at 125 °C	280 A/μs			

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HFA04TB60S



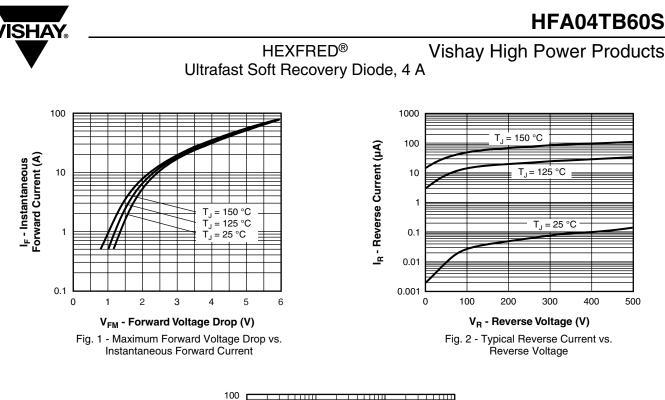
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ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		600	-	-	
	V _{FM}	I _F = 4.0 A	See fig. 1	-	1.5	1.8	V
Maximum forward voltage		I _F = 8.0 A		-	1.8	2.2	
		I _F = 4.0 A, T _J = 125 °C		-	1.4	1.7	
Maximum reverse		$V_R = V_R$ rated	See fig. 2	-	0.17	3.0	μΑ
leakage current	IRM	$T_J = 125 \ ^\circ C$, $V_R = 0.8 \ x \ V_R$ rated	See lig. 2	-	44	300	
Junction capacitance	CT	V _R = 200 V	See fig. 3	-	4.0	8.0	pF
Series inductance	LS	Measured lead to lead 5 mm from package body - 8.0		-	nH		

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t _{rr}	I _F = 1.0 A, dI _F /dt = 200 A/μs, V _R = 30 V		-	17	-	
Reverse recovery time See fig. 5, 6	t _{rr1}	T _J = 25 °C	I _F = 4.0 A dI _F /dt = 200 A/μs V _R = 200 V	-	28	42	ns
	t _{rr2}	T _J = 125 °C		-	38	57	
Peak recovery current	I _{RRM1}	T _J = 25 °C		-	2.9	5.2	A nC
	I _{RRM2}	T _J = 125 °C		-	3.7	6.7	
Reverse recovery charge See fig. 7	Q _{rr1}	T _J = 25 °C		-	40	60	
	Q _{rr2}	T _J = 125 °C		-	70	105	
Peak rate of fall of recovery current during t _b See fig. 8	dl _{(rec)M} /dt1	T _J = 25 °C		-	280	-	- A/μs
	dI _{(rec)M} /dt2	T _J = 125 °C		-	235	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Thermal resistance, junction to case	R _{thJC}		-	-	5.0	K/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	80	r./ vv
Waight			-	2.0	-	g
Weight			-	0.07	-	oz.
Marking device		Case style D ² PAK		HFA04	TB60S	•



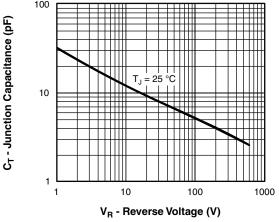


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

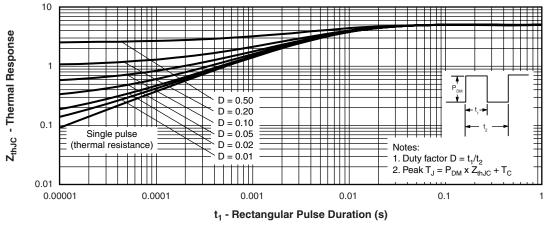


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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400

500

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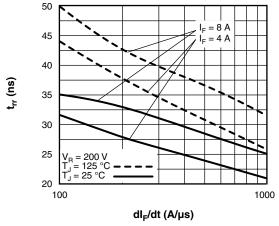
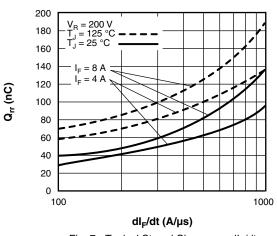


Fig. 5 - Typical Reverse Recovery Time vs. dl_F/dt



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Fig. 7 - Typical Stored Charge vs. dI_F/dt

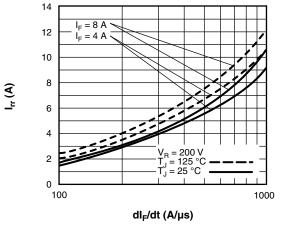


Fig. 6 - Typical Recovery Current vs. dl_F/dt

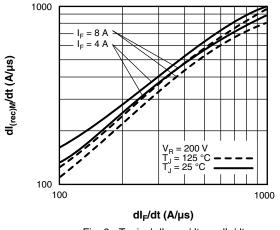


Fig. 8 - Typical dI_{(rec)M}/dt vs. dI_F/dt



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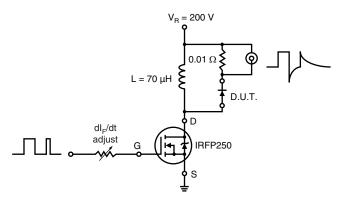


Fig. 9 - Reverse Recovery Parameter Test Circuit

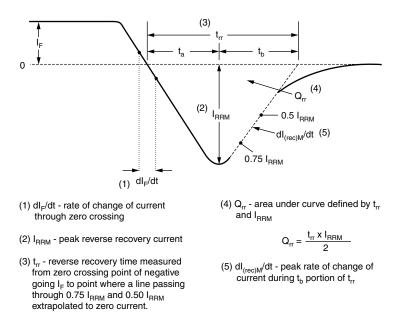


Fig. 10 - Reverse Recovery Waveform and Definitions

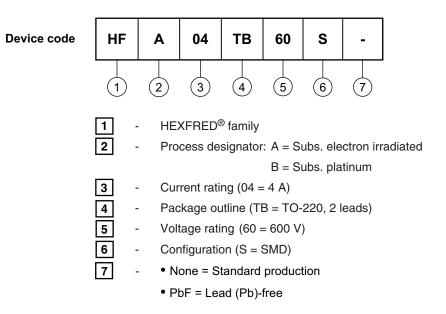
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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95046				
Part marking information	http://www.vishay.com/doc?95054			
Packaging information http://www.vishay.com/doc?95032				



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